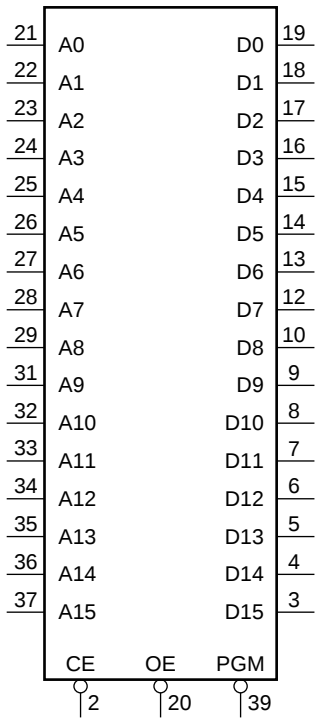
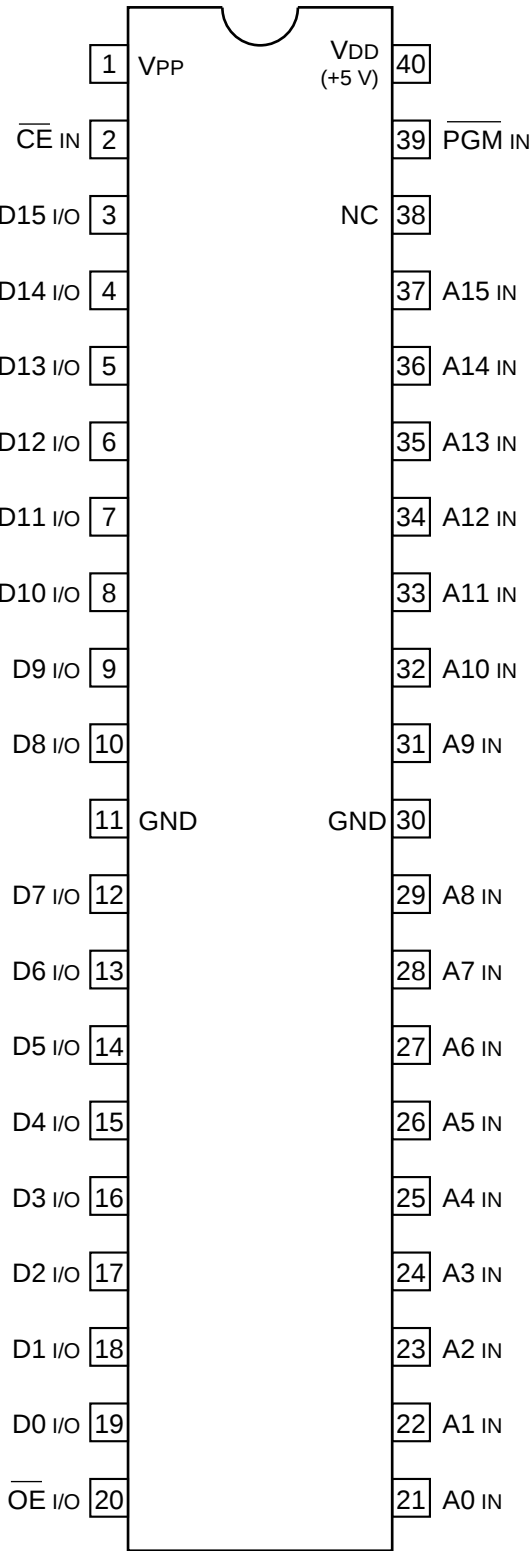

C-MOS 1M (64Kx16)-BIT ERASABLE PROM
— TOP VIEW —



A0 - A15 ; ADDRESS INPUT
CE ; CHIP ENABLE INPUT
D0 - D15 ; DATA INPUT/OUTPUT
OE ; OUTPUT ENABLE INPUT
PGM ; PROGRAM ENABLE INPUT
VPP ; PROGRAM VOLTAGE INPUT
(NORMAL : +5 V)
(PROGRAM : +13 V)