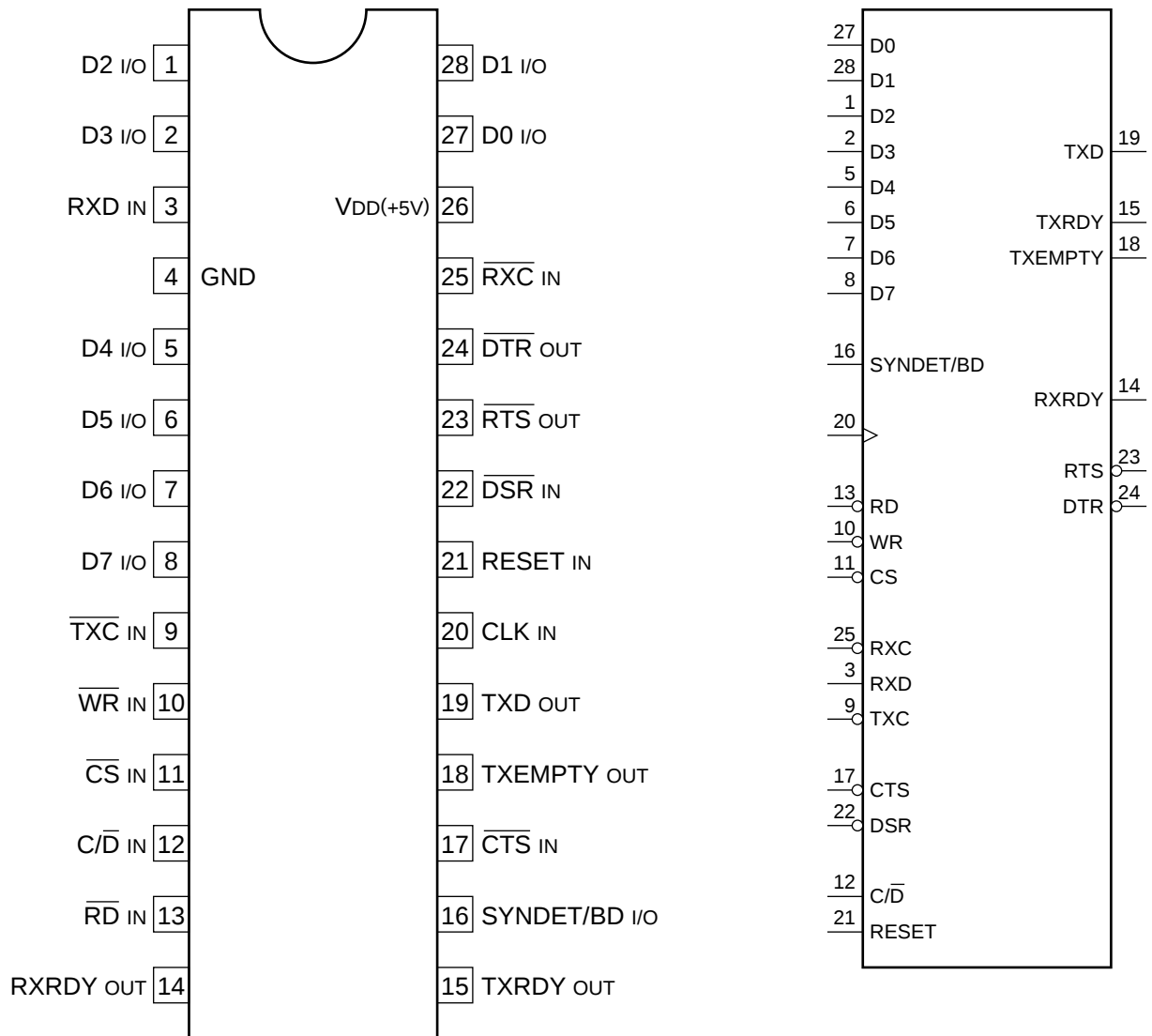

TMP82C51AM(1/2)
IL00

C-MOS 8-BIT MICRO PROCESSOR

-TOP VIEW-



INPUT

C/D ; DATA CONTROL
CLK ; SYSTEM CLOCK
CS ; CHIP SELECT
CTS ; CLEAR TO SEND
DSR ; DATA SET READY
RESET ; MASTER RESET
RD ; DATA READ
RXC ; RECEIVE CLOCK
RXD ; RECEIVE DATA
TXC ; TRANSMIT CLOCK
WR ; DATA WRITE

OUTPUT

DTR ; DATA TERMINAL READY
RTS ; REQUEST TO SEND
RXRDY ; RECEIVE READY
TXD ; TRANSMIT DATA
TXEMPTY ; TRANSMIT EMPTY
TXRDY ; TRANSMIT READY

INPUT/OUTPUT

D0-D7 ; DATA
SYNDET/BD ; SYNC/BREAK CHARACTER DETECT

