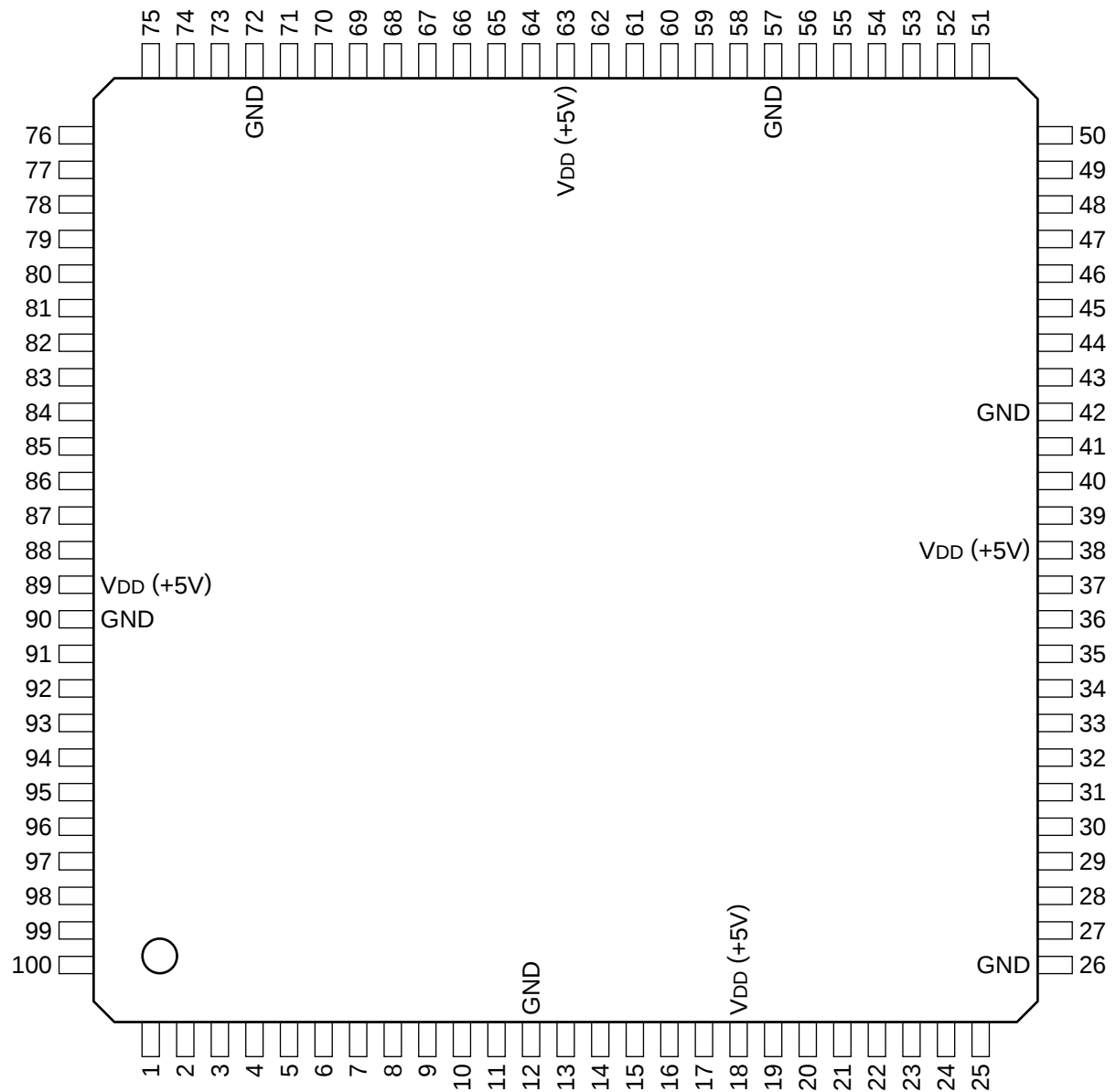

TMP68301AFR-16 (1/4)

IL08

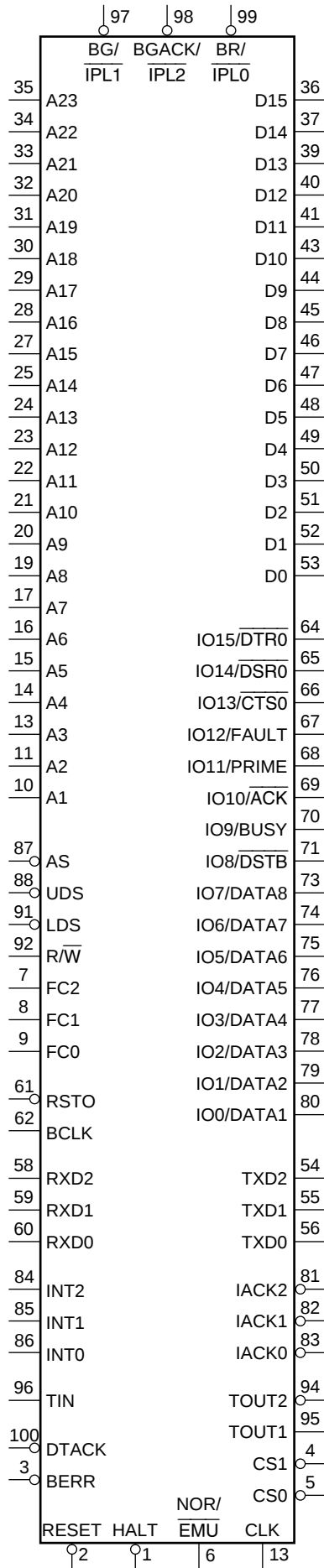
C-MOS 16-BIT MICRO COMPUTER

—TOP VIEW—



(V_{DD} = +5V)

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I/O	$\overline{\text{HALT}}$	26	—	GND	51	I/O	D2	76	I/O	IO4/DATA5
2	I/O	$\overline{\text{RESET}}$	27	I/O	A15	52	I/O	D1	77	I/O	IO3/DATA4
3	I/O	$\overline{\text{BERR}}$	28	I/O	A16	53	I/O	D0	78	I/O	IO2/DATA3
4	O	$\overline{\text{CS1}}$	29	I/O	A17	54	O	TXD2	79	I/O	IO1/DATA2
5	O	$\overline{\text{CS0}}$	30	I/O	A18	55	O	TXD1	80	I/O	IO0/DATA1
6	I	NOR/EMU	31	I/O	A19	56	O	TXD0	81	O	$\overline{\text{IACK2}}$
7	I/O	FC2	32	I/O	A20	57	—	GND	82	O	$\overline{\text{IACK1}}$
8	I/O	FC1	33	I/O	A21	58	I	RXD2	83	O	$\overline{\text{IACK0}}$
9	I/O	FC0	34	I/O	A22	59	I	RXD1	84	I	INT2
10	I/O	A1	35	I/O	A23	60	I	RXD0	85	I	INT1
11	I/O	A2	36	I/O	D15	61	I/O	$\overline{\text{RSTO}}$	86	I	INT0
12	—	GND	37	I/O	D14	62	I	BCLK	87	I/O	$\overline{\text{AS}}$
13	I/O	A3	38	—	V _{DD}	63	—	V _{DD}	88	I/O	$\overline{\text{UDS}}$
14	I/O	A4	39	I/O	D13	64	I/O	IO15/ $\overline{\text{DTR0}}$	89	—	V _{DD}
15	I/O	A5	40	I/O	D12	65	I/O	IO14/ $\overline{\text{DSR0}}$	90	—	GND
16	I/O	A6	41	I/O	D11	66	I/O	IO13/ $\overline{\text{CTS0}}$	91	I/O	$\overline{\text{LDS}}$
17	I/O	A7	42	—	GND	67	I/O	IO12/FAULT	92	I/O	R/ $\overline{\text{W}}$
18	—	V _{DD}	43	I/O	D10	68	I/O	IO11/PRIME	93	I	CLK
19	I/O	A8	44	I/O	D9	69	I/O	IO10/ACK	94	O	TOUT2
20	I/O	A9	45	I/O	D8	70	I/O	IO9/ $\overline{\text{BUSY}}$	95	O	TOUT1
21	I/O	A10	46	I/O	D7	71	I/O	IO8/ $\overline{\text{DSTB}}$	96	I	TIN
22	I/O	A11	47	I/O	D6	72	—	GND	97	O	$\overline{\text{BG/IPL1}}$
23	I/O	A12	48	I/O	D5	73	I/O	IO7/DATA8	98	I/O	$\overline{\text{BGACK/IPL2}}$
24	I/O	A13	49	I/O	D4	74	I/O	IO6/DATA7	99	I/O	$\overline{\text{BR/IPL0}}$
25	I/O	A14	50	I/O	D3	75	I/O	IO5/DATA6	100	I/O	$\overline{\text{DTACK}}$

**INPUT**

BCLK ; BAUD RATE CLOCK
 CLK ; CLOCK
 INT0 - INT2 ; INTERRUPT REQUESTS
 NOR/EMU ; MODE SELECT
 RXD0 - RXD2 ; RECEIVE DATA
 TIN ; TIMER INPUT

OUTPUT

BG (/IPL1) ; BUS GROUND
 CS0, CS1 ; CHIP SELECTS
 IACK0 - IACK2 ; INTERRUPT ACKNOWLEDGE
 TOUT1, TOUT2 ; TIMER OUTPUTS
 TXD0 - TXD2 ; TRANSFER DATA

INPUT/OUTPUT

A1 - A23 ; ADDRESS BUS
 ACK ; I/O PORT
 AS ; ADDRESS STROBE
 BERR ; BUS ERROR
 BGACK (/IPL2) ; BUS GROUND ACKNOWLEDGE
 BR (/IPL0) ; BUS REQUEST
 BUSY ; I/O PORT
 CTS0, DSR0, DTR0, DSTB ; I/O PORTS
 D0 - D15 ; DATA BUS
 DATA1 - DATA8 ; I/O PORTS
 DTACK ; DATA TRANSFER ACKNOWLEDGE
 FAULT ; I/O PORT
 FC0 - FC2 ; FUNCTION CORDS
 HALT ; HALT
 IO0 - IO15 ; I/O PORT
 LDS ; LOWER DATA STROBE
 PRIME ; I/O PORT
 RESET ; RESET
 RSTO ; TRANSFER REQUEST
 R/W ; READ/WRITE
 UDS ; UPPER DATA STROBE

