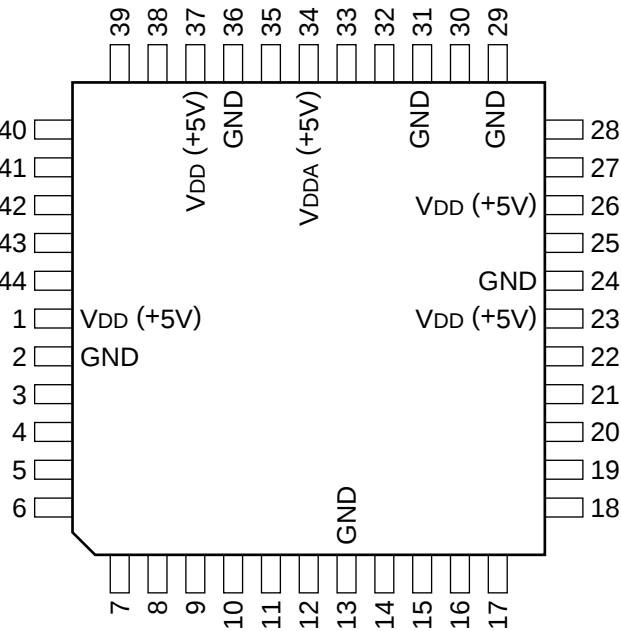

C-MOS DIGITAL VIDEO ENCODER
—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	V _{DD}	12	I/O	D4	23	—	V _{DD}	34	—	V _{DDA}
2	—	GND	13	—	GND	24	—	GND	35	O	CHROMA
3	I	PD0	14	I/O	D3	25	I	PXCK	36	—	GND
4	I	SA1	15	I/O	D2	26	—	V _{DD}	37	—	V _{DD}
5	I	SA0, ADR	16	I/O	D1	27	I/O	VREF	38	I	PD7
6	I/O	SDA, R/W	17	I/O	D0	28	—	RREF	39	I	PD6
7	I	SCL, $\overline{CS}$	18	O	$\overline{HSYNC}$, TDO	29	—	GND	40	I	PD5
8	I	$\overline{SER}$	19	I/O	$\overline{VSYNC}$, T/B, TDI	30	O	COMPOSITE	41	I	PD4
9	I/O	D7	20	O	$\overline{CBSEL}$, PDC, TMS	31	—	GND	42	I	PD3
10	I/O	D6	21	I/O	SELC, TCK	32	O	LUMA	43	I	PD2
11	I/O	D5	22	I	$\overline{RESET}$	33	—	CBYP	44	I	PD1

INPUT

ADR	; TABLE ADDRESS INPUT IN PARALLEL INTERFACE
CBYP	; REFERENCE BYPASS CAPACITOR
CS	; CHIP SELECT
PD7-PD0	; PIXEL DATA
PXCK	; PIXEL CLOCK
RESET	; MASTER RESET
RREF	; CURRENT SETTING RESISTOR
R/W	; READ/WRITE CONTROL
SA1, SA0	; SERIAL/PARALLEL PORT SELECT
SCL	; SERIAL CLOCK
SELC	; LUMA/CHROMA MUX CONTROL
SER	; SERIAL/PARALLEL PORT SELECT
TCK	; JTAG SCAN CLOCK
TDI	; JTAG DATA
VREF	; VOLTAGE REFERENCE

OUTPUT

CBSEL	; CB ELEMENT OF THE Cb-Y, Cr-Y CCIR-656 DATA SEQUENCE SELECT
CHROMA	; CHROMINANCE-ONLY VIDEO
COMPOSITE	; COMPOSITE NTSC/PAL VIDEO
HSYNC	; HORIZONTAL SYNC
LUMA	; LUMINANCE-ONLY VIDEO
PDC	; PIXEL DATA PHASE
SDA	; SERIAL DATA
T/B	; ODD/EVEN FIELD ID
TDO	; JTAG DATA OUTPUT PORT
TMS	; JTAG SCAN SELECT PORT

INPUT/OUTPUT

D7 - D0	; DATA I/O, GENERAL PURPOSE I/O, CHROMA INPUT PORT
VSYNC	; VERTICAL SYNC

