



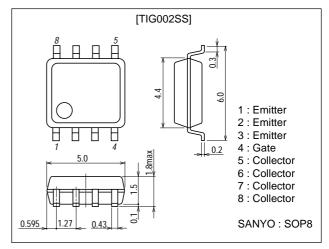
# **Light-Controlling Strobe Applications**

## **Features**

- · Low-saturation voltage.
- · 4V drive.
- · Enhansment type.

## **Package Dimensions**

unit : mm 2203



## **Specifications**

## Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Emitter Voltage	VCES		400	V
Gate-to-Emitter Voltage (DC)	VGES		±6	V
Gate-to-Emitter Voltage (Pulse)	VGES		±8	V
Collector Current (Pulse)	ICP	PW≤500μs, duty cycle≤0.5%	150	Α
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-40 to +150	°C

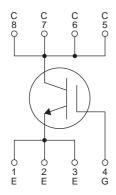
#### Electrical Characteristics at Ta=25°C

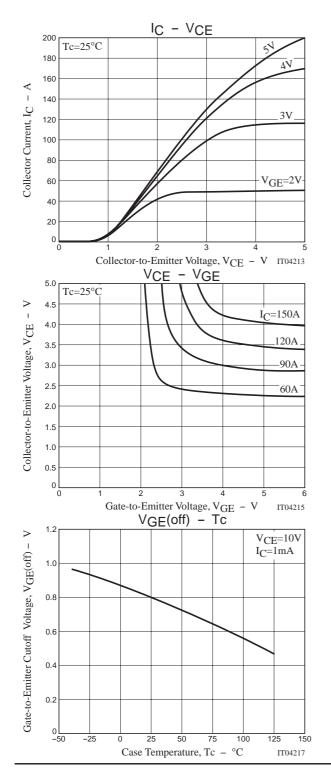
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offile
Collector-to-Emitter Breakdown Voltage	V(BR)CES	IC=5mA, VGE=0	400			V
Collector-to-Emitter Cutoff Current	ICES	V <sub>CE</sub> =320V, V <sub>GE</sub> =0			10	μΑ
Gate-to-Emitter Leakage Current	IGES	V <sub>GE</sub> =±6V, V <sub>CE</sub> =0			±100	nA
Gate-to-Emitter Cutoff Voltage	V <sub>GE</sub> (off)	V <sub>CE</sub> =10V, I <sub>C</sub> =1mA	0.5		1.2	٧
Collector-to-Emitter Saturation Voltage	V <sub>CE</sub> (sat)1	I <sub>C</sub> =150A, V <sub>GE</sub> =4V		4.2	5.5	V
	V <sub>CE</sub> (sat)2	I <sub>C</sub> =60A, V <sub>GE</sub> =2.5V		2.4	3.4	V
Input Capacitance	Cies	V <sub>CE</sub> =10V, f=1MHz		3300		pF
Output Capacitance	Coes	V <sub>CE</sub> =10V, f=1MHz		75		pF
Reverse Transfer Capacitance	Cres	V <sub>CE</sub> =10V, f=1MHz		40		pF

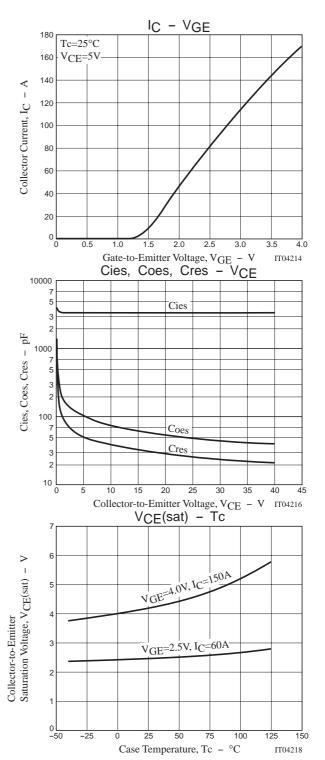
(Note) Handling the TIG002SS requires sufficient care to be taken because it has no protection diode between gate and emitter.

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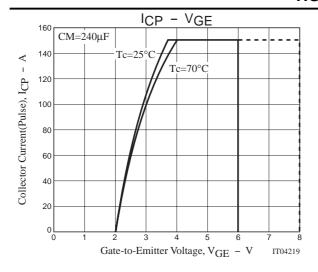
### **Electrical Connection**

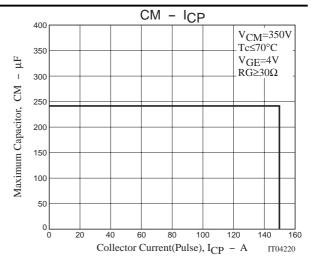






### TIG002SS





The gate series resistance  $R_G$  must be  $30\Omega$  or more to protect the device when it is turned off.

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