TF219..B

FAST SWITCHING THYRISTOR

APPLICATIONS

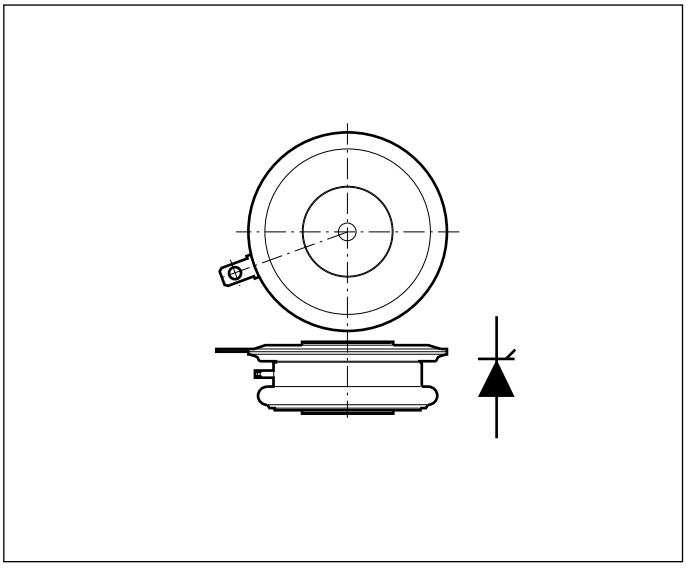
- High Power Inverters And Choppers.
- UPS.
- Railway Traction.
- Induction Heating.
- AC Motor Drives.
- Cycloconverters.

KEY PARAMETERS

V_{DRM}	2000V
$I_{T(RMS)}$	190A
I_{TSM}	1200A
dV/dt	200V/μs
dI/dt	500A/μs
t_q	40μs

FEATURES

- Double Side Cooling.
- High Surge Capability.
- High Voltage.



Outline type code: MU86. Turn to page 12 for further information.

VOLTAGE RATINGS

Type Number	Repetitive Peak Voltages V_{DRM} V_{RRM}	Conditions
TF219 20B TF219 18B TF219 16B TF219 14B	2000 1800 1600 1400	$V_{RSM} = V_{RRM} + 100V$ $I_{DRM} = I_{RRM} = 15mA$ at V_{RRM} or V_{DRM} & T_{vj}

Lower voltage grades available.

CURRENT RATINGS

Symbol	Parameter	Conditions	Max.	Units
$I_{T(AV)}$	Mean on-state current	Half sinewave, 50Hz, T_{case} = 80°C	120	A
$I_{T(RMS)}$	RMS value	Half sinewave, 50Hz, T_{case} = 80°C	190	A

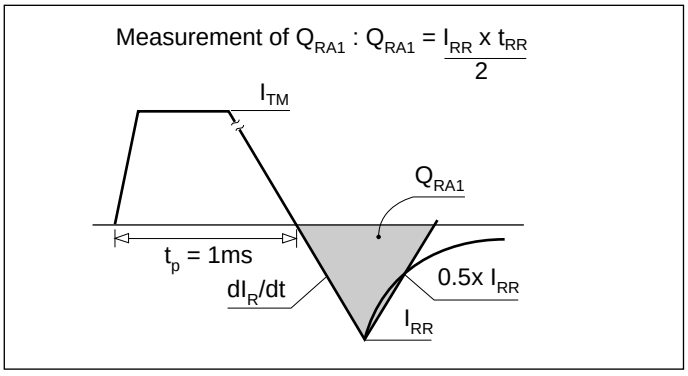
SURGE RATINGS

Symbol	Parameter	Conditions	Max.	Units
I_{TSM}	Surge (non-repetitive) on-state current	10ms half sine; V_R = 0% V_{RRM}, T_j = 125˚C	1.2	kA
I^2t	I^2t for fusing	10ms half sine; V_R = 0% V_{RRM}, T_j = 125˚C	7.2	A^2s

THERMAL AND MECHANICAL DATA

Symbol	Parameter	Conditions		Min.	Max.	Units
$R_{th(j-c)}$	Thermal resistance - junction to case	Double side cooled	dc	-	0.1	°C/W
		Single side cooled	Anode dc	-	0.19	°C/W
			Cathode dc	-	0.24	°C/W
$R_{th(c-h)}$	Thermal resistance - case to heatsink	Clamping force 3.5kN with mounting compound	Double side	-	0.02	°C/W
			Single side	-	0.04	°C/W
T_{vj}	Virtual junction temperature	On-state (conducting)		-	125	°C
		Reverse (blocking)		-	125	°C
T_{stg}	Storage temperature range			-40	150	°C
-	Clamping force			3.3	3.6	kN

MEASUREMENT OF RECOVERED CHARGE - Q_{RA1}



DYNAMIC CHARACTERISTICS

Symbol	Parameter	Conditions		Min.	Max.	Units
V_{TM}	Maximum on-state voltage	At 150A peak, T_{case} = 25°C		-	2.75	V
I_{RRM}/I_{DRM}	Peak reverse and off-state current	At V_{RRM}/V_{DRM}, T_{case} = 125°C		-	15	mA
dV/dt	Maximum linear rate of rise of off-state voltage	Linear to 60% V_{DRM} T_j = 125°C, Gate open circuit		-	200	V/µs
dI/dt	Rate of rise of on-state current	Gate source 20V, 20Ω $t_r \leq 0.5\mu s$, T_j = 125°C	Repetitive 50Hz	-	500	A/µs
			Non-repetitive	-	800	A/µs
$V_{T(TO)}$	Threshold voltage	At T_{vj} = 125°C		-	2.0	V
r_T	On-state slope resistance	At T_{vj} = 125°C		-	5.0	mΩ
t_{gd}	Delay time	T_j = 25°C, I_T = 100A, V_D = 50V, I_G = 1A, dI/dt = 50A/µs, dI_G/dt = 1A/µs		-	3*	µs
$t_{(ON)TOT}$	Total turn-on time			-	1.5*	µs
I_H	Holding current	T_j = 25°C, I_{TM} = 1A, V_D = 12V		-	60	mA
t_q	Turn-off time	T_j = 125°C, I_T = 100A, V_R = 50V, dV/dt = 200V/µs (Linear to 60% V_{DRM}), dI_R/dt = 30A/µs, Gate open circuit	t_q code: B	-	40	µs

**Typical value.*

GATE TRIGGER CHARACTERISTICS AND RATINGS

Symbol	Parameter	Conditions	Typ.	Max.	Units
V_{GT}	Gate trigger voltage	V_{DRM} = 12V, T_{case} = 25°C, R_L = 6Ω	-	3.0	V
I_{GT}	Gate trigger current	V_{DRM} = 12V, T_{case} = 25°C, R_L = 6Ω	-	200	mA
V_{GD}	Gate non-trigger voltage	At V_{DRM} T_{case} = 125°C, R_L = 1kΩ	-	0.2	V
V_{RGM}	Peak reverse gate voltage		-	5.0	V
I_{FGM}	Peak forward gate current	Anode positive with respect to cathode	-	4	A
P_{GM}	Peak gate power		-	16	W
$P_{G(AV)}$	Mean gate power		-	3	W

CURVES

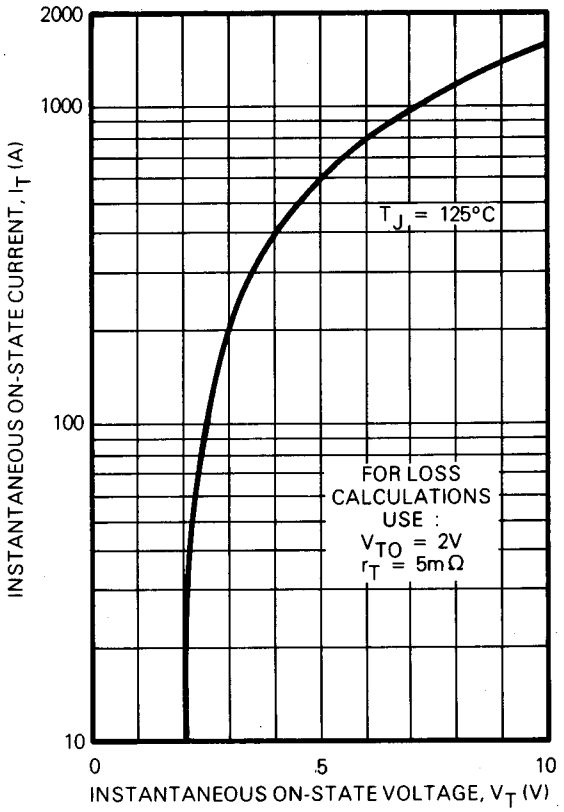


FIG. 1 MAXIMUM (LIMIT) ON-STATE CHARACTERISTICS

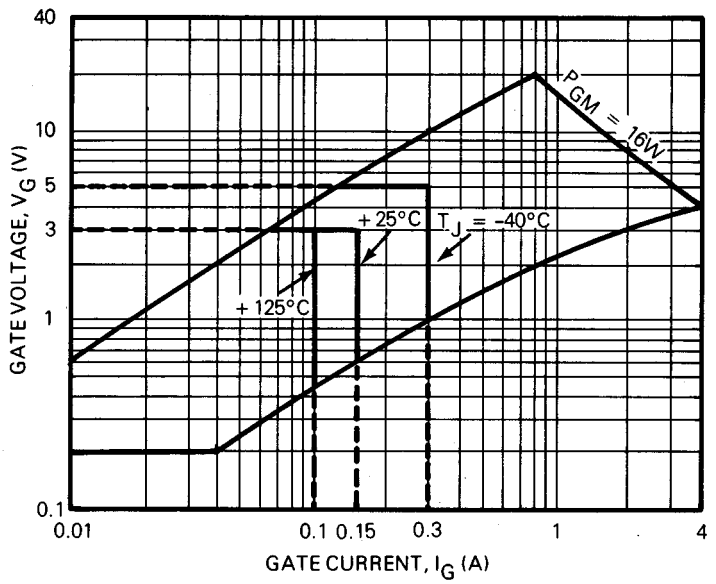


FIG. 2 GATE TRIGGER CHARACTERISTICS

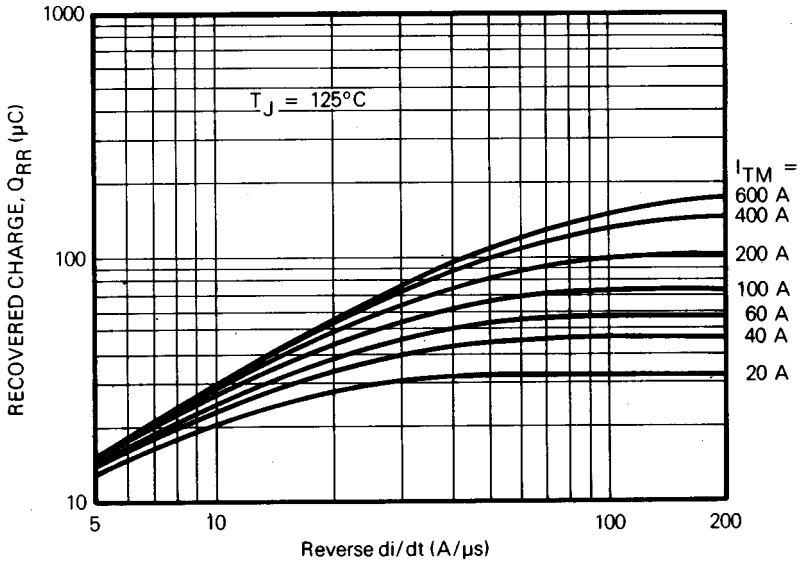


FIG. 3 TYPICAL RECOVERED CHARGE
(FOR A DEVICE RATED Vdrm = 1000V, tq = 40µs)

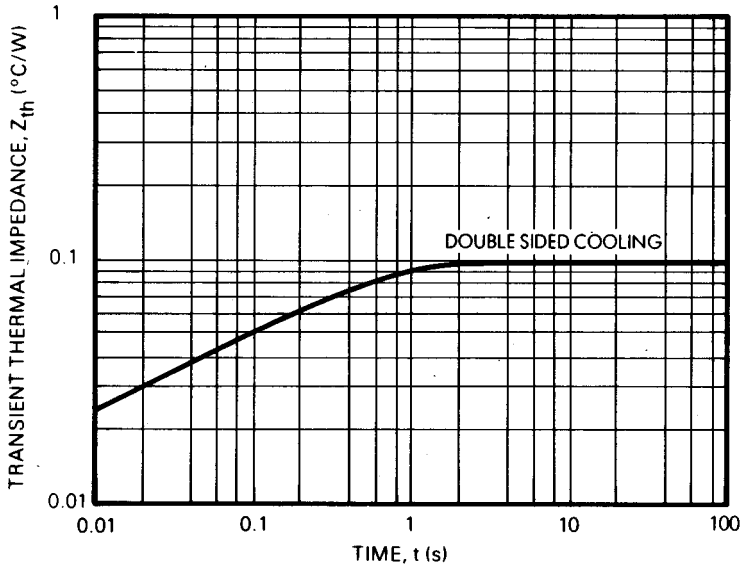


FIG. 4 TRANSIENT THERMAL IMPEDANCE

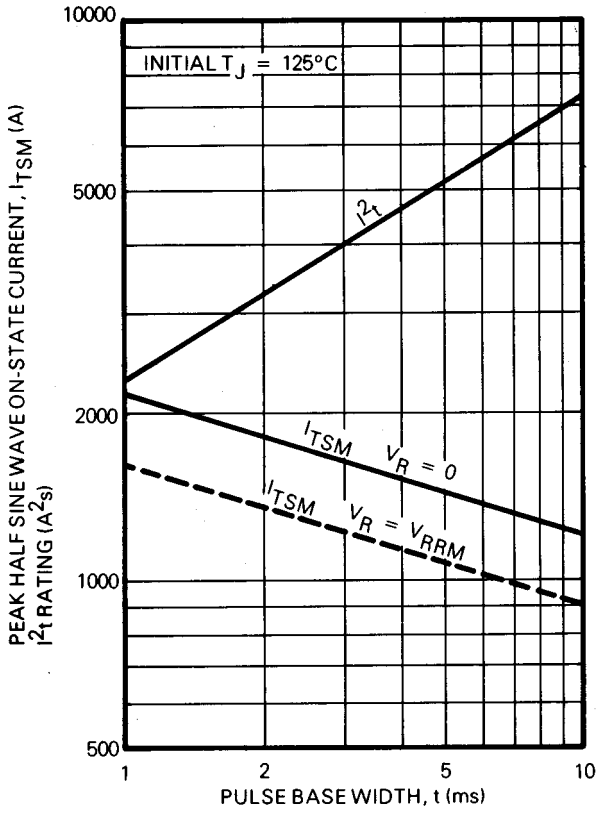


FIG. 5 NON-REPETITIVE SUB-CYCLE SURGE ON-STATE CURRENT AND I2t RATING

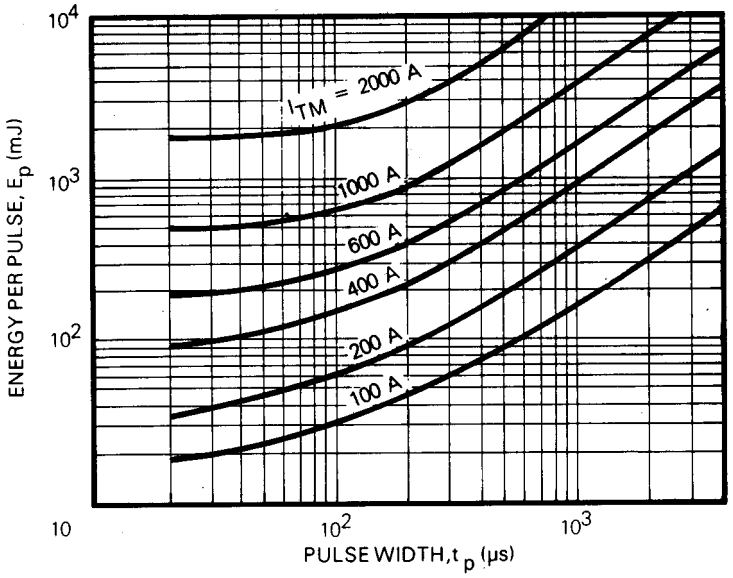
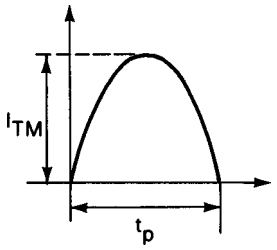


FIG. 6 ENERGY PER PULSE FOR SINUSOIDAL PULSES

NOTES:
1. $V_D \leq 600V$.
2. $V_R \leq 10V$.
3. R.C Snubber, C = 0.22µF, R = 4.7Ω



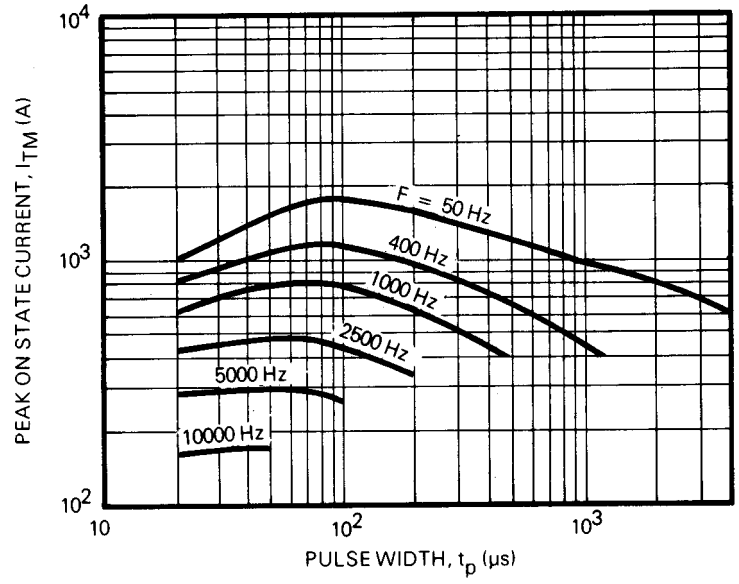
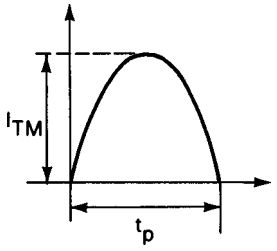


FIG. 7 MAXIMUM ALLOWABLE PEAK ON-STATE CURRENT vs PULSE WIDTH FOR Tc = 65°C

NOTES:
1. $V_D \leq 600V$.
2. $V_R \leq 10V$.
3. R.C Snubber, C = 0.22µF, R = 4.7Ω



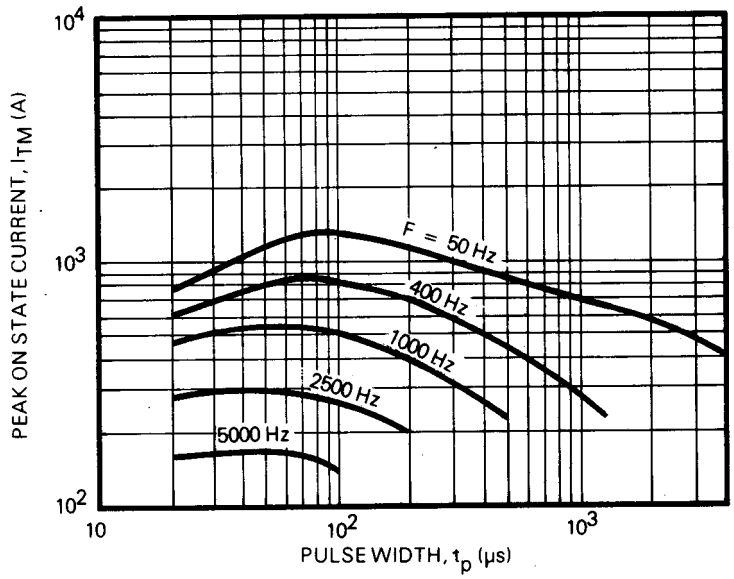
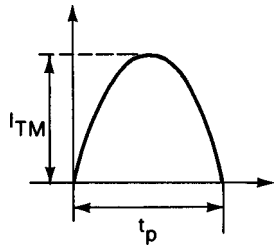


FIG. 8 MAXIMUM ALLOWABLE PEAK ON-STATE CURRENT vs PULSE WIDTH FOR Tc = 90°C

NOTES:
1. $V_D \leq 600V$.
2. $V_R \leq 10V$.
3. R.C Snubber, C = 0.22µF, R = 4.7Ω



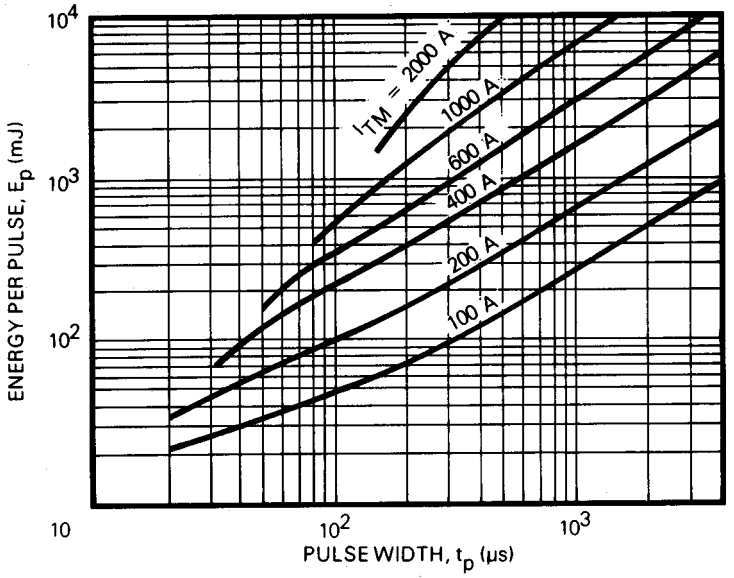
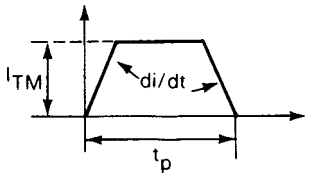


FIG. 9 ENERGY PER PULSE FOR TRAPEZOIDAL PULSES

NOTES:
1. dI/dt = 25A/µs
2. $V_D \leq 600V$.
3. $V_R \leq 10V$.
4. R.C Snubber, C = 0.22µF, R = 4.7Ω



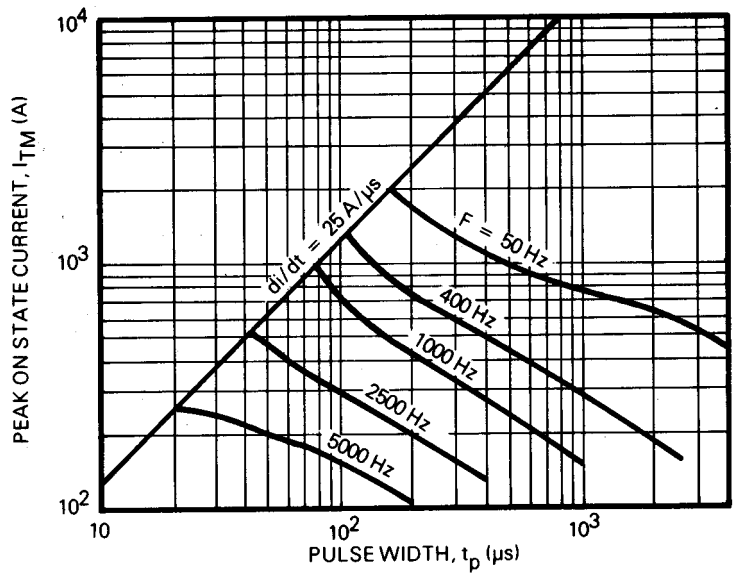
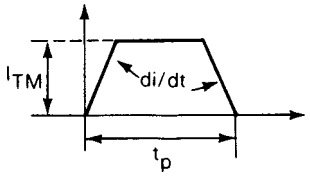


FIG. 10 MAXIMUM ALLOWABLE PEAK ON-STATE CURRENT vs PULSE WIDTH FOR Tc = 65°C

NOTES:
1. dI/dt = 25A/µs
2. $V_D \leq 600V$.
3. $V_R \leq 10V$.
4. R.C Snubber, C = 0.22µF, R = 4.7Ω



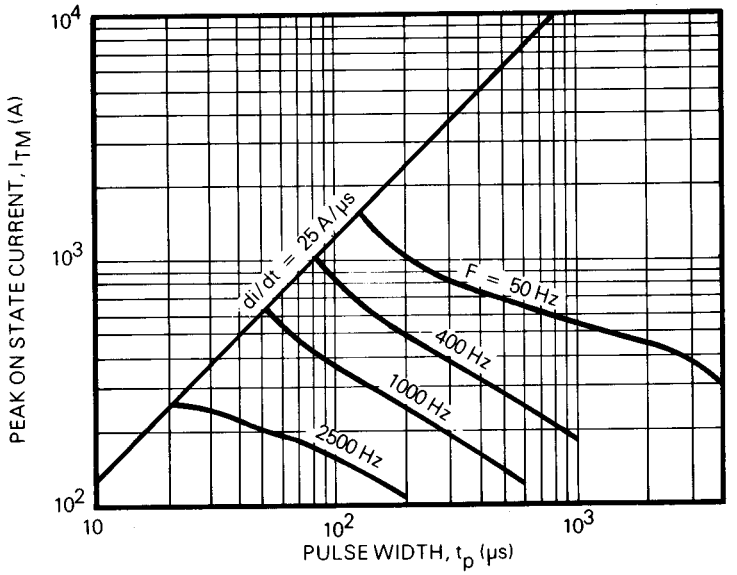
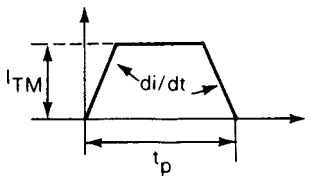


FIG. 11 MAXIMUM ALLOWABLE PEAK ON-STATE CURRENT vs PULSE WIDTH FOR Tc = 90°C

NOTES:
1. dI/dt = 25A/µs
2. $V_D \leq 600V$.
3. $V_R \leq 10V$.
4. R.C Snubber, C = 0.22µF, R = 4.7Ω



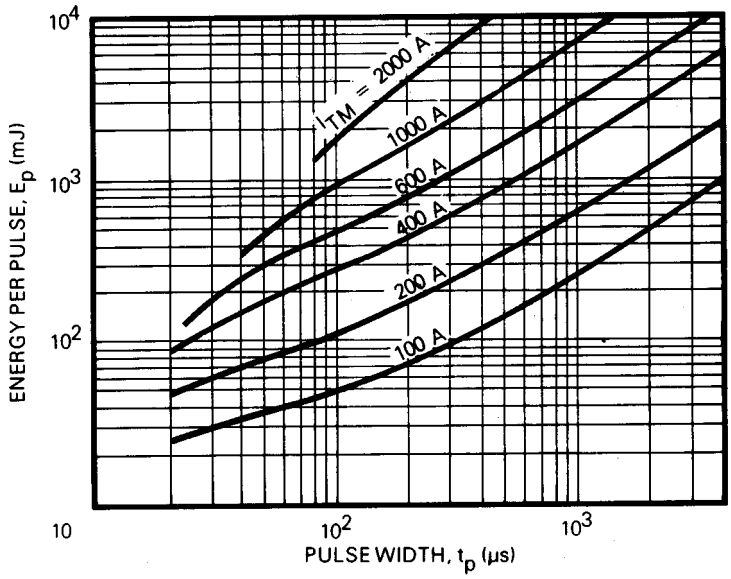
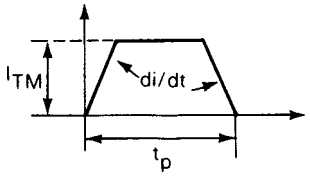


FIG. 12 ENERGY PER PULSE FOR TRAPEZOIDAL PULSES

NOTES:
1. dI/dt = 50A/µs
2. $V_D \leq 600V$.
3. $V_R \leq 10V$.
4. R.C Snubber, C = 0.22µF, R = 4.7Ω



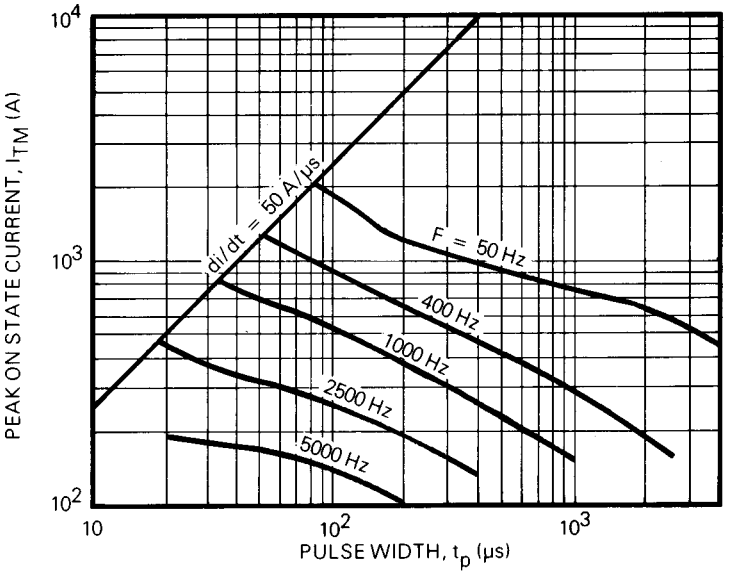
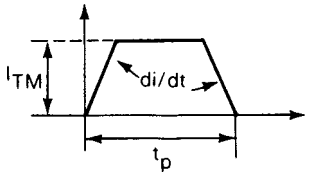


FIG. 13 MAXIMUM ALLOWABLE PEAK ON-STATE CURRENT vs PULSE WIDTH FOR Tc = 65°C

NOTES:
1. dI/dt = 50A/µs
2. $V_D \leq 600V$.
3. $V_R \leq 10V$.
4. R.C Snubber, C = 0.22µF, R = 4.7Ω



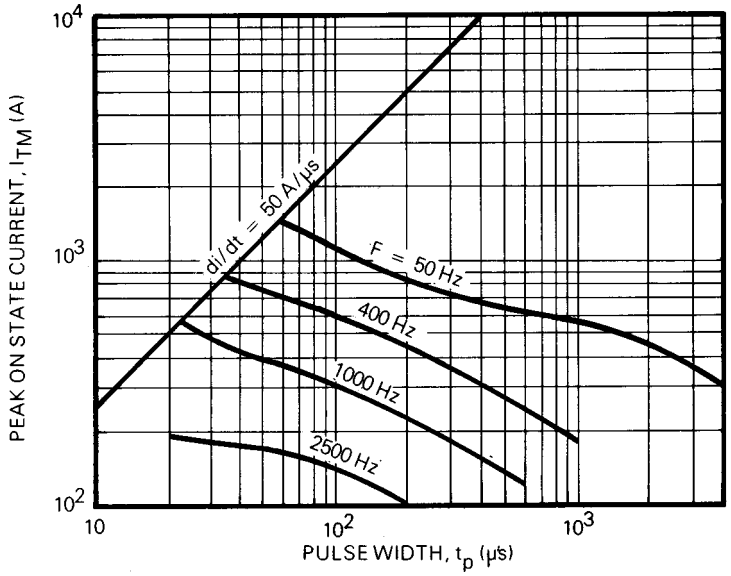
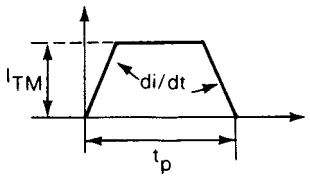


FIG. 14 MAXIMUM ALLOWABLE PEAK ON-STATE CURRENT vs PULSE WIDTH FOR Tc = 90˚C

NOTES:
1. dI/dt = 50A/μs
2. $V_D \leq 600V$.
3. $V_R \leq 10V$.
4. R.C Snubber, C = 0.22μF, R = 4.7Ω



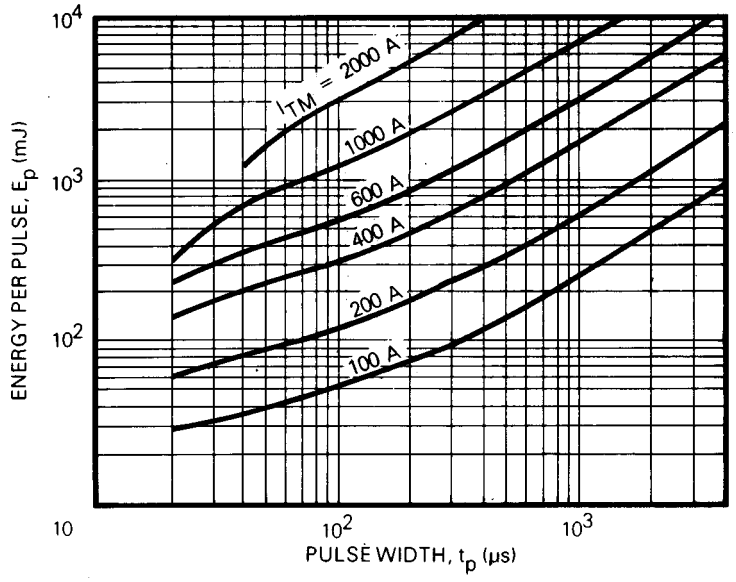
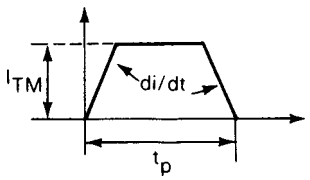


FIG. 15 ENERGY PER PULSE FOR TRAPEZOIDAL PULSES

NOTES:
1. dI/dt = 100A/μs
2. $V_D \leq 600V$.
3. $V_R \leq 10V$.
4. R.C Snubber, C = 0.22μF, R = 4.7Ω



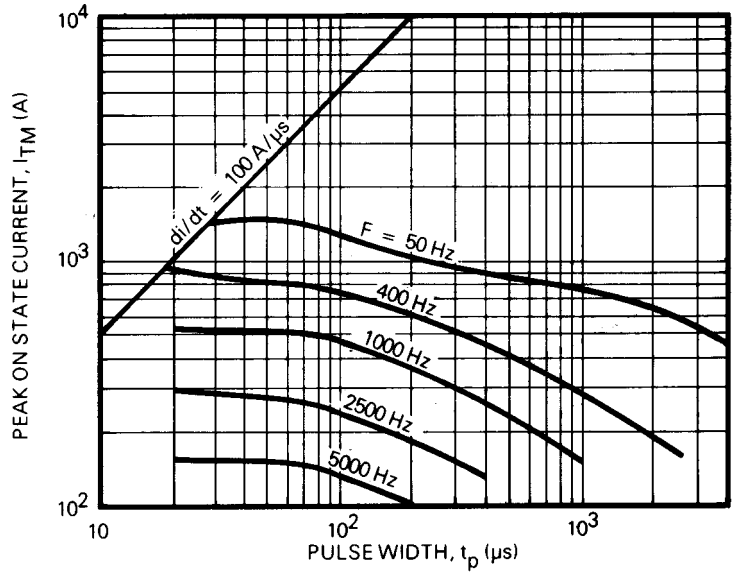
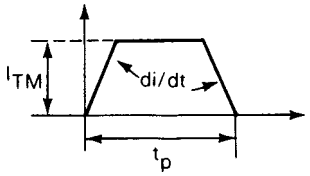


FIG. 16 MAXIMUM ALLOWABLE PEAK ON-STATE CURRENT vs PULSE WIDTH FOR Tc = 65˚C

NOTES:
1. dI/dt = 100A/µs
2. $V_D \leq 600V$.
3. $V_R \leq 10V$.
4. R.C Snubber, C = 0.22µF, R = 4.7Ω



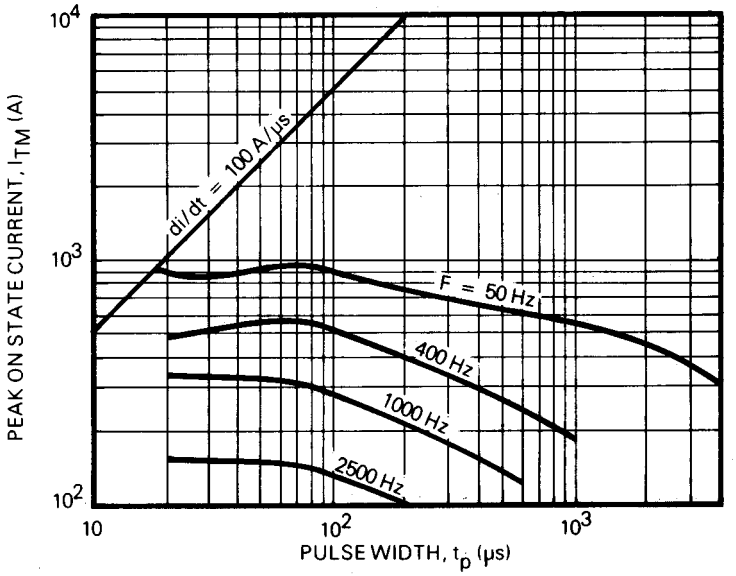
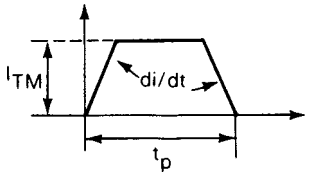


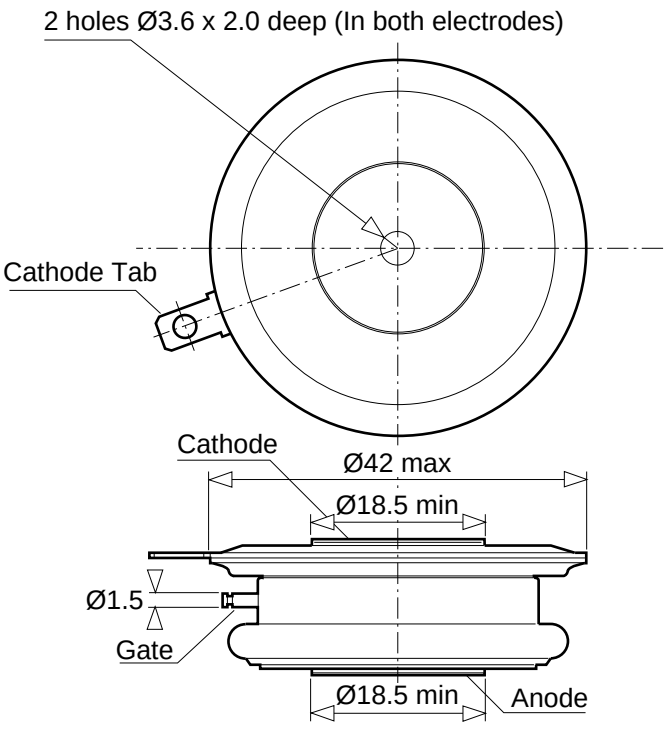
FIG. 17 MAXIMUM ALLOWABLE PEAK ON-STATE CURRENT vs PULSE WIDTH FOR Tc = 90˚C

NOTES:
1. dI/dt = 100A/µs
2. $V_D \leq 600V$.
3. $V_R \leq 10V$.
4. R.C Snubber, C = 0.22µF, R = 4.7Ω



PACKAGE DETAILS - MU86

For further package information, please contact your local Customer Service Centre. All dimensions in mm, unless stated otherwise. DO NOT SCALE.



Weight: 50g

GEC PLESSEY
SEMICONDUCTORS

HEADQUARTERS OPERATIONS

GEC PLESSEY SEMICONDUCTORS
Cheney Manor, Swindon,
Wiltshire, SN2 2QW, United Kingdom.
Tel: + 44 (0)1793 518000
Fax: + 44 (0)1793 518411

GEC PLESSEY SEMICONDUCTORS
P.O. Box 660017
1500 Green Hills Road,
Scotts Valley, California 95067-0017,
United States of America.
Tel: + 1 (408) 438 2900
Fax: + 1 (408) 438 5576

POWER PRODUCT CUSTOMER SERVICE CENTRES

- **FRANCE.** 2 rue Henri-Bergson, 92665 Asnieres Cedex.
  Tel: + 33 1 40 80 54 00. Fax: + 33 1 40 80 55 87.
- **GERMANY**. Ungererstrasse 129, 80505 München.
  Tel: + 49 (0)89 36 09 060. Fax: + 49 (0)89 36 09 06 55.
- **NORTH AMERICA.** Two Dedham Place, Suite 125, 3 Allied Drive, Dedham. MA 02026.
  Tel: + 1 617 251 0126. Fax: + 1 617 251 0106.
- **UNITED KINGDOM.** Doddington Road, Lincoln. LN6 3LF.
  Tel: + 44 (0)1522 500500. Fax: + 44 (0)1522 500550.

These are supported by Agents and Distributors in major countries world-wide.

 Publication No. DS4271-2 Issue No. 2.2 October 1995

TECHNICAL DOCUMENTATION - NOT FOR RESALE. PRINTED IN UNITED KINGDOM.