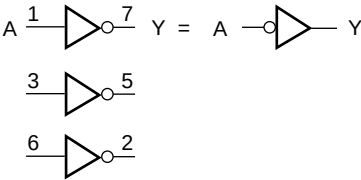
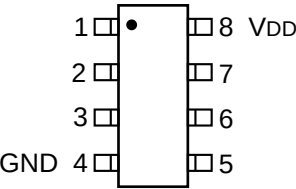


C-MOS HEX INVERTERS

—TOP VIEW—



$Y = \overline{A}$

A	Y
0	1
1	0

0 : LOW LEVEL
1 : HIGH LEVEL