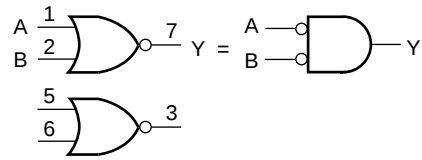
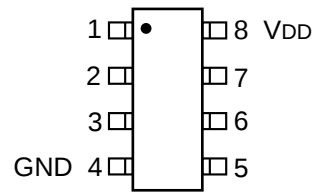


C-MOS DUAL 2-INPUT NOR GATE

—TOP VIEW—



$$Y = A \cdot B = \overline{\overline{A} + \overline{B}}$$

A	B	Y
0	0	1
0	1	0
1	0	0
1	1	0

0 : LOW LEVEL

1 : HIGH LEVEL