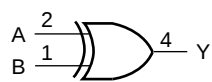
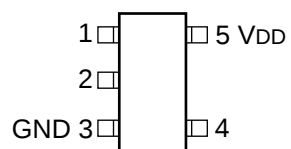


C-MOS 2-INPUT EXCLUSIVE OR GATE

—TOP VIEW—



$$Y = \bar{A} \cdot B + A \cdot \bar{B}$$

A	B	Y
0	0	0
0	1	1
1	0	1
1	1	0

0 : LOW LEVEL

1 : HIGH LEVEL