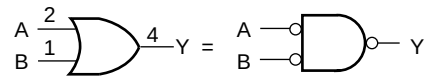
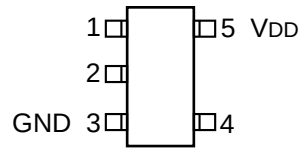


C-MOS 2-INPUT OR GATE

—TOP VIEW—



$$Y = A + B = \overline{\overline{A} \cdot \overline{B}}$$

A	B	Y
0	0	0
0	1	1
1	0	1
1	1	1

0 : LOW LEVEL

1 : HIGH LEVEL