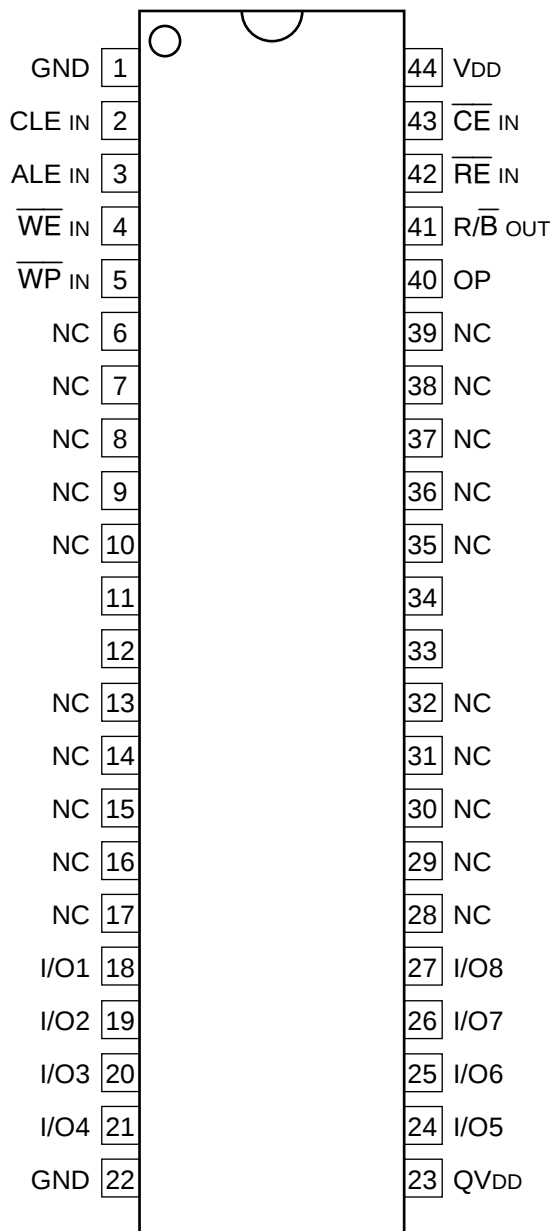


64 MB (8 MB × 8)-BIT NAND EEPROM

—TOP VIEW—

**INPUTS**

ALE	: ADDRESS LATCH ENABLE
$\overline{CE}$	: CHIP ENABLE
CLE	: COMMAND LATCH ENABLE
OP	: OPTION PIN
$\overline{RE}$	: READ ENABLE
$\overline{WE}$	: WRITE ENABLE
$\overline{WP}$	: WRITE PROTECT

OUTPUT

R/ $\overline{B}$	: READY/ $\overline{BUSY}$
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INPUT/OUTPUT

I/O1 - I/O8 : ADDRESS DATA COMMAND

OTHER

NC : NO CONNECTION