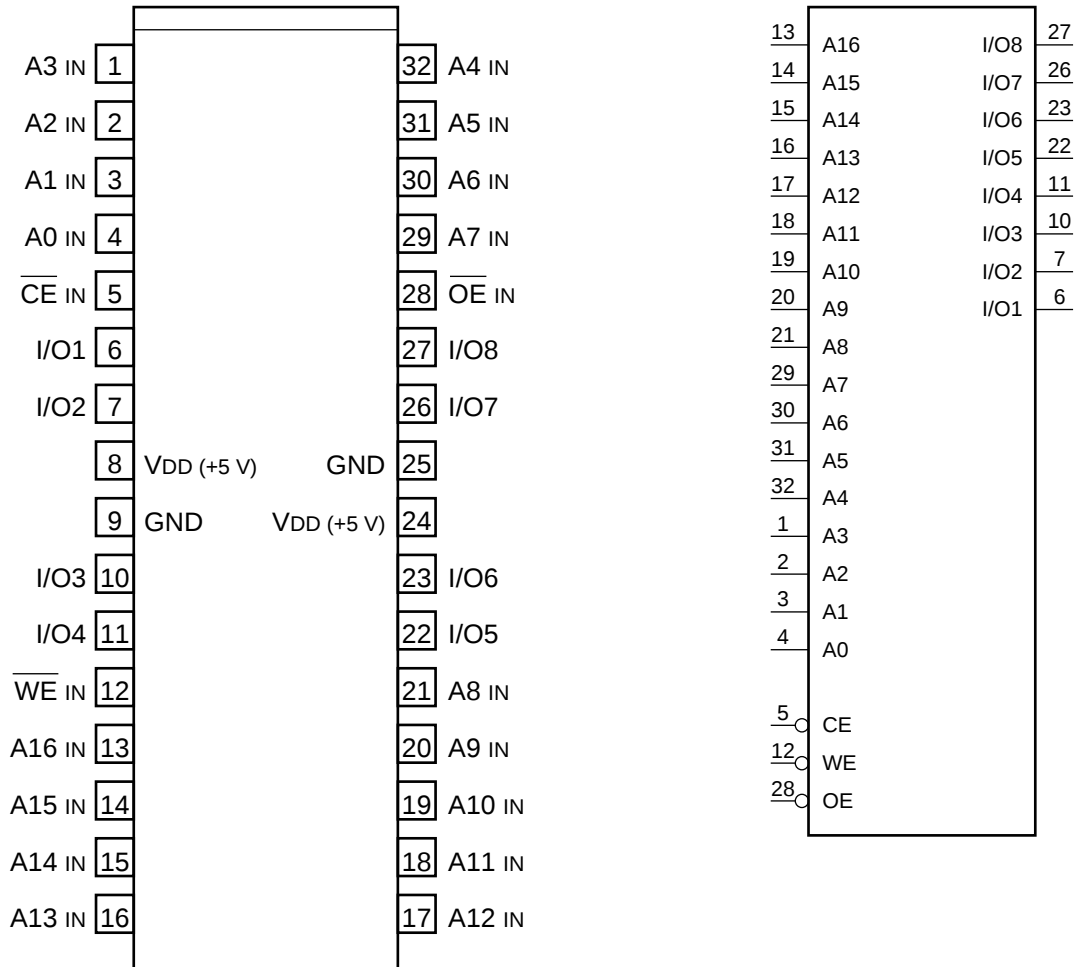


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# C-MOS 1M (131,072x8)-BIT STATIC RAM

## - TOP VIEW -



### INPUT

A0 - A16 ; ADDRESS

 $\overline{\text{CE}}$  ; CHIP ENABLE $\overline{\text{OE}}$  ; OUTPUT BUFFER CONTROL $\overline{\text{WE}}$  ; WRITE ENABLE

### INPUT/OUTPUT

I/O1 - I/O8 ; DATA

| ACTIVE MODE    | $\overline{\text{CE}}$ | $\overline{\text{OE}}$ | $\overline{\text{WE}}$ | I/O   | POWER            |
|----------------|------------------------|------------------------|------------------------|-------|------------------|
| READ           | 0                      | 0                      | 1                      | D OUT | I <sub>DDO</sub> |
| WRITE          | 0                      | X                      | 0                      | D IN  | I <sub>DDO</sub> |
| OUTPUT DISABLE | 0                      | 1                      | 1                      | HI-Z  | I <sub>DDO</sub> |
| STANDBY        | 1                      | X                      | X                      | HI-Z  | I <sub>DDS</sub> |

0 ; LOW LEVEL

1 ; HIGH LEVEL

HI-Z; HIGH IMPEDANCE

X ; DON'T CARE

