Toshiba Bi-CD Integrated Circuit Silicon Monolithic

TB6592FL

Dual-Bridge Driver IC for DC motor

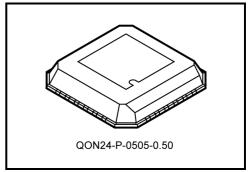
TB6592FL is a dual-bridge driver IC for DC motor with output transistor in LD MOS structure with low ON-resistor. Two input signals, IN1 and IN2, can chose one of four modes such as CW, CCW, short brake, and stop mode. Efficient driven at high temperature is possible by PWM drive system.

Features

- Power supply voltage for motor: $VM \le 6 V \text{ (max)}$
- Power supply voltage for control: VCC = 2.7 V to 6.0 V
- Output current: IOUT = 0.8 A (max)
- Low ON resistor: 1.5 Ω (typ.)

(Upper side + Lower side combined @ VM = 5 V, VCC = 5 V)

- Direct PWM control
- Standby system (Power save)
- CW/CCW/short brake/stop function modes.
- Built-in thermal shutdown circuit
- Small-size leadless package: QON24-P-0505-0.50

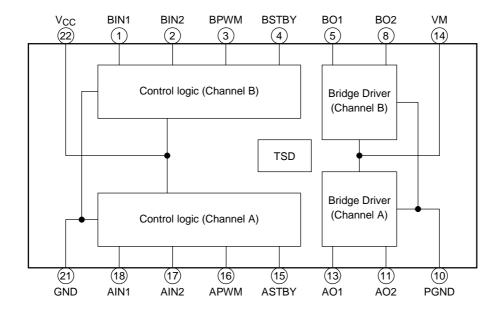


Weight: 0.05 g (typ.)

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^{*} This product has a MOS structure and is sensitive to electrostatic discharge. When handling this product, ensure that the environment is protected against electrostatic discharge by using an earth strap, a conductive mat and an ionizer. Ensure also that the ambient temperature and relative humidity are maintained at reasonable levels.

Block Diagram



Pin Functions

Pin Name	Pin No	Functional Description	Remarks				
GND	21	Small-signal GND pin	GND for small-signal power supply (V _{CC})				
AIN1	18	Control signal input 1 (Channel A)					
AIN2	17	Control signal input 2 (Channel A)					
APWM	16	PWM control signal input pin (Channel A)	Input PWM signal				
ASTBY	15	Standby control input pin (Channel A)	Channel A circuit is in standby (power save) state while this signal is Low.				
AO1	13	Output pin 1 (Channel A)	Channel A connect to motor coil pin				
AO2	11	Output pin 2 (Channel A)	Channel A connect to motor coil pin				
PGND	10	GND pin for motor	GND for motor power supply (VM)				
VM	14	Motor power supply pin	VM (ope) = 2.5 V to 5.5 V				
BO2	8	Output pin 2 (Channel B)	Channel B connect to motor coil pin				
BO1	5	Output pin 1 (Channel B)	Channel B connect to motor coil pin				
BSTBY	4	Standby control input pin (Channel B)	Channel B circuit is in standby (power save) state while this signal is Low.				
BPWM	3	PWM control signal input pin (Channel B)	Input PWM signal				
BIN2	2	Control signal input 2 (Channel B)					
BIN1	1	Control signal input 1 (Channel B)					
Vcc	22	Small-signal power supply pin	V _{CC (ope)} = 2.7 V to 5.5 V				

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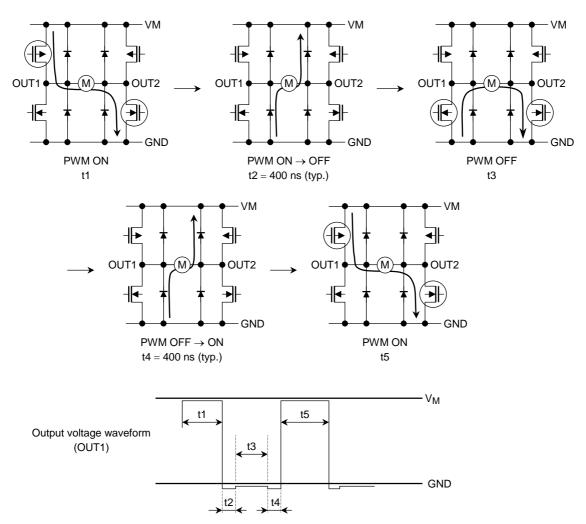
Note: Pins 6, 7, 9, 12, 19, 20, 23 and 24 are NC (not connected) pins.

Input/Output Function (common for channel A and B)

Input			Output			
IN1	IN2	STBY	PWM	O1	O2	Mode
Н	Н	Н	H L	L	L	Short brake
	L H	Н	Н	L	Н	CW/CCW
-		""	L	L	L	Short brake
ы	н L н	ш	Н	Н	L	CCW/CW
- ''		11	L	L	L	Short brake
L	L	Н	H L	OFF (high impedance)		Stop
H/L	H/L	L	H L	OFF (high impedance)		Standby

Operating Description

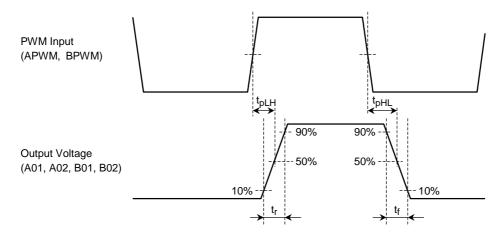
PWM control function
 Speed can be controlled by inputting the high-level or low-level PWM signal to the pin PWM.
 When PWM control is provided, normal operation and short brake operation are repeated.
 To prevent penetrating current, dead time t2 and t4 is provided in the IC.



Note: Please hold the pin PWM high when PWM control functionn is not used.

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Switching characteristics of output transistors
 The switching characteristics between PWM input and the output transistors are shown below.

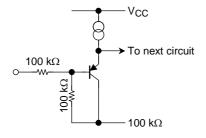


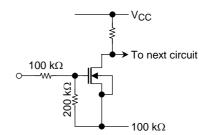
<Typical Value>

Item	Item Typical Value	
t _{pLH}	1000	
t _{pHL}	1000	ns
t _r	100	113
t _f	100	

• Input pin
Input pins (AIN1, AIN2, PWM, STBY) have internal pull-down resistors that are connected to ground.

IN1, IN2, PWM STBY





Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit	Remarks
Supply voltage	VM	6	V	
Supply Voltage	V _{CC}	6	V	
Input voltage	V _{IN}	−0.2 to 6	V	IN1, 2, STBY and PWM pins
Output current	lout	0.8	Α	
Power dissipation	P _D	0.78 (Note 1)	W	
Operating temperature	T _{opr}	-20 to 85	°C	
Storage temperature	T _{stg}	-55 to 150	°C	

Note 1: This value is obtained by $50 \times 30 \times 1.6$ mm glass-epoxy PCB mounting occupied 40% of copper area.

Operating Range ($Ta = -20 \text{ to } 85^{\circ}\text{C}$)

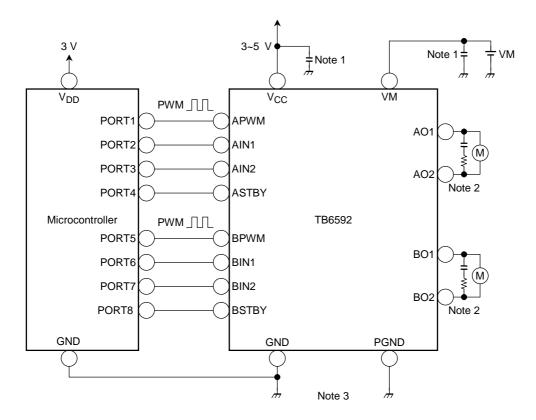
Characteristics	Symbol	Min	Тур.	Max	Unit
Supply voltage (V _{CC})	V _{CC}	2.7	3.0	5.5	V
Supply voltage (VM)	VM	2.2	_	5.5	V
Output current	lout	_	_	0.6	Α
PWM frequency	fPWM			100	kHz

Electrical Characteristics (unless otherwise specified, $V_{CC}=3~V,~VM=5~V,~Ta=25^{\circ}C)$

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit	
Supply current		I _{CC} (STP)	Stop mode	_	0.7	1.2		
		I _{CC (W)}	CW/CCW mode	_	0.7	1.2	mA	
		I _{CC (SB)}	Short break mode	_	0.7	1.2		
		I _{CC} (STB)	(Standby mode)	_	_	10	μА	
		I _{M (STB)}	(Standby mode)	_	_	1	μΑ	
	Input voltage	V_{INH}		2	_	V _{CC} + 0.2 V	0.2 V	
		V_{INL}		-0.2	_	0.8	V	
Control circuit	Hysteresis voltage	V _{IN (HIS)}	(Not tested)	_	0.2	_		
	Input current	I _{INH}		5	15	25	μА	
	input current	I _{INL}		_	_	1	μΑ	
	Input voltage	V _{INSH}		2	_	V _{CC} + 0.2 V	V	
Standby circuit		V _{INSL}		-0.2	_	0.8		
	Input current	I _{INSH}		5	10	20	μΑ	
		I _{INSL}		_	_	1		
Output saturating	voltage	V (11 - 1)	$I_O = 0.2 \text{ A}, V_{CC} = VM = 5 \text{ V}$	_	0.3	0.4	V	
Output saturating	voitage	V _{sat (U + L)}	$I_O = 0.6 \text{ A}, V_{CC} = VM = 5 \text{ V}$	_	0.9	1.2	v	
Output leakage co	ırrent	I _{L (U)}	I _{L (U)} VM = 6 V		_	1	μА	
Output leakage of	arrent	I _{L (L)}			_	1	μΑ	
Diode forward vol	tane	V _{F (U)}	$I_{O} = 0.6 \text{ A}$	_	1	1 —		
Diode forward voi	lage	V _{F (L)}	$I_{O} = 0.6 \text{ A}$	_	1		V	
PWM control	PWM frequency	f _{PWM}		_	_	100	kHz	
circuit	Minimum clock pulse width	t _{w (PWM)}		_	_	10	μS	
Output transistor switching		Tr		_	100	_		
		Tf			100	_	ne	
		t _{pLH} (PWM)	(Not tested)	_	1000	_	ns	
		t _{pHL} (PWM)		_	1000	_		
Thermal shutdown circuit operating temperature		T _{SD}	(Not tested)	_	170	_	°C	
Thermal shutdown hysteriesis		ΔT_{SD}	(Not tested)	_	20	_	°C	

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Typical Application Diagram



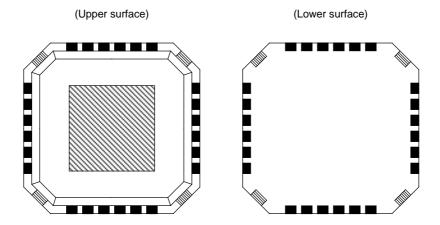
- Note 1: The power supply capacitor should be connected as close as possible to the IC.
- Note 2: When connecting the motor pins through the capacitor for reducing noise, connect a resistor to the capacitor for limiting the charge current.

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Note 3: Avoid using common impedance for GND and PGND.

Requests Concerning Use of QON

Outline Drawing of Package



When using QON, please take into account the following items.

Caution

- (1) Do not carry out soldering on the island section in the four corners of the package (the section shown on the lower surface drawing with diagonal lines) with the aim of increasing mechanical strength.
- (2) The island section exposed on the package surface (the section shown on the upper surface drawing with diagonal lines) must be used as (Note) below while electrically insulated from outside.

Note: Ensure that the island section (the section shown on the lower surface drawing with diagonal lines) does not come into contact with solder from through-holes on the board layout.

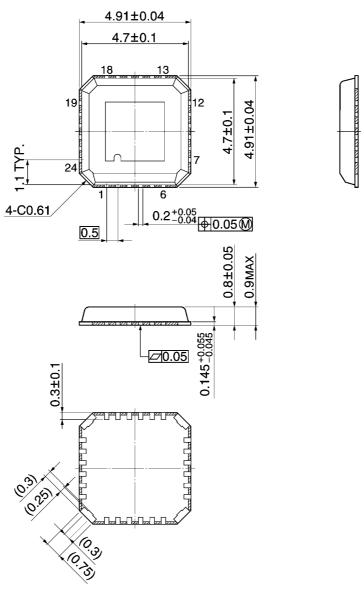
• When mounting or soldering, take care to ensure that neither static electricity nor electrical overstress is applied to the IC (measures to prevent anti-static, leaks, etc.).

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 When incorporating into a set, adopt a set design that does not apply voltage directly to the island section.

Package Dimensions

QON24-P-0505-0.50 Unit: mm



- Note 1) The solder plating portion in four corners of the package shall not be treated as an external terminal.
- Note 2) Don't carry out soldering to four corners of the package.
- Note 3) area: Resin surface

Weight: 0.05 g (typ.)

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