

IC INFORMATION

TA2151FN

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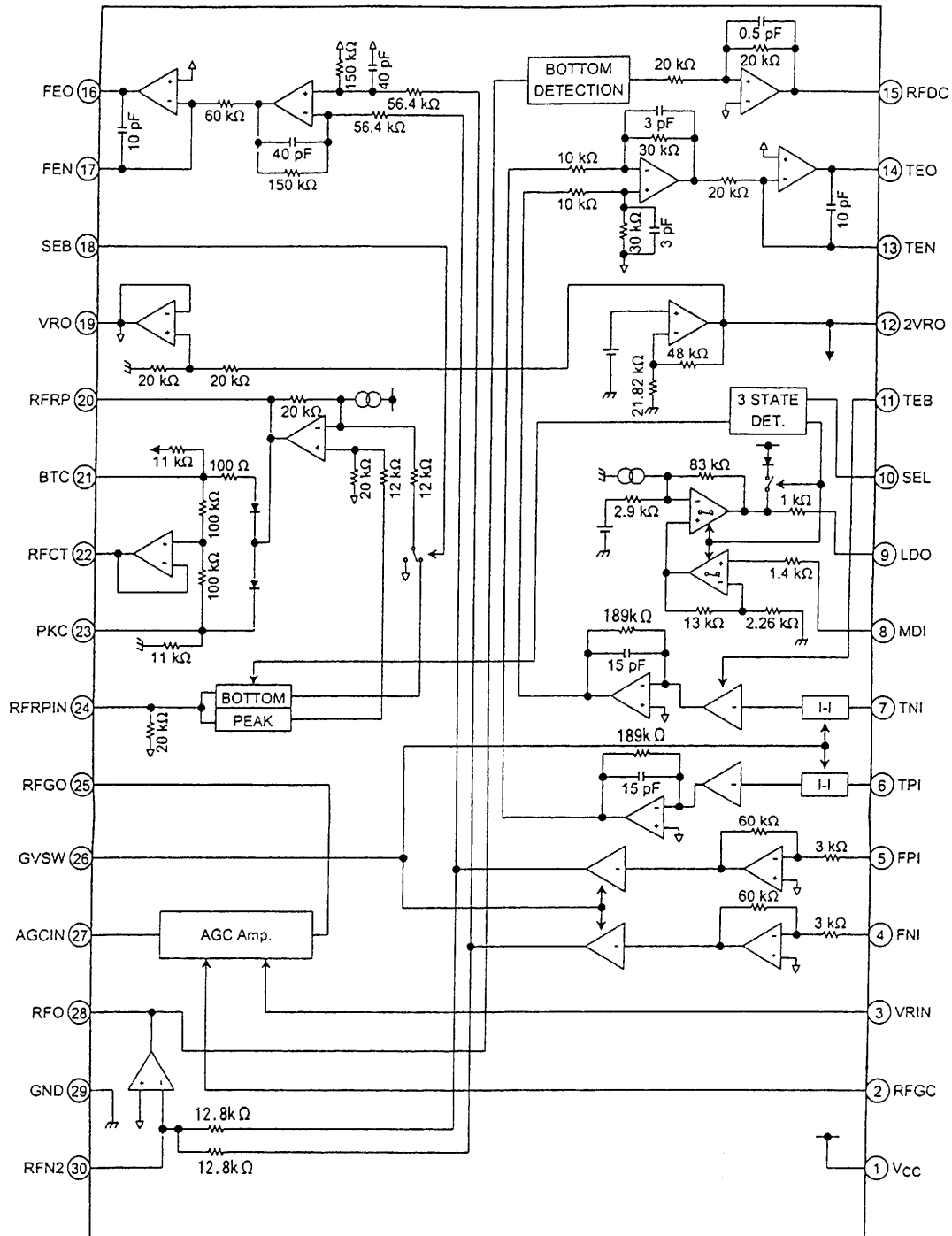
E

Function RF Amplifier for Digital Servo CD System

Type C MOS

Model XR-VS400

● Block Diagram



SEL	LDC			RFRP Detect Frequency
	SW1	SW2	SW3	
GND	ON	OFF	OFF	Low
HiZ	OFF	ON	ON	High
VCC	OFF	ON	ON	High

GVSU	Mode
GND	CD-RW
HiZ	Normal
VCC	

SEB	Bottom Detect	Peak Detect
GND	ON	ON
HiZ		
VCC	OFF	

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●Pin Function

Pin No.	Symbol	I/O	Functional Description	Remarks													
1	VCC	—	Power supply input terminal.	—													
2	RFGC	I	RF amplitude adjustment control signal input terminal. Controlled by 3-PWM signals. (PWM carrier = 88.2 kHz) RFGC input voltage: VRO ± 1.5 V AGC amplifier voltage again: x0.7~1.5 (typ.)	—													
3	VRIN	I	AGC amp. Reference voltage input terminal.	Connected to VRO													
4	FNI	I	Main beam I-V amp input terminal.	Connected to pin diode output B + D (through resistor).													
5	FPI	I	Main beam I-V amp input terminal.	Connected to pin diode output A + C (through resistor).													
6	TPI	I	Sub beam I-V amp input terminal.	Connected to pin diode output F.													
7	TNI	I	Sub beam I-V amp input terminal.	Connected to pin diode output E.													
8	MDI	I	Monitor photo diode amp input terminal.	Connected to monitor photo diode.													
9	LDO	O	Laser diode amp input terminal.	Connected to laser diode control circuit.													
10	SEL	I	Laser diode control signal input terminal and APC circuit ON/OFF control signal terminal. <table border="1"><tr><td>SEL Level</td><td>APC Circuit</td><td>LDO</td><td>Detect Frequency</td></tr><tr><td>GND</td><td>OFF</td><td>Connected to VCC through resistor (1 kΩ)</td><td rowspan="2">Low</td></tr><tr><td>HiZ</td><td rowspan="2">ON</td><td rowspan="2">Control signal output</td></tr><tr><td>VCC</td><td>High</td></tr></table>	SEL Level	APC Circuit	LDO	Detect Frequency	GND	OFF	Connected to VCC through resistor (1 kΩ)	Low	HiZ	ON	Control signal output	VCC	High	3 signals input. (VCC, HiZ, GND)
SEL Level	APC Circuit	LDO	Detect Frequency														
GND	OFF	Connected to VCC through resistor (1 kΩ)	Low														
HiZ	ON	Control signal output															
VCC			High														
11	TEB	I	Tracking error balance adjustment signal input terminal. Controlled by 3-PWM signal. (PWM carrier = 88.2 kHz)	3 signals input. (2VRO, VRO, GND)													
12	2VRO	O	Reference voltage (2VRO) output terminal. 2VRO = 4.2 V when VCC = 5 V	—													
13	TEN	I	TE amp negative input terminal.	Connected to TEO through feedback resistor.													
14	TEO	O	TE error signal output terminal.	—													
15	RFDC	O	RF signal peak detect output terminal.	—													
16	FEO	O	Focus error signal output terminal.	—													
17	FEN	I	FE amp negative input terminal.	Connected to FEO through feedback resistor.													
18	SEB	I	RFRP output circuit switching terminal. <table border="1"><tr><td>SEB Level</td><td>Bottom Detection</td><td>Peak Detection</td></tr><tr><td>GND</td><td>ON</td><td rowspan="2">ON</td></tr><tr><td>VCC</td><td>OFF</td></tr></table>	SEB Level	Bottom Detection	Peak Detection	GND	ON	ON	VCC	OFF	Low (GND) is for normal use.					
SEB Level	Bottom Detection	Peak Detection															
GND	ON	ON															
VCC	OFF																
19	VRO	O	Reference voltage (VRO) output terminal. VRO = 2.1 V when VCC = 5 V	—													

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Pin No.	Symbol	I/O	Functional Description	Remarks							
20	RFRP	O	Track count signal output terminal.	—							
21	BTC	I	Time constant adjustment terminal for bottom detection.	Adjusted by capacitance.							
22	RFCT	O	RFRP signal center level output terminal.	—							
23	PKC	I	Time constant adjustment terminal for peak detection.	Adjusted by capacitance.							
24	RFRPIN	I	Input terminal for track count signal output amp.	—							
25	RFGO	O	Output terminal for RF signal amplitude adjustment amp.	—							
26	GVSF	I	Amp (FE, TE) gain switching terminal. <table><tr><td>GVSF</td><td>Mode</td></tr><tr><td>GND</td><td>CD-RW</td></tr><tr><td>HiZ</td><td rowspan="2">Normal</td></tr><tr><td>V_{CC}</td></tr></table>	GVSF	Mode	GND	CD-RW	HiZ	Normal	V _{CC}	Low (GND) is for 5 times gain.
GVSF	Mode										
GND	CD-RW										
HiZ	Normal										
V _{CC}											
27	AGCIN	I	Input terminal for RF signal amplitude adjustment amp.	Connected to RFO through capacitance.							
28	RFO	O	Output terminal RF signal amp.	—							
29	GND	—	Ground terminal.	—							
30	RFN2	I	Input terminal for RF signal amp.	—							

Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Power supply voltage	V _{CC}	8	V
Power dissipation	P _D	500	mW
Operating temperature	T _{opr}	−40~85	°C
Storage temperature	T _{stg}	−55~150	°C