

N - CHANNEL ENHANCEMENT MODE FAST POWER MOS TRANSISTOR

PRELIMINARY DATA

TYPE	V _{DSS}	R _{DS(on)}	I _D
STU8NA80	800 V	< 1.0 Ω	8.3 A

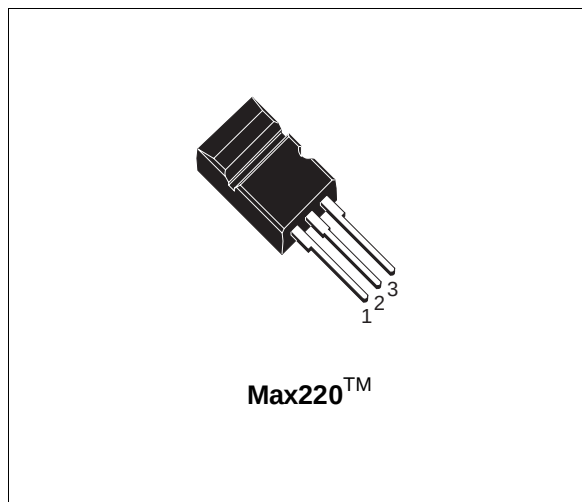
- TYPICAL R_{DS(on)} = 0.85 Ω
- EFFICIENT AND RELIABLE MOUNTING THROUGH CLIP
- ± 30V GATE TO SOURCE VOLTAGE RATING
- 100% AVALANCHE TESTED
- GATE CHARGE MINIMIZED
- REDUCED THRESHOLD VOLTAGE SPREAD.

DESCRIPTION

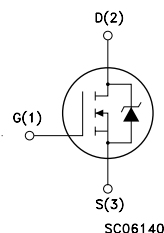
The Max220™ package is a new high volume power package exhibiting the same footprint as the industry standard TO-220, but designed to accommodate much larger silicon chips, normally supplied in bigger packages. The increased die capacity makes the device ideal to reduce component count in multiple paralleled TO-220 designs and save board space with respect to larger packages.

APPLICATIONS

- HIGH CURRENT, HIGH SPEED SWITCHING
- SWITCH MODE POWER SUPPLIES (SMPS)
- DC-AC CONVERTERS FOR WELDING EQUIPMENT AND UNINTERRUPTIBLE POWER SUPPLIES (UPS)



INTERNAL SCHEMATIC DIAGRAM



ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V _{DS}	Drain-source Voltage (V _{GS} = 0)	800	V
V _{DGR}	Drain- gate Voltage (R _{GS} = 20 kΩ)	800	V
V _{GS}	Gate-source Voltage	± 30	V
I _D	Drain Current (continuous) at T _c = 25 °C	8.3	A
I _D	Drain Current (continuous) at T _c = 100 °C	5.3	A
I _{DM} (•)	Drain Current (pulsed)	33.2	A
P _{tot}	Total Dissipation at T _c = 25 °C	160	W
	Derating Factor	1.28	W/°C
T _{stg}	Storage Temperature	-65 to 150	°C
T _j	Max. Operating Junction Temperature	150	°C

(•) Pulse width limited by safe operating area

THERMAL DATA

$R_{thj-case}$	Thermal Resistance Junction-case	Max	0.78	$^{\circ}C/W$
$R_{thj-amb}$	Thermal Resistance Junction-ambient	Max	62.5	$^{\circ}C/W$
$R_{thc-sink}$	Thermal Resistance Case-sink	Typ	0.5	$^{\circ}C/W$
T_l	Maximum Lead Temperature For Soldering Purpose		300	$^{\circ}C$

AVALANCHE CHARACTERISTICS

Symbol	Parameter	Max Value	Unit
I_{AR}	Avalanche Current, Repetitive or Not-Repetitive (pulse width limited by T_j max, $\delta < 1\%$)	8	A
E_{AS}	Single Pulse Avalanche Energy (starting $T_j = 25^{\circ}C$, $I_D = I_{AR}$, $V_{DD} = 50 V$)	320	mJ
E_{AR}	Repetitive Avalanche Energy (pulse width limited by T_j max, $\delta < 1\%$)	14	mJ
I_{AR}	Avalanche Current, Repetitive or Not-Repetitive ($T_c = 100^{\circ}C$, pulse width limited by T_j max, $\delta < 1\%$)	5.3	A

ELECTRICAL CHARACTERISTICS ($T_{case} = 25^{\circ}C$ unless otherwise specified)

OFF

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$V_{(BR)DSS}$	Drain-source Breakdown Voltage	$I_D = 250 \mu A$ $V_{GS} = 0$	800			V
I_{DSS}	Zero Gate Voltage Drain Current ($V_{GS} = 0$)	$V_{DS} = \text{Max Rating}$ $V_{DS} = \text{Max Rating} \times 0.8$ $T_c = 125^{\circ}C$			250 1000	μA μA
I_{GSS}	Gate-body Leakage Current ($V_{DS} = 0$)	$V_{GS} = \pm 30 V$			± 100	nA

ON (*)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}$ $I_D = 250 \mu A$	2.25	3	3.75	V
$R_{DS(on)}$	Static Drain-source On Resistance	$V_{GS} = 10V$ $I_D = 4A$ $V_{GS} = 10V$ $I_D = 4A$ $T_c = 100^{\circ}C$		0.85	1 2	Ω Ω
$I_{D(on)}$	On State Drain Current	$V_{DS} > I_{D(on)} \times R_{DS(on)max}$ $V_{GS} = 10 V$	8.3			A

DYNAMIC

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$g_{fs} (*)$	Forward Transconductance	$V_{DS} > I_{D(on)} \times R_{DS(on)max}$ $I_D = 4 A$	7	9.5		S
C_{iss}	Input Capacitance	$V_{DS} = 25 V$ $f = 1 MHz$ $V_{GS} = 0$		2900	3800	pF
C_{oss}	Output Capacitance			290	370	pF
C_{rss}	Reverse Transfer Capacitance			80	110	pF

ELECTRICAL CHARACTERISTICS (continued)**SWITCHING ON**

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$t_{d(on)}$	Turn-on Time	$V_{DD} = 400\text{ V}$		37	50	ns
t_r	Rise Time	$R_G = 4.7\ \Omega$ $I_D = 4\text{ A}$ $V_{GS} = 10\text{ V}$		45	60	ns
$(di/dt)_{on}$	Turn-on Current Slope	$V_{DD} = 640\text{ V}$ $I_D = 8\text{ A}$ $R_G = 47\ \Omega$ $V_{GS} = 10\text{ V}$		120		A/ μ s
Q_g	Total Gate Charge	$V_{DD} = 400\text{ V}$ $I_D = 8\text{ A}$ $V_{GS} = 10\text{ V}$		115	150	nC
Q_{gs}	Gate-Source Charge			15		nC
Q_{gd}	Gate-Drain Charge			55		nC

SWITCHING OFF

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$t_{r(Voff)}$	Off-voltage Rise Time	$V_{DD} = 640\text{ V}$ $I_D = 8\text{ A}$		45	60	ns
t_f	Fall Time	$R_G = 4.7\ \Omega$ $V_{GS} = 10\text{ V}$		15	20	ns
t_c	Cross-over Time			70	91	ns

SOURCE DRAIN DIODE

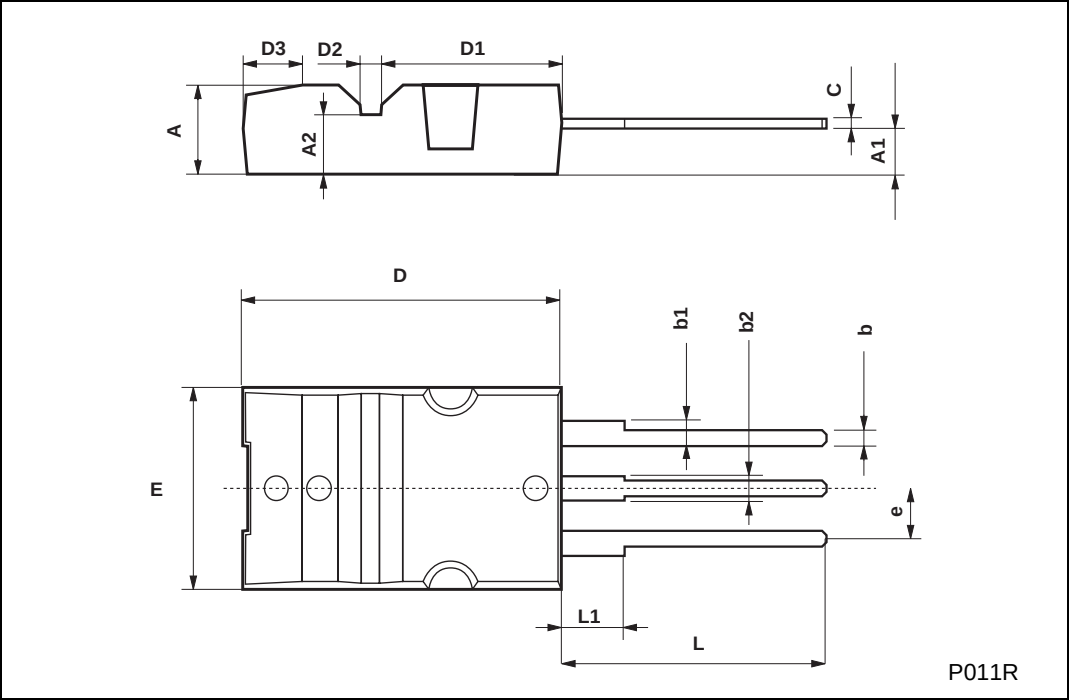
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
I_{SD}	Source-drain Current				8.3	A
$I_{SDM}(\bullet)$	Source-drain Current (pulsed)				33.2	A
$V_{SD} (*)$	Forward On Voltage	$I_{SD} = 8.3\text{ A}$ $V_{GS} = 0$			1.6	V
t_{rr}	Reverse Recovery Time	$I_{SD} = 8\text{ A}$ $di/dt = 100\text{ A}/\mu\text{s}$ $V_{DD} = 100\text{ V}$ $T_j = 150\text{ }^\circ\text{C}$		765		ns
Q_{rr}	Reverse Recovery Charge			13.4		μC
I_{RRM}	Reverse Recovery Current			35		A

(*) Pulsed: Pulse duration = 300 μ s, duty cycle 1.5 %

(\bullet) Pulse width limited by safe operating area

Max220 MECHANICAL DATA

DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A	4.3		4.6	0.169		0.181
A1	2.2		2.4	0.087		0.094
A2	2.9		3.1	0.114		0.122
b	0.7		0.93	0.027		0.036
b1	1.25		1.4	0.049		0.055
b2	1.2		1.38	0.047		0.054
c	0.45		0.6		0.18	0.023
D	15.9		16.3		0.626	0.641
D1	9		9.35	0.354		0.368
D2	0.8		1.2	0.031		0.047
D3	2.8		3.2	0.110		0.126
e	2.44		2.64	0.096		0.104
E	10.05		10.35	0.396		0.407
L	13.2		13.6	0.520		0.535
L1	3		3.4	0.118		0.133



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