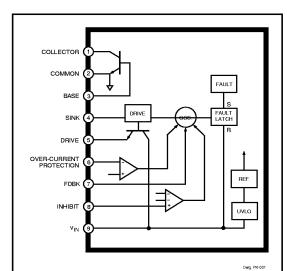
### OFF-LINE SWITCHING REGULATORS WITH BIPOLAR SWITCHING TRANSISTOR



ABSOLUTE MAXIMUM RATINGS
Supply Voltage, V <sub>IN</sub> <b>15 V</b>
Output Voltage, V <sub>CEX</sub>
Continuous Output Current,
I <sub>C</sub> See Table
1 ms Single-Pulse Output Current,
I <sub>CM</sub> See Table
Sink Current, I <sub>S</sub> <b>See Table</b>
Drive Current, I <sub>D</sub> 700 mA
Feedback Current, I <sub>FDBK</sub> 20 mA
Inhibit Voltage, V <sub>INH</sub> <b>15 V</b>
Over-Current Protection Voltage Range,
V <sub>OCP</sub> ±3.5 V
Insulation Voltage, V <sub>WM(RMS)</sub> <b>2000 V</b>
Package Power Diss., P <sub>D</sub> <b>See Graph</b>
Output Junction Temperature, T <sub>J</sub> +150°C
Internal Frame Temperature, T <sub>F</sub> <b>+125°C</b>
Operating Temperature Range, T <sub>A</sub> 20°C to +125°C
Storage Temperature Range,
T <sub>stg</sub> 40°C to +125°C

The STR-S6707, STR-S6708, and STR-S6709 are specifically designed to meet the requirement for increased integration and reliability in off-line quasi-resonant flyback converters. These devices incorporate the primary control and proportional drive circuit with a thirdgeneration high-voltage bipolar switching transistor.

Crucial system parameters such as maximum ON time and OFF time are fixed during manufacture. Local control circuit decoupling and layout are optimized within each device.

Cycle-by-cycle current limiting, under-voltage lock-out with hysteresis, over-voltage protection, and thermal shutdown protect these devices during all normal and overload conditions. Over-voltage protection and thermal shutdown are latched after a short delay. A versatile triple-level inhibit circuit includes the OFF time synchronization required to establish quasi-resonant operation. The inhibit function has also been expanded to initiate operation in stand-by mode in which the power supply delivers a small fraction of the steady-state output power. The dual requirements of dielectric isolation and low transient thermal impedance and steady-state thermal resistance are satisfied in an overmolded single-in-line power package.

Proven in substantial volumes, these devices and their fixedfrequency counterparts represent a significant advance in off-line SMPS reliability growth and integration.

#### **FEATURES**

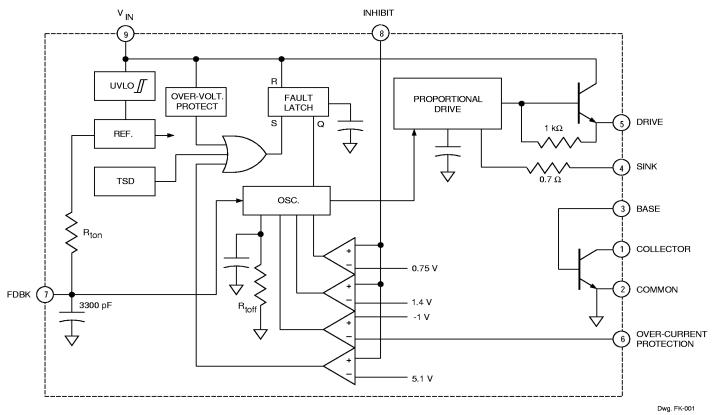
- Quasi-Resonant Operation for Low EMI and High Efficiency
- Output Power to 220 W
- Low-Power Output Standby Mode
- Pulse-by-Pulse Over-Current Protection
- Latched Over-Voltage and Thermal Protection
- Third-Generation Switching Transistor with Proportional Drive
- Maximum ON Time and Off Time Set During Manufacture
- Internal Under-Voltage Lockout with Hysteresis
- Over-Molded SIP with Integral Isolated Heat Spreader

Always order by complete part number:

Part Number	Max. Cont. Current, I <sub>c</sub>	Peak Current, I <sub>CM</sub>	Max. Sink Current, I <sub>S</sub>
STR-S6707	6 A	12 A	1.5 A
STR-S6708	7.5 A	15 A	1.5 A
STR-S6709	10 A	20 A	2 A

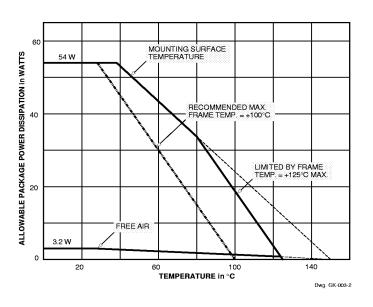


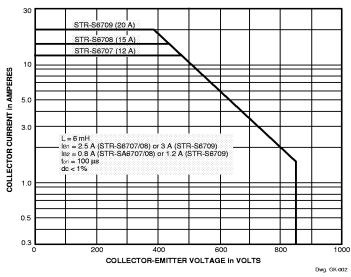
#### STR-S6707 AND STR-S6708 FUNCTIONAL BLOCK DIAGRAM



#### **ALLOWABLE PACKAGE POWER DISSIPATION**

#### **MAXIMUM SAFE OPERATING AREA**

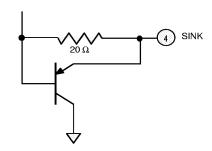






115 Northeast Cutoff, Box 15036 Worcester, Massachusetts 01615-0036 (508) 853-5000 Copyright © 1994 Allegro MicroSystems, Inc.

> STR-S6709 FUNCTIONAL BLOCK DIAGRAM AS ABOVE EXCEPT FOR SINK OUTPUT

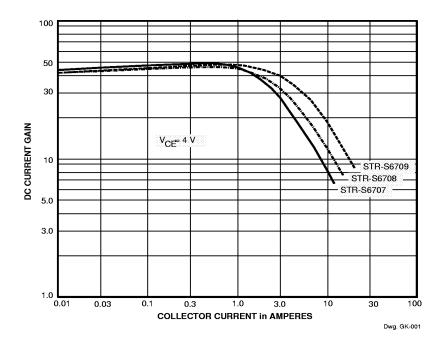


# ELECTRICAL CHARACTERISTICS at $T_A = +25^{\circ}C$ , $V_{IN} = 8.5$ V, voltage measurements are referenced to Common (pin 2) (unless otherwise noted).

			Limits			
Characteristic	Symbol	Test Conditions	Min.	Тур.	Max.	Units
On-State Voltage	V <sub>INT</sub>	Turn-on, increasing V <sub>IN</sub>	7.6	8.0	8.4	V
Under-Voltage Lockout	V <sub>INQ</sub>	Turn-off, decreasing V <sub>IN</sub>	4.6	4.9	5.2	V
Over-Voltage Threshold	V <sub>OVP(th)</sub>	**	9.2	-	10.7	V
Output Leakage Current	I <sub>CEX</sub>	$V_{CE} = 850 \text{ V}, V_{BE} = -1.5 \text{ V}$	_	_	100	μΑ
Output Saturation Voltage	V <sub>CE(sat)</sub>	STR-S6707, $I_C = 2 \text{ A}$ , $I_B = 400 \text{ mA}$	_	_	400	mV
		STR-S6708, $I_C = 3 \text{ A}$ , $I_B = 600 \text{ mA}$	_	_	400	mV
		STR-S6709, I <sub>C</sub> = 4 A, I <sub>B</sub> = 800 mA	_	_	400	mV
	V <sub>BE(sat)</sub>	STR-S6707, $I_C = 2 \text{ A}$ , $I_B = 400 \text{ mA}$	_	_	1.5	V
	, ,	STR-S6708, $I_C = 3 \text{ A}$ , $I_B = 600 \text{ mA}$	_	_	1.5	V
		STR-S6709, I <sub>C</sub> = 4 A, I <sub>B</sub> = 800 mA	_	_	1.5	V
DC Current Gain	h <sub>FE</sub>	$V_{CE} = 4 \text{ V}, I_{C} = 1 \text{ A}$	29	_	61	_
Maximum ON Time	t <sub>on</sub>		33	_	41	μs
Minimum OFF Time	t <sub>off</sub>		45	_	55	μs
Over-Current Threshold	V <sub>OCP(th)</sub>		-0.9	-1.0	-1.1	V
Feedback Threshold Volt.	V <sub>FDBK(th)</sub>		_	650	_	mV
Inhibit Threshold Voltage	V <sub>INH(th)</sub>	Oscillation stops	0.65	0.75	0.85	V
		Oscillation synchronized	_	1.4	2.0	V
		Oscillation stops (fault latch set)	3.2	5.1	5.6	V
Latch Holding Current	I <sub>INH</sub>	V <sub>IN</sub> reduced from 10.7 V to 4 V	_	_	500	μΑ
Latch Reset Voltage	V <sub>Q</sub>	$I_{IN} \le 100 \mu A$ , $V_{IN}$ reduced from 10.7 V	2.5	3.1	_	V
Supply Current	I <sub>IN(ON)</sub>	Operating	15	_	29	mA
	I <sub>IN(OFF)</sub>		_	_	200	μA
Insulation RMS Voltage	V <sub>WM(RMS)</sub>	All terminals simultaneous reference metal plate against backside	2000	_	_	V
Thermal Shutdown	T <sub>J</sub>		125	150	_	°C
Thermal Resistance	$R_{\theta JM}$	Output junction to mounting surface	_	2.0	_	°C/W

NOTES: Negative current is defined as coming out of (sourcing) the specified device terminal. Typical Data is for design information only.

#### **TYPICAL CHARACTERISTICS**

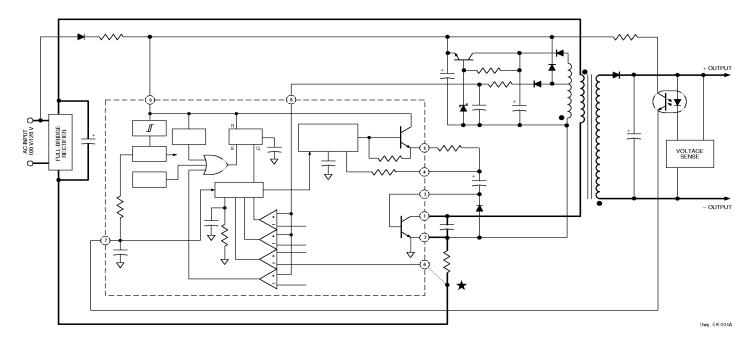






#### TYPICAL QUASI-RESONANT FLYBACK CONVERTER

**WARNING:** lethal potentials are present. See text.



#### **APPLICATIONS INFORMATION**



WARNING — These devices are designed to be operated at lethal voltages and energy levels. Circuit designs that embody these components must conform with applicable safety requirements. Precautions must be taken to prevent accidental contact with power-line potentials. Do not connect grounded test equipment.

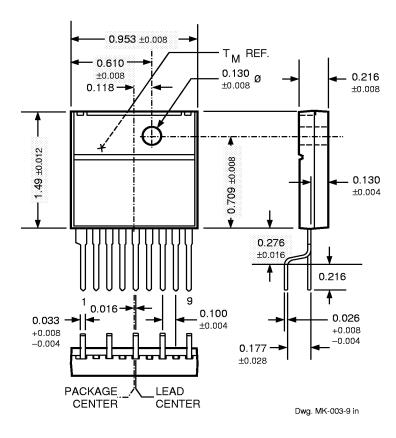
The use of an isolation transformer is recommended during circuit development and breadboarding.

Recommended mounting hardware torque:

4.34 - 5.79 lbf•ft (6 – 8 kg•cm or 0.588 - 0.784 Nm).

Recommended metal-oxide-filled, alkyl-degenerated oil base, silicone grease: Dow Corning 340, or equivalent

## Dimensions in Inches (for reference only)



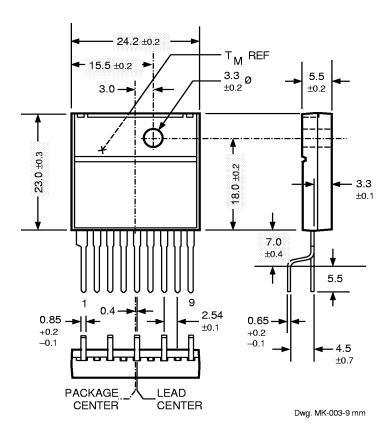
NOTE: Exact body and lead configuration at vendor's option within limits shown.





#### **Dimensions in Millimeters**

(controlling dimensions)



NOTE: Exact body and lead configuration at vendor's option within limits shown.

The products described here are manufactured in Japan by Sanken Electric Co., Ltd. for sale by Allegro MicroSystems, Inc.

Sanken Electric Co., Ltd. and Allegro MicroSystems, Inc. reserve the right to make, from time to time, such departures from the detail specifications as may be required to permit improvements in the design of their products.

The information included herein is believed to be accurate and reliable. However, Sanken Electric Co., Ltd. and Allegro MicroSystems, Inc. assume no responsibility for its use; nor for any infringements of patents or other rights of third parties which may result from its use.