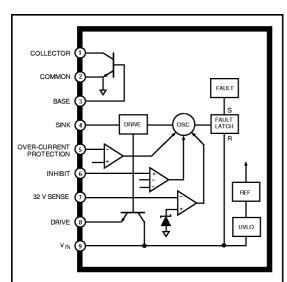
STR-S5707 AND **STR-S5708**

OFF-LINE SWITCHING REGULATORS - WITH BIPOLAR SWITCHING TRANSISTOR



ABSOLUTE MAXIMUM RATINGS
Supply Voltage, V _{IN} 15 V
Output Voltage, V _{CEX} 850 V V _{EBO} 7 V
Continuous Output Current, I _C See Table
1 ms Single-Pulse Output Current, I _{CM} See Table
Sink Current, I _S
Drive Current, I _D 700 mA
Inhibit Voltage, V _{INH} 15 V
Over-Current Protection Voltage Range, V _{OCP} ±3.5 V
Insulation Voltage,V _{WM(RMS)} 2000 V
Package Power Diss., P _D See Graph
Output Junction Temperature, T _J +150°C
Internal Frame Temperature, T _F +125°C
Operating Temperature Range, T _A 20°C to +125°C
Storage Temperature Range,

The STR-S5707 and STR-S5708 are specifically designed to meet the requirement for increased integration and reliability in off-line quasiresonant flyback converters with indirect feedback. Each device incorporates the primary control and proportional drive circuit with a third-generation high-voltage bipolar switching transistor.

Crucial system parameters such as maximum ON time and OFF time are fixed during manufacture. Local control circuit decoupling and layout are optimized within each device.

Cycle-by-cycle current limiting, under-voltage lock-out with hysteresis, over-voltage protection, and thermal shutdown protect these devices during all normal and overload conditions. Over-voltage protection and thermal shutdown are latched after a short delay. A primary-side error amplifier with reference is included to facilitate regulation from an auxiliary or bias winding of the power transformer. A versatile triple-level inhibit circuit includes the OFF time synchronization required to establish quasi-resonant operation. The inhibit function has also been expanded to initiate operation in stand-by mode in which the power supply delivers a small fraction of the steady-state output power. The dual requirements of dielectric isolation and low transient thermal impedance and steady-state thermal resistance are satisfied in an overmolded single-in-line power package.

Proven in substantial volumes, this device and its fixed-frequency counterparts represents a significant advance in off-line SMPS reliability growth and integration.

FEATURES

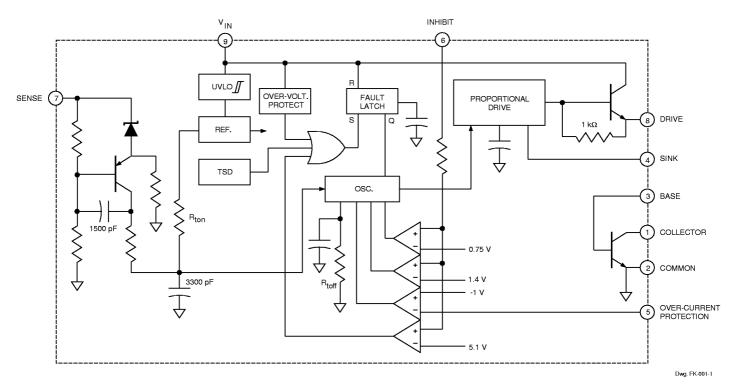
- Quasi-Resonant Operation for Low EMI and High Efficiency
- Low-Power Output Standby Mode
- Indirect Feedback from Auxiliary Winding Reduces External Component Count
- Pulse-by-Pulse Over-Current Protection
- Latched Over-Voltage and Thermal Protection
- Third-Generation Switching Transistor with Proportional Drive
- Maximum ON Time and Off Time Set During Manufacture
- Internal Under-Voltage Lockout with Hysteresis
- Over-Molded SIP with Integral Isolated Heat Spreader

Always order by complete part number:

Part Number	Output Current		Output Power, Po		
Number	Continuous, $I_{\rm C}$	Peak, I _{CM}		220/240 VAC In	
STR-S5707	6 A	12 A	90 W	140 W	
STR-S5708	7.5 A	15 A	120 W	180 W	

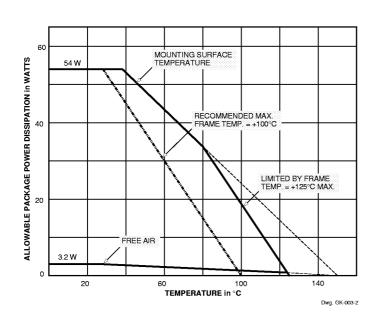


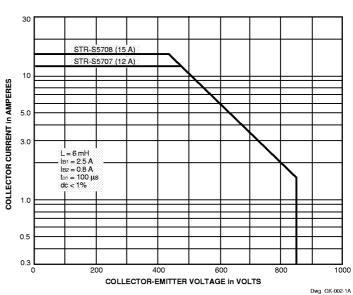
FUNCTIONAL BLOCK DIAGRAM



ALLOWABLE PACKAGE POWER DISSIPATION

MAXIMUM SAFE OPERATING AREA







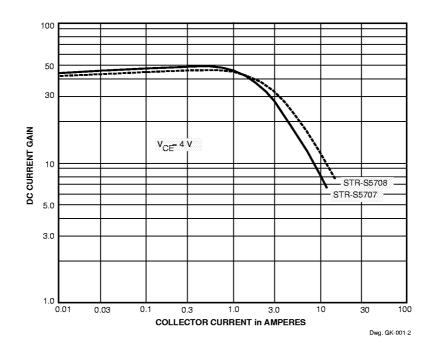


ELECTRICAL CHARACTERISTICS at $T_A = +25^{\circ}C$, $V_{IN} = 8.5$ V, voltage measurements are referenced to Common (pin 2) (unless otherwise noted).

			Limits			
Characteristic	Symbol	Test Conditions	Min.	Тур.	Max.	Units
On-State Voltage	V _{INT}	Turn-on, increasing V _{IN}	7.6	8.0	8.4	V
Under-Voltage Lockout	V _{INQ}	Turn-off, decreasing V _{IN}	4.6	4.9	5.2	٧
Over-Voltage Threshold	V _{OVP(th)}		9.2	_	10.7	٧
Output Leakage Current	I _{CEX}	$V_{CE} = 850 \text{ V}, V_{BE} = -1.5 \text{ V}$	_	_	100	μА
Output Saturation Voltage	V _{CE(sat)}	STR-S5707, I _C = 2 A, I _B = 400 mA	_	_	400	mV
		STR-S5708, $I_C = 3 \text{ A}, I_B = 600 \text{ mA}$	_	_	400	mV
	V _{BE(sat)}	STR-S5707, I _C = 2 A, I _B = 400 mA	_	_	1.5	٧
		STR-S5708, I _C = 3 A, I _B = 600 mA	_	_	1.5	٧
DC Current Gain	h _{FE}	V _{CE} = 4 V, I _C = 1 A	29	_	61	_
Maximum ON Time	t _{on}		33	_	41	μs
Minimum OFF Time	t _{off}		45	_	55	μs
Over-Current Threshold	V _{OCP(th)}		-0.88	-1.0	-1.12	٧
Sense Voltage	V _{SENSE}	I _{SENSE} = 3.2 mA	31.7	32.0	32.3	٧
Inhibit Threshold Voltage	V _{INH(th)}	Oscillation stops	0.65	0.75	0.85	٧
		Oscillation synchronized	_	1.4	2.0	٧
		Oscillation stops (fault latch set)	3.2	5.1	5.8	٧
Latch Holding Current	I _{INH}	V _{IN} reduced from 10.7 V to 4 V	_	_	500	μА
Latch Reset Voltage	V _Q	$I_{IN} \le 100 \mu A$, V_{IN} reduced from 10.7 V	2.5	3.1	_	٧
Ref. Voltage Temp. Coeff.	α_{VZ}	$-20^{\circ}\text{C} \le \text{T}_{\text{F}} \le +100^{\circ}\text{C}, \text{ I}_{\text{IN}} = 3.2 \text{ mA}$	_	2.5	_	mV/°C
Supply Current	I _{IN(ON)}	Operating	15	_	28	mA
	I _{IN(OFF)}		_	_	200	μА
Insulation RMS Voltage	V _{WM(RMS)}	All terminals simultaneous reference metal plate against backside	2000	_	_	٧
Thermal Shutdown	T _J		125	150	=	°C
Thermal Resistance	R _{eJM}	Output junction to mounting surface	_	2.0	_	°C/W

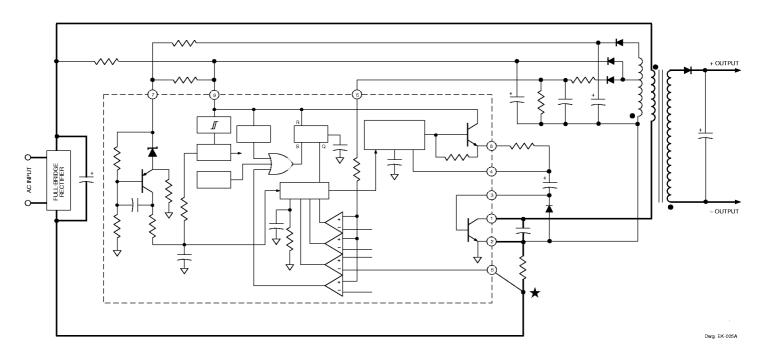
NOTES: Negative current is defined as coming out of (sourcing) the specified device terminal. Typical Data is for design information only.

TYPICAL CHARACTERISTICS



TYPICAL QUASI-RESONANT FLYBACK CONVERTER

WARNING: lethal potentials are present. See below.



APPLICATIONS INFORMATION

WARNING — These devices are designed to be operated at lethal voltages and energy levels.

Circuit designs that embody these components must conform with applicable safety requirements. Precautions must be taken to prevent accidental contact with power-line potentials. Do not connect grounded test equipment.

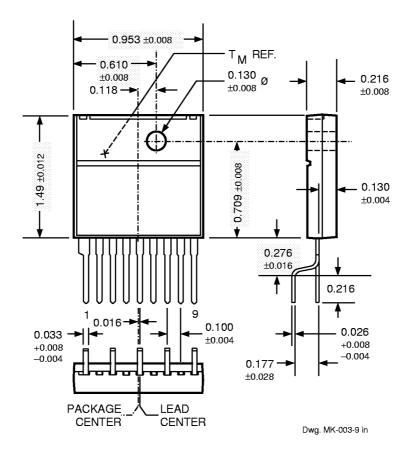
The use of an isolation transformer is recommended during circuit development and breadboarding.

Recommended mounting hardware torque:

4.34 - 5.79 lbf•ft (6 – 8 kg•cm or 0.588 - 0.784 Nm).

Recommended metal-oxide-filled, alkyl-degenerated oil base, silicone grease: Dow Corning 340, or equivalent

Dimensions in Inches (for reference only)



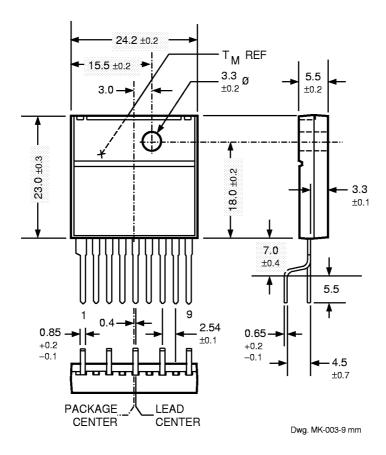
NOTE: Exact body and lead configuration at vendor's option within limits shown.





Dimensions in Millimeters

(controlling dimensions)



NOTE: Exact body and lead configuration at vendor's option within limits shown.

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