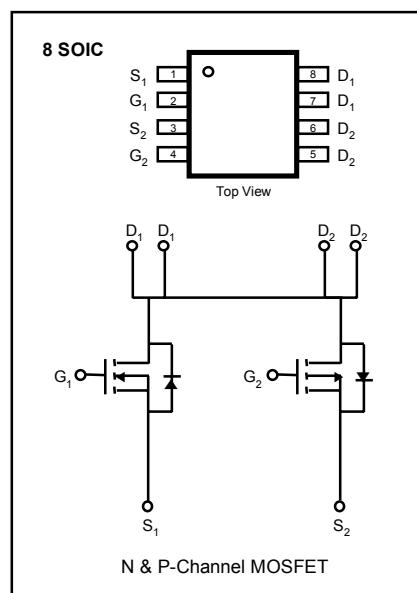


## FEATURES

- Lower  $R_{DS(ON)}$
- Improved Inductive Ruggedness
- Fast Switching Times
- Low Input Capacitance
- Extended Safe Operating Area
- Improved High Temperature Reliability

## Product Summary

SSD2006	BVdss	Rds(on)	$I_D$
N-Channel	30V	0.05Ω	5.4A
P-Channel	-30V	0.10Ω	-3.8A



## Absolute Maximum Ratings

Symbol	Characteristic	N-Channel	P-Channel	Units
$V_{DSS}$	Drain-to-Source Voltage	30	-30	V
$I_D$	Continuous Drain Current $T_A=25^\circ C$	5.4	-3.8	A
	Continuous Drain Current $T_A=70^\circ C$	4.3	-3.0	
$I_{DM}$	Drain Current-Pulsed	14.0	-14.0	A
$V_{GS}$	Gate-to-Source Voltage	$\pm 20$	$\pm 20$	V
$P_D$	Total Power Dissipation ( $T_A=25^\circ C$ )	2.4	W	
	( $T_A=70^\circ C$ )	1.5		
$T_J, T_{STG}$	Operating and Junction Storage Temperature Range	-55 to +150		°C

## Thermal Resistance

Symbol	Characteristic	Typ.	Max.	Units
$R_{\theta JA}$	Junction-to-Ambient	--	52	°C/W

## ( N-Channel )

Electrical Characteristics ( $T_C=25^\circ\text{C}$  unless otherwise specified)

Symbol	Characteristic	Min.	Typ.	Max.	Units	Test Condition
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	30	--	--	V	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	1.0	--	--	V	$\text{V}_{\text{DS}}=5\text{V}, \text{I}_D=250\mu\text{A}$
$\text{I}_{\text{GSS}}$	Gate-Source Leakage , Forward	--	--	100	nA	$\text{V}_{\text{GS}}=20\text{V}$
	Gate-Source Leakage , Reverse	--	--	-100	nA	$\text{V}_{\text{GS}}=-20\text{V}$
$\text{I}_{\text{DSS}}$	Drain-to-Source Leakage Current	--	--	1.0	$\mu\text{A}$	$\text{V}_{\text{DS}}=24\text{V}$
		--	--	5.0		$\text{V}_{\text{DS}}=15\text{V}, T_C=70^\circ\text{C}$
$\text{I}_{\text{DON}}$	On-State Drain-Source Current	14	--	--	A	$\text{V}_{\text{DS}}=5\text{V}, \text{V}_{\text{GS}}=10\text{V}$
$\text{R}_{\text{DS(on)}}$	Static Drain-Source On-State Resistance <sup>(2)</sup>	--	0.031	0.05	$\Omega$	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=3.5\text{A}$
		--	0.042	0.08		$\text{V}_{\text{GS}}=4.5\text{V}, \text{I}_D=2.5\text{A}$
$\text{g}_{\text{fs}}$	Forward Transconductance <sup>(2)</sup>	--	8.0	--	$\text{S}$	$\text{V}_{\text{DS}}=15\text{V}, \text{I}_D=3.5\text{A}$
$\text{t}_{\text{d(on)}}$	Turn-On Delay Time	--	16	30	ns	$\text{V}_{\text{DD}}=15\text{V}, \text{I}_D=1.0\text{A}, \text{R}_0=6.0\Omega,$
$\text{t}_r$	Rise Time	--	18	40		
$\text{t}_{\text{d(off)}}$	Turn-Off Delay Time	--	38	50		
$\text{t}_f$	Fall Time	--	24	50		
$\text{Q}_g$	Total Gate Charge	--	18	35	nC	$\text{V}_{\text{DS}}=10\text{V}, \text{V}_{\text{GS}}=10\text{V}, \text{I}_D=3.5\text{A}$
$\text{Q}_{\text{gs}}$	Gate-Source Charge	--	3.5	--		
$\text{Q}_{\text{gd}}$	Gate-Drain( " Miller " ) Charge	--	3.5	--		

## Source-Drain Diode Ratings and Characteristics

Symbol	Characteristic	Min.	Typ.	Max.	Units	Test Condition
$\text{I}_s$	Continuous Source Current (Body Diode)	--	--	1.7	A	Modified MOSFET Symbol Showing the Integral Reverse P-N Junction Rectifier 
$\text{V}_{\text{SD}}$	Diode Forward Voltage <sup>(2)</sup>	--	--	1.2	V	$\text{T}_A=25^\circ\text{C}, \text{I}_s=1.7\text{A}, \text{V}_{\text{GS}}=0\text{V}$
$\text{t}_{\text{rr}}$	Reverse Recovery Time <sup>(2)</sup>	--	70	120	ns	$\text{T}_A=25^\circ\text{C}, \text{I}_F=3.5\text{A}, \text{di}_F/\text{dt}=100\text{A}/\mu\text{s}$

## Notes :

<sup>(1)</sup> Repetitive Rating : Pulse Width Limited by Maximum Junction Temperature<sup>(2)</sup> Pulse Test : Pulse Width = 250 $\mu\text{s}$ , Duty Cycle  $\leq 2\%$ <sup>(3)</sup> Essentially Independent of Operating Temperature

( P-Channel )

**Electrical Characteristics** ( $T_C=25^\circ\text{C}$  unless otherwise specified)

Symbol	Characteristic	Min.	Typ.	Max.	Units	Test Condition
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	-30	--	--	V	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=-250\mu\text{A}$
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	-1.0	--	--	V	$\text{V}_{\text{DS}} = -5\text{V}, \text{I}_D=-250\mu\text{A}$
$\text{I}_{\text{GSS}}$	Gate-Source Leakage , Forward	--	--	-100	nA	$\text{V}_{\text{GS}}=-20\text{V}$
	Gate-Source Leakage , Reverse	--	--	100	nA	$\text{V}_{\text{GS}}=20\text{V}$
$\text{I}_{\text{DSS}}$	Drain-to-Source Leakage Current	--	--	-1.0	$\mu\text{A}$	$\text{V}_{\text{DS}}=-24\text{V}$
		--	--	-5.0		$\text{V}_{\text{DS}}=-15\text{V}, T_C=70^\circ\text{C}$
$\text{I}_{\text{DON}}$	On-State Drain-Source Current	-14	--	--	A	$\text{V}_{\text{DS}}=-5\text{V}, \text{V}_{\text{GS}}=-10\text{V}$
$\text{R}_{\text{DS(on)}}$	Static Drain-Source	--	0.08	0.1	$\Omega$	$\text{V}_{\text{GS}}=-10\text{V}, \text{I}_D=-3.5\text{A}$
	On-State Resistance <sup>(2)</sup>	--	0.11	0.16		$\text{V}_{\text{GS}}=-4.5\text{V}, \text{I}_D=-2.0\text{A}$
$\text{g}_{\text{fs}}$	Forward Transconductance <sup>(2)</sup>	--	5.0	--	$\text{S}$	$\text{V}_{\text{DS}}=-15\text{V}, \text{I}_D=-3.5\text{A}$
$\text{t}_{\text{d(on)}}$	Turn-On Delay Time	--	17	30	ns	$\text{V}_{\text{DD}}=-15\text{V}, \text{I}_D=-1.0\text{A}, \text{R}_0=6.0\Omega,$ <sup>(2)(3)</sup>
$\text{t}_r$	Rise Time	--	17	40		
$\text{t}_{\text{d(off)}}$	Turn-Off Delay Time	--	33	50		
$\text{t}_f$	Fall Time	--	19	50		
$\text{Q}_g$	Total Gate Charge	--	17	35	nC	$\text{V}_{\text{DS}}=-10\text{V}, \text{V}_{\text{GS}}=-10\text{V}, \text{I}_D=-3.5\text{A}$ <sup>(2)(3)</sup>
$\text{Q}_{\text{gs}}$	Gate-Source Charge	--	3.6	--		
$\text{Q}_{\text{gd}}$	Gate-Drain( " Miller " ) Charge	--	4.3	--		

**Source-Drain Diode Ratings and Characteristics**

Symbol	Characteristic	Min.	Typ.	Max.	Units	Test Condition
$\text{I}_s$	Continuous Source Current (Body Diode)	--	--	-1.7	A	Modified MOSFET Symbol Showing the Integral Reverse P-N Junction Rectifier
$\text{V}_{\text{SD}}$	Diode Forward Voltage <sup>(2)</sup>	--	--	-1.2	V	$T_A=25^\circ\text{C}, \text{I}_s=-1.7\text{A}, \text{V}_{\text{GS}}=0\text{V}$
$\text{t}_{\text{rr}}$	Reverse Recovery Time <sup>(2)</sup>	--	40	100	ns	$T_A=25^\circ\text{C}, \text{I}_F=-3.5\text{A}, \text{di}_F/\text{dt}=100\text{A}/\mu\text{s}$

**Notes :**

<sup>(1)</sup> Repetitive Rating : Pulse Width Limited by Maximum Junction Temperature

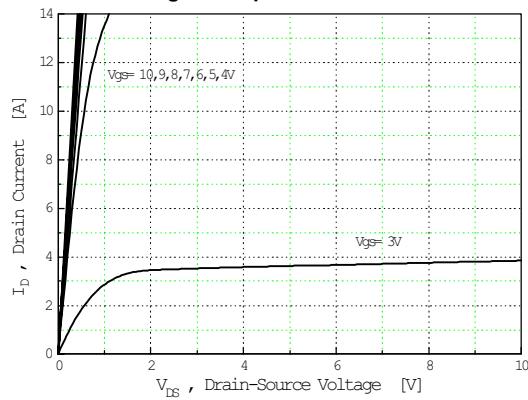
<sup>(2)</sup> Pulse Test : Pulse Width = 250 $\mu\text{s}$ , Duty Cycle  $\leq 2\%$

<sup>(3)</sup> Essentially Independent of Operating Temperature

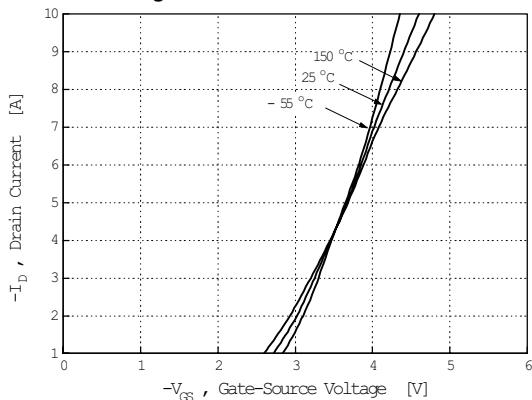


## ( N-Channel )

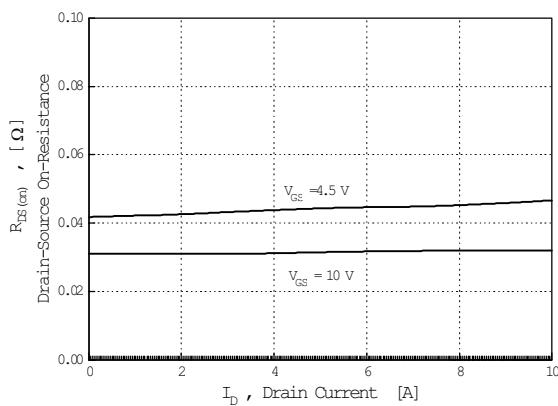
**Fig 1. Output Characteristics**



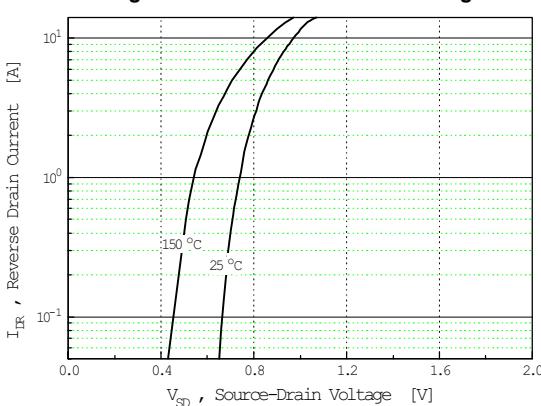
**Fig 2. Transfer Characteristics**



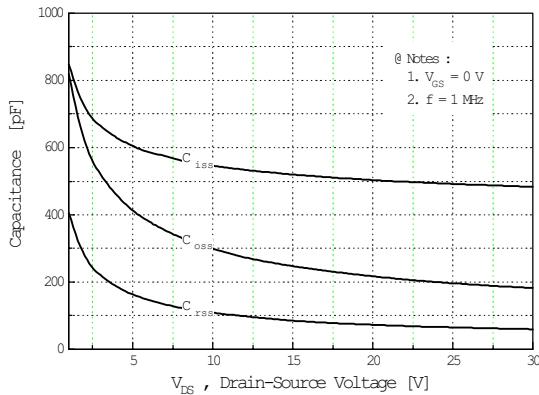
**Fig 3. On-Resistance vs. Drain Current**



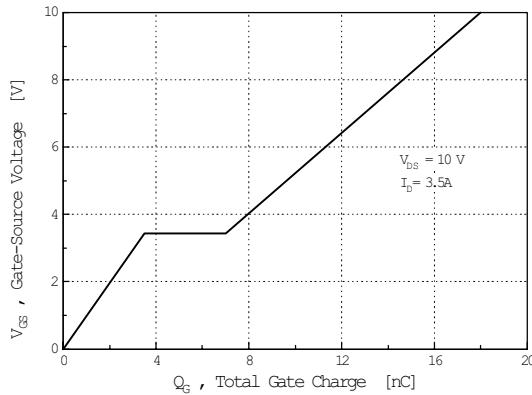
**Fig 4. Source-Drain Forward Voltage**



**Fig 5. Capacitance vs. Drain-Source Voltage**

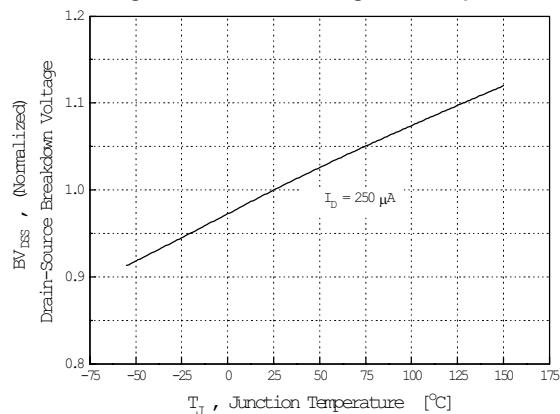


**Fig 6. Gate Charge vs. Gate-Source Voltage**

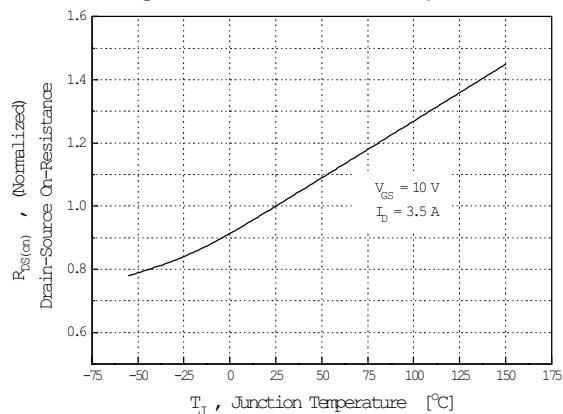


## ( N-Channel )

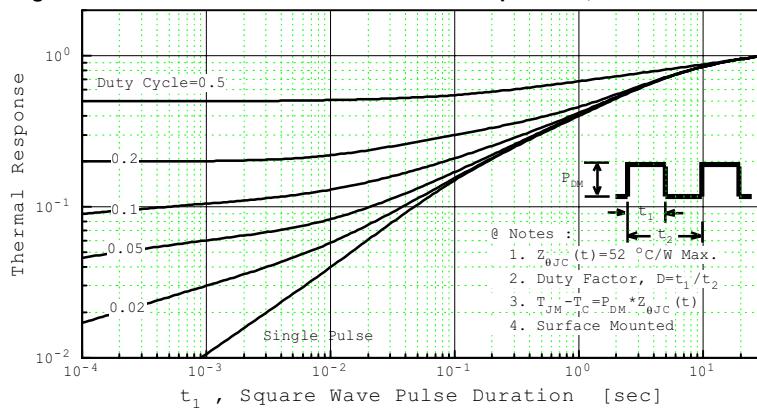
**Fig 7. Breakdown Voltage vs. Temperature**



**Fig 8. On-Resistance vs. Temperature**

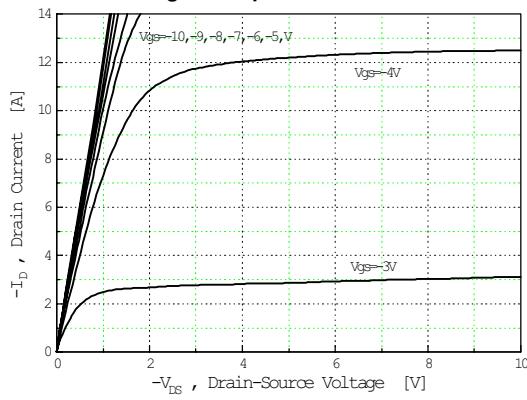


**Fig 9. Normalized Effective Transient Thermal Impedance, Junction-to-Ambient**

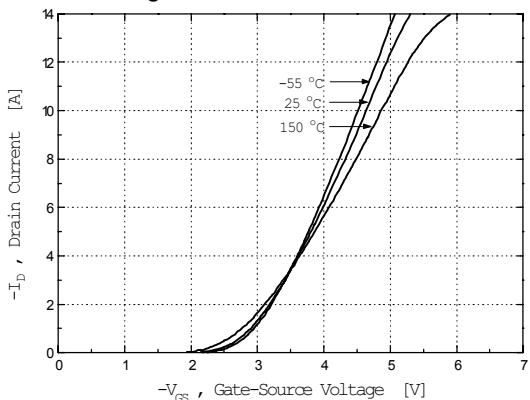


## ( P-Channel )

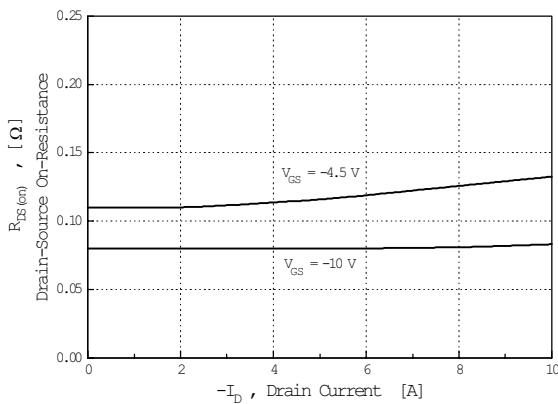
**Fig 1. Output Characteristics**



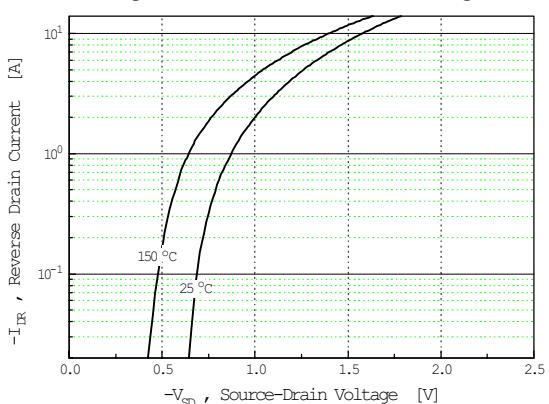
**Fig 2. Transfer Characteristics**



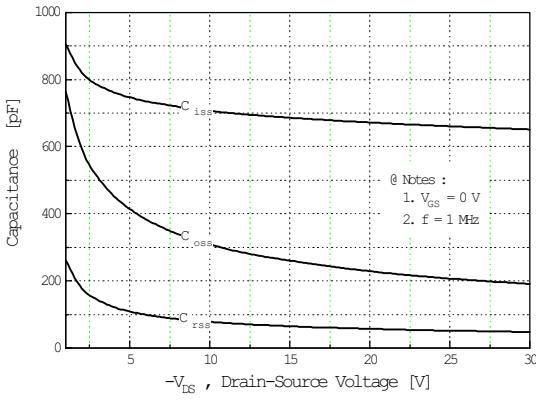
**Fig 3. On-Resistance vs. Drain Current**



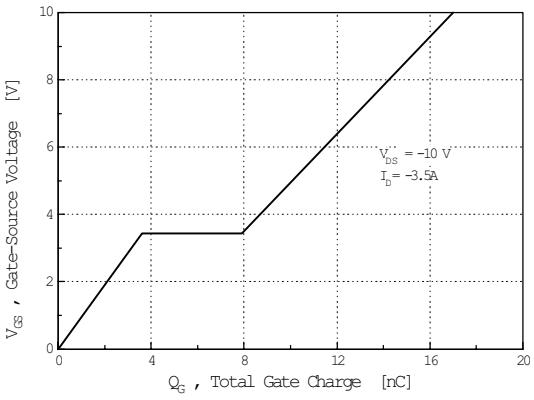
**Fig 4. Source-Drain Forward Voltage**



**Fig 5. Capacitance vs. Drain-Source Voltage**



**Fig 6. Gate Charge vs. Gate-Source Voltage**



( P-Channel )

Fig 7. Breakdown Voltage vs. Temperature

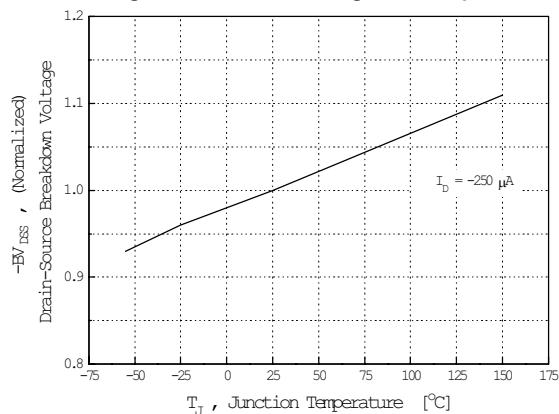


Fig 8. On-Resistance vs. Temperature

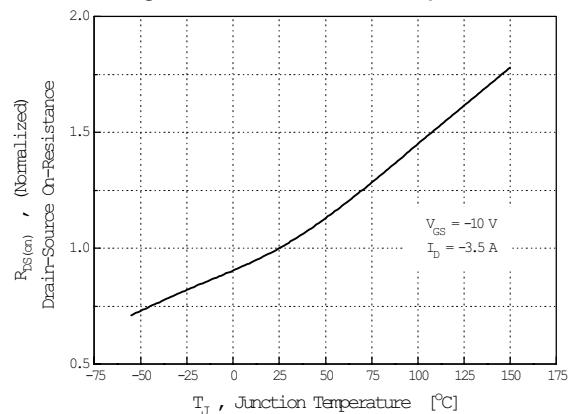


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