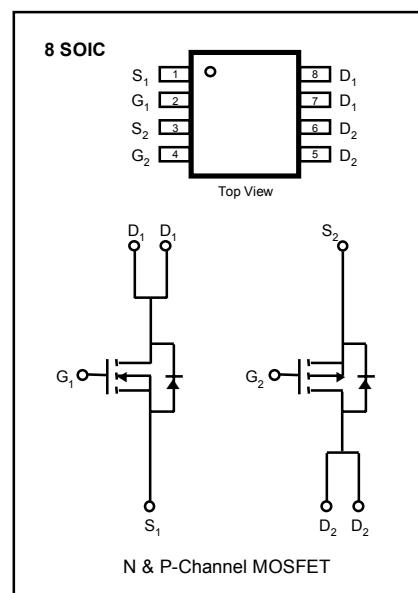


FEATURES

- Lower $R_{DS(ON)}$
- Improved Inductive Ruggedness
- Fast Switching Times
- Low Input Capacitance
- Extended Safe Operating Area
- Improved High Temperature Reliability

Product Summary

SSD2004	BVdss	Rds(on)	I_D
N-Channel	20V	0.125Ω	3.0A
P-Channel	-20V	0.20Ω	-2.5A



Absolute Maximum Ratings

Symbol	Characteristic	N-Channel	P-Channel	Units
V_{DSS}	Drain-to-Source Voltage	20	-20	V
I_D	Continuous Drain Current $T_A=25^\circ C$	3.0	-2.5	A
	Continuous Drain Current $T_A=70^\circ C$	2.5	-2.0	
I_{DM}	Drain Current-Pulsed	10.0	-10.0	A
V_{GS}	Gate-to-Source Voltage	± 20	± 20	V
P_D	Total Power Dissipation ($T_A=25^\circ C$)	2.0		W
	($T_A=70^\circ C$)	1.3		
T_J, T_{STG}	Operating and Junction Storage Temperature Range	-55 to +150		°C

Thermal Resistance

Symbol	Characteristic	Typ.	Max.	Units
$R_{\theta JA}$	Junction-to-Ambient	--	62.5	°C/W

(N-Channel)

Electrical Characteristics ($T_C=25^\circ\text{C}$ unless otherwise specified)

Symbol	Characteristic	Min.	Typ.	Max.	Units	Test Condition
BV_{DSS}	Drain-Source Breakdown Voltage	20	--	--	V	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	1.0	--	--	V	$\text{V}_{\text{DS}}=5\text{V}, \text{I}_D=250\mu\text{A}$
I_{GSS}	Gate-Source Leakage , Forward	--	--	100	nA	$\text{V}_{\text{GS}}=20\text{V}$
	Gate-Source Leakage , Reverse	--	--	-100	nA	$\text{V}_{\text{GS}}=-20\text{V}$
I_{DSS}	Drain-to-Source Leakage Current	--	--	2.0	μA	$\text{V}_{\text{DS}}=16\text{V}$
		--	--	25		$\text{V}_{\text{DS}}=16\text{V}, T_C=55^\circ\text{C}$
I_{DON}	On-State Drain-Source Current	10	--	--	A	$\text{V}_{\text{DS}}=5\text{V}, \text{V}_{\text{GS}}=10\text{V}$
$\text{R}_{\text{DS(on)}}$	Static Drain-Source On-State Resistance ⁽²⁾	--	0.031	0.125	Ω	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=1.0\text{A}$
		--	0.042	0.25		$\text{V}_{\text{GS}}=4.5\text{V}, \text{I}_D=0.5\text{A}$
g_{fs}	Forward Transconductance ⁽²⁾	--	8.0	--	S	$\text{V}_{\text{DS}}=15\text{V}, \text{I}_D=3.0\text{A}$
$\text{t}_{\text{d(on)}}$	Turn-On Delay Time	--	13	15	ns	$\text{V}_{\text{DD}}=20\text{V}, \text{I}_D=1.0\text{A}, \text{R}_0=6.0\Omega,$
t_r	Rise Time	--	16	20		
$\text{t}_{\text{d(off)}}$	Turn-Off Delay Time	--	38	50		
t_f	Fall Time	--	24	50		
Q_g	Total Gate Charge	--	18	25	nC	$\text{V}_{\text{DS}}=10\text{V}, \text{V}_{\text{GS}}=10\text{V}, \text{I}_D=2.3\text{A}$
Q_{gs}	Gate-Source Charge	--	2.4	--		
Q_{gd}	Gate-Drain(" Miller ") Charge	--	3.8	--		

Source-Drain Diode Ratings and Characteristics

Symbol	Characteristic	Min.	Typ.	Max.	Units	Test Condition
I_s	Continuous Source Current (Body Diode)	--	--	1.25	A	Modified MOSFET Symbol Showing the Integral Reverse P-N Junction Rectifier 
V_{SD}	Diode Forward Voltage ⁽²⁾	--	--	1.2	V	$\text{T}_A=25^\circ\text{C}, \text{I}_s=1.25\text{A}, \text{V}_{\text{GS}}=0\text{V}$
t_{rr}	Reverse Recovery Time ⁽²⁾	--	100	--	ns	$\text{T}_A=25^\circ\text{C}, \text{I}_F=1.25\text{A}, \text{di}_F/\text{dt}=100\text{A}/\mu\text{s}$

Notes :

⁽¹⁾ Repetitive Rating : Pulse Width Limited by Maximum Junction Temperature⁽²⁾ Pulse Test : Pulse Width = 250 μs , Duty Cycle $\leq 2\%$ ⁽³⁾ Essentially Independent of Operating Temperature

(P-Channel)

Electrical Characteristics ($T_C=25^\circ\text{C}$ unless otherwise specified)

Symbol	Characteristic	Min.	Typ.	Max.	Units	Test Condition
BV_{DSS}	Drain-Source Breakdown Voltage	-20	--	--	V	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=-250\mu\text{A}$
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	-1.0	--	--	V	$\text{V}_{\text{DS}} = -5\text{V}, \text{I}_D=-250\mu\text{A}$
I_{GSS}	Gate-Source Leakage , Forward	--	--	-100	nA	$\text{V}_{\text{GS}}=-20\text{V}$
	Gate-Source Leakage , Reverse	--	--	100	nA	$\text{V}_{\text{GS}}=20\text{V}$
I_{DSS}	Drain-to-Source Leakage Current	--	--	-2.0	μA	$\text{V}_{\text{DS}}=-16\text{V}$
		--	--	-25		$\text{V}_{\text{DS}}=-16\text{V}, T_C=55^\circ\text{C}$
I_{DON}	On-State Drain-Source Current	-2.3	--	--	A	$\text{V}_{\text{DS}}=-5\text{V}, \text{V}_{\text{GS}}=-10\text{V}$
$\text{R}_{\text{DS(on)}}$	Static Drain-Source	--	0.08	0.25	Ω	$\text{V}_{\text{GS}}=-10\text{V}, \text{I}_D=-1.0\text{A}$
	On-State Resistance ⁽²⁾	--	0.11	0.35		$\text{V}_{\text{GS}}=-4.5\text{V}, \text{I}_D=-0.5\text{A}$
g_{fs}	Forward Transconductance ⁽²⁾	--	5.0	--	S	$\text{V}_{\text{DS}}=-15\text{V}, \text{I}_D=-3.0\text{A}$
$t_{\text{d(on)}}$	Turn-On Delay Time	--	17	40	ns	$\text{V}_{\text{DD}}=-20\text{V}, \text{I}_D=-1.0\text{A}, \text{R}_0=6.0\Omega,$ ⁽²⁾⁽³⁾
t_r	Rise Time	--	17	40		
$t_{\text{d(off)}}$	Turn-Off Delay Time	--	33	90		
t_f	Fall Time	--	19	50		
Q_g	Total Gate Charge	--	17	25	nC	$\text{V}_{\text{DS}}=-10\text{V}, \text{V}_{\text{GS}}=-10\text{V}, \text{I}_D=-2.3\text{A}$ ⁽²⁾⁽³⁾
Q_{gs}	Gate-Source Charge	--	3.3	--		
Q_{gd}	Gate-Drain(" Miller ") Charge	--	3.6	--		

Source-Drain Diode Ratings and Characteristics

Symbol	Characteristic	Min.	Typ.	Max.	Units	Test Condition
I_s	Continuous Source Current (Body Diode)	--	--	-1.25	A	Modified MOSFET Symbol Showing the Integral Reverse P-N Junction Rectifier
V_{SD}	Diode Forward Voltage ⁽²⁾	--	--	-1.6	V	$T_A=25^\circ\text{C}, \text{I}_s=-1.25\text{A}, \text{V}_{\text{GS}}=0\text{V}$
t_{rr}	Reverse Recovery Time ⁽²⁾	--	100	--	ns	$T_A=25^\circ\text{C}, \text{I}_F=-1.25\text{A}, \text{di}_F/\text{dt}=100\text{A}/\mu\text{s}$

Notes :

① Repetitive Rating : Pulse Width Limited by Maximum Junction Temperature

② Pulse Test : Pulse Width = 250 μs , Duty Cycle $\leq 2\%$

③ Essentially Independent of Operating Temperature



(N-Channel)

Fig 1. Output Characteristics

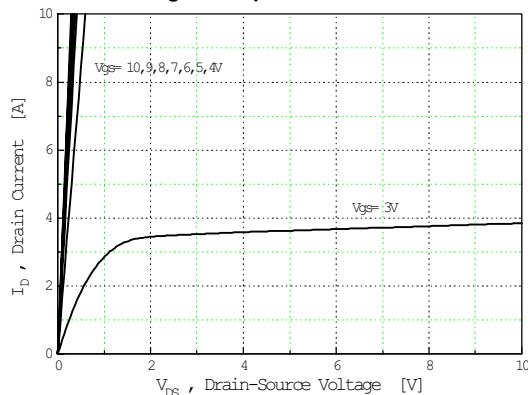


Fig 2. Transfer Characteristics

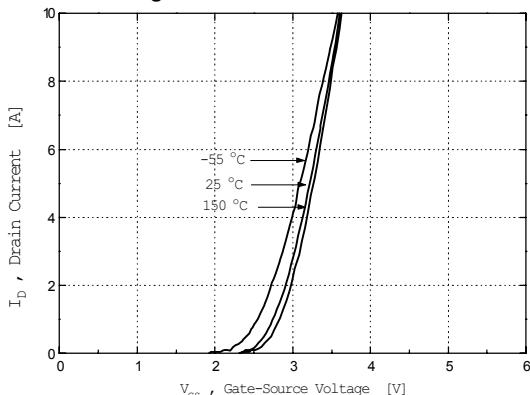


Fig 3. On-Resistance vs. Drain Current

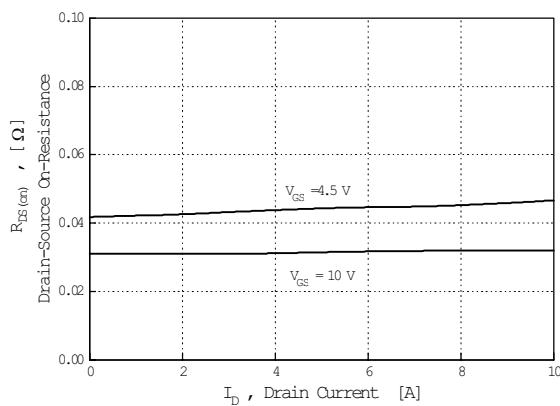


Fig 4. Source-Drain Forward Voltage

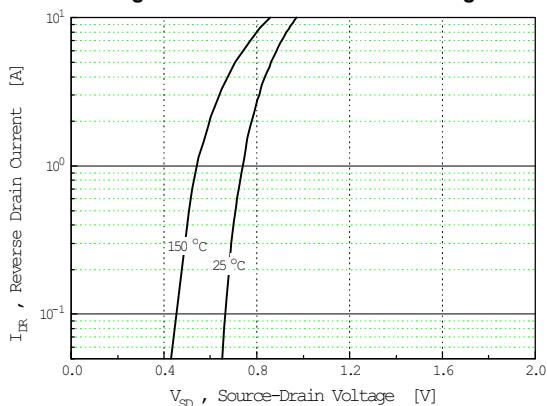


Fig 5. Capacitance vs. Drain-Source Voltage

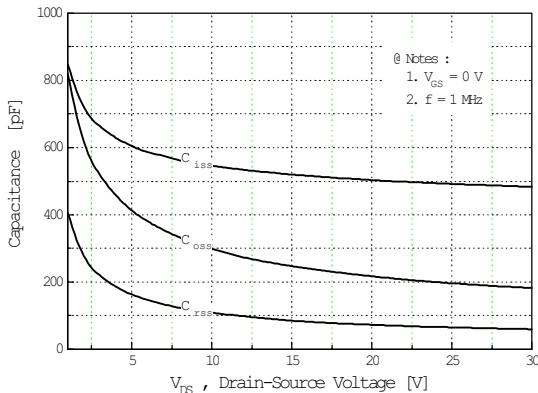
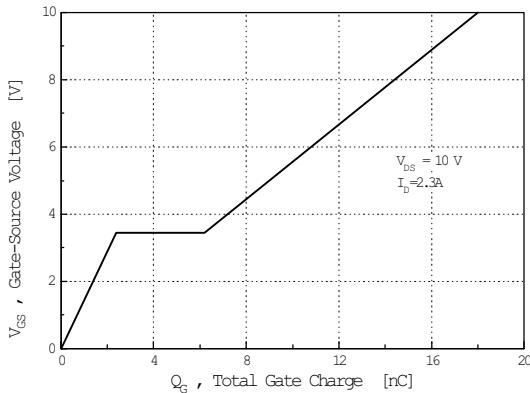


Fig 6. Gate Charge vs. Gate-Source Voltage



(N-Channel)

Fig 7. Breakdown Voltage vs. Temperature

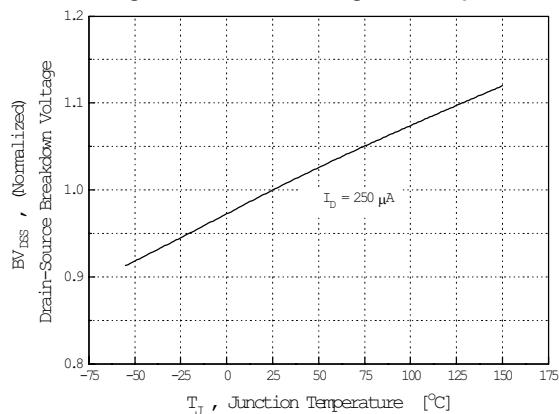


Fig 8. On-Resistance vs. Temperature

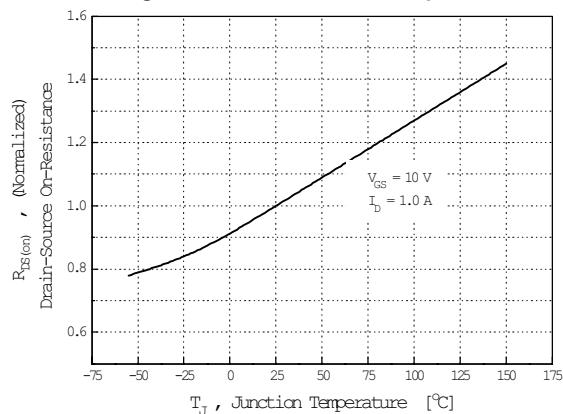
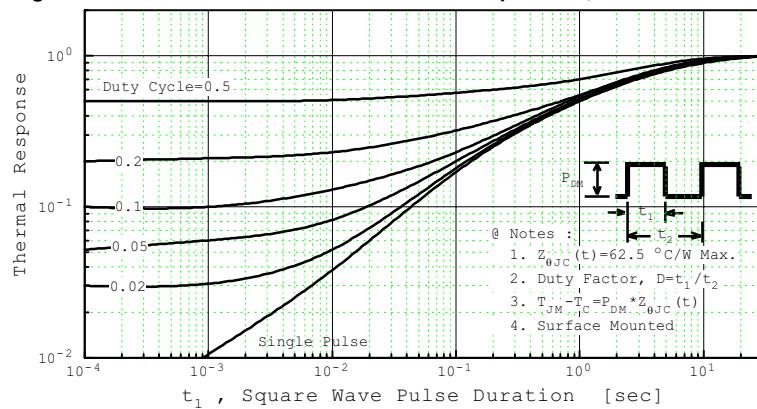


Fig 9. Normalized Effective Transient Thermal Impedance, Junction-to-Ambient



(P-Channel)

Fig 1. Output Characteristics

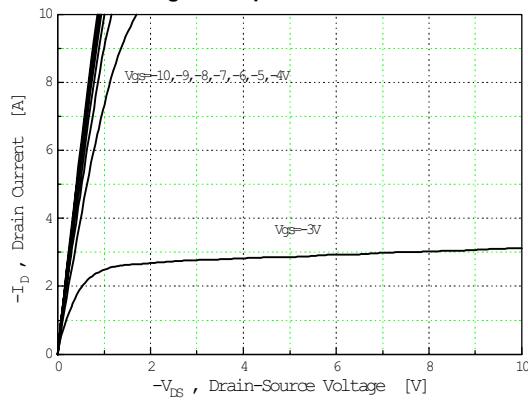


Fig 2. Transfer Characteristics

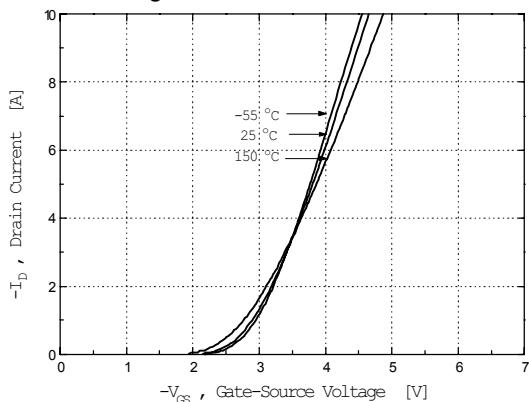


Fig 3. On-Resistance vs. Drain Current

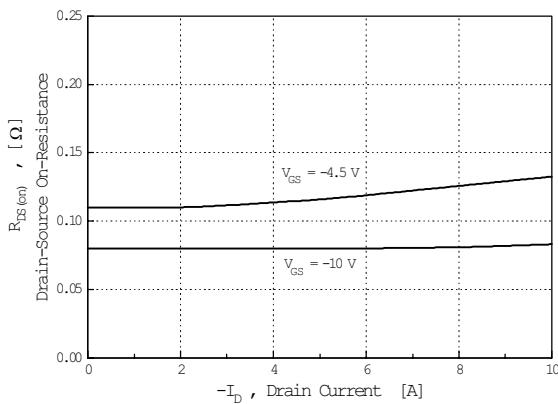


Fig 4. Source-Drain Forward Voltage

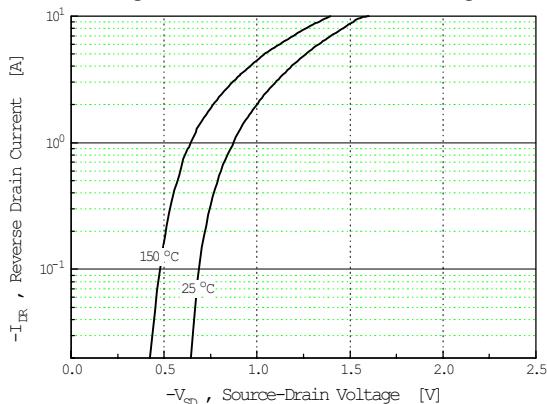


Fig 5. Capacitance vs. Drain-Source Voltage

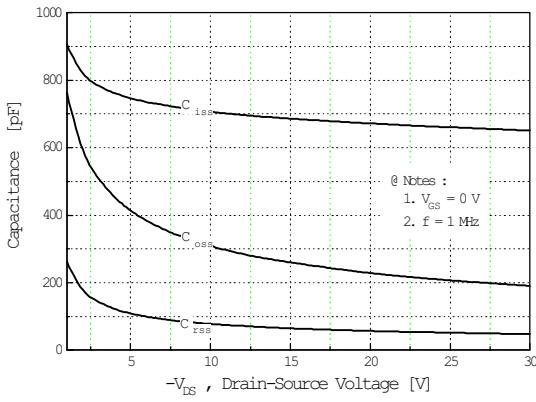
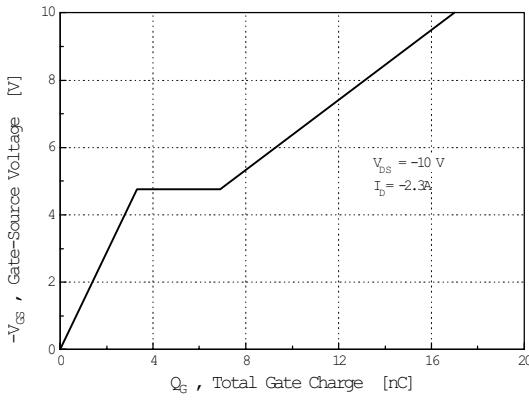


Fig 6. Gate Charge vs. Gate-Source Voltage



(P-Channel)

Fig 7. Breakdown Voltage vs. Temperature

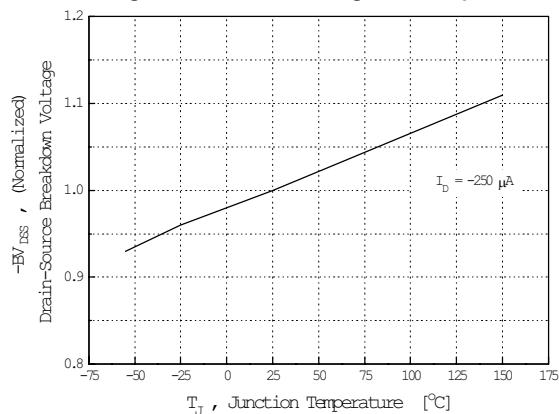


Fig 8. On-Resistance vs. Temperature

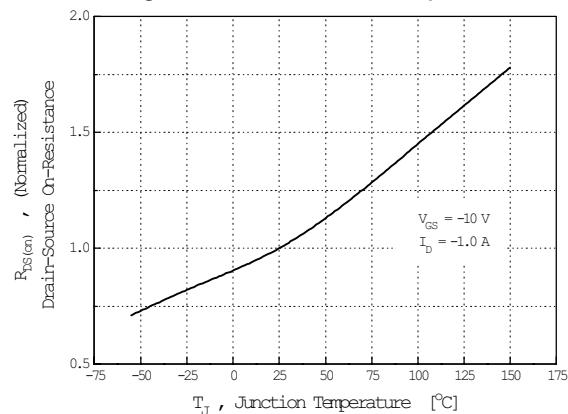


Fig 9. Normalized Effective Transient Thermal Impedance, Junction-to-Ambient

