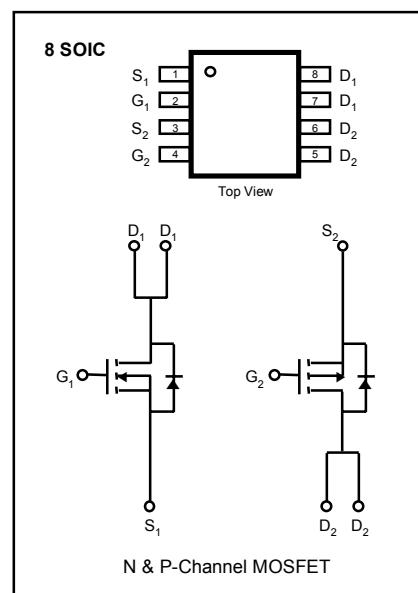


## FEATURES

- Lower  $R_{DS(ON)}$
- Improved Inductive Ruggedness
- Fast Switching Times
- Low Input Capacitance
- Extended Safe Operating Area
- Improved High Temperature Reliability

## Product Summary

SSD2004	BVdss	Rds(on)	$I_D$
N-Channel	20V	0.125Ω	3.0A
P-Channel	-20V	0.20Ω	-2.5A



## Absolute Maximum Ratings

Symbol	Characteristic	N-Channel	P-Channel	Units
$V_{DSS}$	Drain-to-Source Voltage	20	-20	V
$I_D$	Continuous Drain Current $T_A=25^\circ C$	3.0	-2.5	A
	Continuous Drain Current $T_A=70^\circ C$	2.5	-2.0	
$I_{DM}$	Drain Current-Pulsed	10.0	-10.0	A
$V_{GS}$	Gate-to-Source Voltage	$\pm 20$	$\pm 20$	V
$P_D$	Total Power Dissipation ( $T_A=25^\circ C$ )	2.0		W
	( $T_A=70^\circ C$ )	1.3		
$T_J, T_{STG}$	Operating and Junction Storage Temperature Range	-55 to +150		°C

## Thermal Resistance

Symbol	Characteristic	Typ.	Max.	Units
$R_{\theta JA}$	Junction-to-Ambient	--	62.5	°C/W

## ( N-Channel )

Electrical Characteristics ( $T_C=25^\circ\text{C}$  unless otherwise specified)

Symbol	Characteristic	Min.	Typ.	Max.	Units	Test Condition
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	20	--	--	V	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	1.0	--	--	V	$\text{V}_{\text{DS}}=5\text{V}, \text{I}_D=250\mu\text{A}$
$\text{I}_{\text{GSS}}$	Gate-Source Leakage , Forward	--	--	100	nA	$\text{V}_{\text{GS}}=20\text{V}$
	Gate-Source Leakage , Reverse	--	--	-100	nA	$\text{V}_{\text{GS}}=-20\text{V}$
$\text{I}_{\text{DSS}}$	Drain-to-Source Leakage Current	--	--	2.0	$\mu\text{A}$	$\text{V}_{\text{DS}}=16\text{V}$
		--	--	25		$\text{V}_{\text{DS}}=16\text{V}, T_C=55^\circ\text{C}$
$\text{I}_{\text{DON}}$	On-State Drain-Source Current	10	--	--	A	$\text{V}_{\text{DS}}=5\text{V}, \text{V}_{\text{GS}}=10\text{V}$
$\text{R}_{\text{DS(on)}}$	Static Drain-Source On-State Resistance <sup>(2)</sup>	--	0.031	0.125	$\Omega$	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=1.0\text{A}$
		--	0.042	0.25		$\text{V}_{\text{GS}}=4.5\text{V}, \text{I}_D=0.5\text{A}$
$\text{g}_{\text{fs}}$	Forward Transconductance <sup>(2)</sup>	--	8.0	--	$\text{S}$	$\text{V}_{\text{DS}}=15\text{V}, \text{I}_D=3.0\text{A}$
$\text{t}_{\text{d(on)}}$	Turn-On Delay Time	--	13	15	ns	$\text{V}_{\text{DD}}=20\text{V}, \text{I}_D=1.0\text{A}, \text{R}_0=6.0\Omega,$
$\text{t}_r$	Rise Time	--	16	20		
$\text{t}_{\text{d(off)}}$	Turn-Off Delay Time	--	38	50		
$\text{t}_f$	Fall Time	--	24	50		
$\text{Q}_g$	Total Gate Charge	--	18	25	nC	$\text{V}_{\text{DS}}=10\text{V}, \text{V}_{\text{GS}}=10\text{V}, \text{I}_D=2.3\text{A}$
$\text{Q}_{\text{gs}}$	Gate-Source Charge	--	2.4	--		
$\text{Q}_{\text{gd}}$	Gate-Drain( " Miller " ) Charge	--	3.8	--		

## Source-Drain Diode Ratings and Characteristics

Symbol	Characteristic	Min.	Typ.	Max.	Units	Test Condition
$\text{I}_s$	Continuous Source Current (Body Diode)	--	--	1.25	A	Modified MOSFET Symbol Showing the Integral Reverse P-N Junction Rectifier 
$\text{V}_{\text{SD}}$	Diode Forward Voltage <sup>(2)</sup>	--	--	1.2	V	$\text{T}_A=25^\circ\text{C}, \text{I}_s=1.25\text{A}, \text{V}_{\text{GS}}=0\text{V}$
$\text{t}_{\text{rr}}$	Reverse Recovery Time <sup>(2)</sup>	--	100	--	ns	$\text{T}_A=25^\circ\text{C}, \text{I}_F=1.25\text{A}, \text{di}_F/\text{dt}=100\text{A}/\mu\text{s}$

## Notes :

<sup>(1)</sup> Repetitive Rating : Pulse Width Limited by Maximum Junction Temperature<sup>(2)</sup> Pulse Test : Pulse Width = 250 $\mu\text{s}$ , Duty Cycle  $\leq 2\%$ <sup>(3)</sup> Essentially Independent of Operating Temperature

( P-Channel )

**Electrical Characteristics** ( $T_C=25^\circ\text{C}$  unless otherwise specified)

Symbol	Characteristic	Min.	Typ.	Max.	Units	Test Condition
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	-20	--	--	V	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=-250\mu\text{A}$
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	-1.0	--	--	V	$\text{V}_{\text{DS}} = -5\text{V}, \text{I}_D=-250\mu\text{A}$
$\text{I}_{\text{GSS}}$	Gate-Source Leakage , Forward	--	--	-100	nA	$\text{V}_{\text{GS}}=-20\text{V}$
	Gate-Source Leakage , Reverse	--	--	100	nA	$\text{V}_{\text{GS}}=20\text{V}$
$\text{I}_{\text{DSS}}$	Drain-to-Source Leakage Current	--	--	-2.0	$\mu\text{A}$	$\text{V}_{\text{DS}}=-16\text{V}$
		--	--	-25		$\text{V}_{\text{DS}}=-16\text{V}, T_C=55^\circ\text{C}$
$\text{I}_{\text{DON}}$	On-State Drain-Source Current	-2.3	--	--	A	$\text{V}_{\text{DS}}=-5\text{V}, \text{V}_{\text{GS}}=-10\text{V}$
$\text{R}_{\text{DS(on)}}$	Static Drain-Source	--	0.08	0.25	$\Omega$	$\text{V}_{\text{GS}}=-10\text{V}, \text{I}_D=-1.0\text{A}$
	On-State Resistance <sup>(2)</sup>	--	0.11	0.35		$\text{V}_{\text{GS}}=-4.5\text{V}, \text{I}_D=-0.5\text{A}$
$\text{g}_{\text{fs}}$	Forward Transconductance <sup>(2)</sup>	--	5.0	--	$\text{S}$	$\text{V}_{\text{DS}}=-15\text{V}, \text{I}_D=-3.0\text{A}$
$t_{\text{d(on)}}$	Turn-On Delay Time	--	17	40	ns	$\text{V}_{\text{DD}}=-20\text{V}, \text{I}_D=-1.0\text{A}, \text{R}_0=6.0\Omega,$ <sup>(2)(3)</sup>
$t_r$	Rise Time	--	17	40		
$t_{\text{d(off)}}$	Turn-Off Delay Time	--	33	90		
$t_f$	Fall Time	--	19	50		
$\text{Q}_g$	Total Gate Charge	--	17	25	nC	$\text{V}_{\text{DS}}=-10\text{V}, \text{V}_{\text{GS}}=-10\text{V}, \text{I}_D=-2.3\text{A}$ <sup>(2)(3)</sup>
$\text{Q}_{\text{gs}}$	Gate-Source Charge	--	3.3	--		
$\text{Q}_{\text{gd}}$	Gate-Drain( " Miller " ) Charge	--	3.6	--		

**Source-Drain Diode Ratings and Characteristics**

Symbol	Characteristic	Min.	Typ.	Max.	Units	Test Condition
$\text{I}_s$	Continuous Source Current (Body Diode)	--	--	-1.25	A	Modified MOSFET Symbol Showing the Integral Reverse P-N Junction Rectifier
$\text{V}_{\text{SD}}$	Diode Forward Voltage <sup>(2)</sup>	--	--	-1.6	V	$T_A=25^\circ\text{C}, \text{I}_s=-1.25\text{A}, \text{V}_{\text{GS}}=0\text{V}$
$t_{\text{rr}}$	Reverse Recovery Time <sup>(2)</sup>	--	100	--	ns	$T_A=25^\circ\text{C}, \text{I}_F=-1.25\text{A}, \text{di}_F/\text{dt}=100\text{A}/\mu\text{s}$

**Notes :**

<sup>(1)</sup> Repetitive Rating : Pulse Width Limited by Maximum Junction Temperature

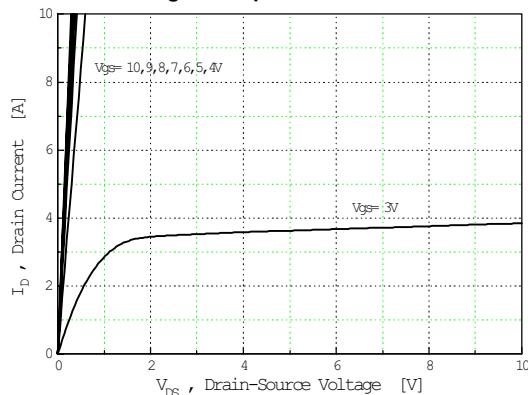
<sup>(2)</sup> Pulse Test : Pulse Width = 250 $\mu\text{s}$ , Duty Cycle  $\leq 2\%$

<sup>(3)</sup> Essentially Independent of Operating Temperature

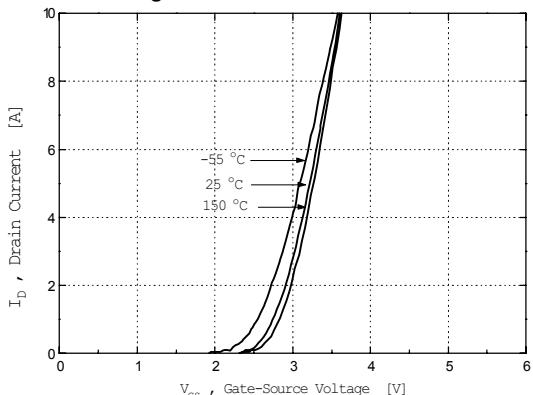


## ( N-Channel )

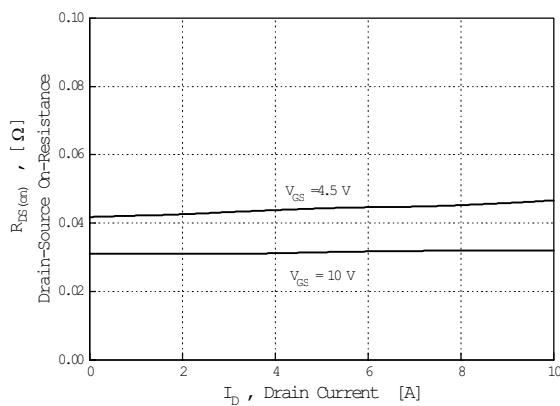
**Fig 1. Output Characteristics**



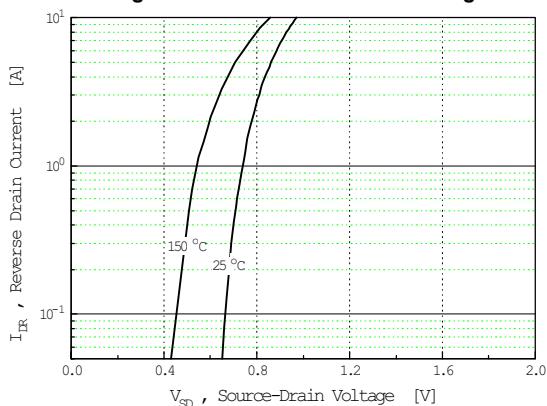
**Fig 2. Transfer Characteristics**



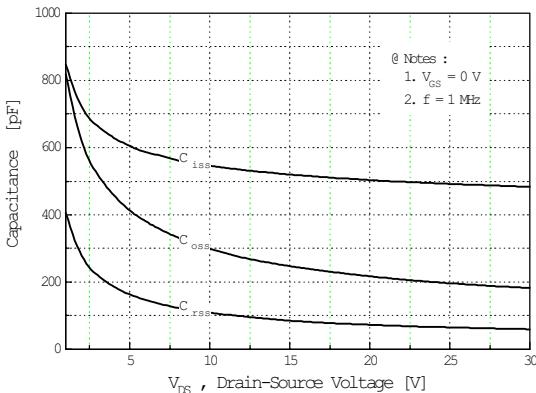
**Fig 3. On-Resistance vs. Drain Current**



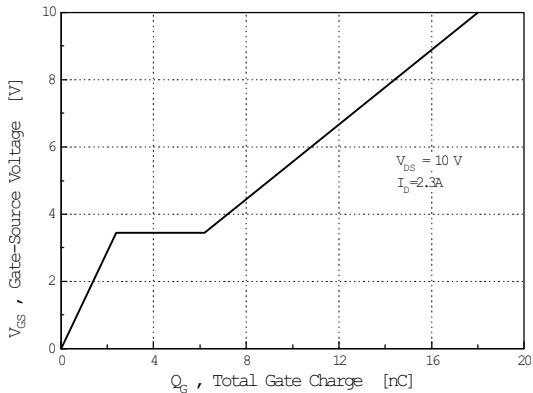
**Fig 4. Source-Drain Forward Voltage**



**Fig 5. Capacitance vs. Drain-Source Voltage**

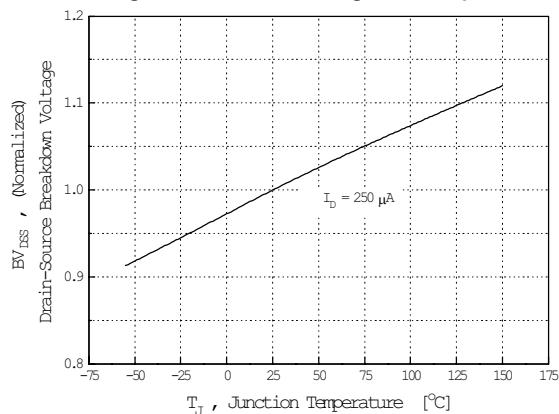


**Fig 6. Gate Charge vs. Gate-Source Voltage**

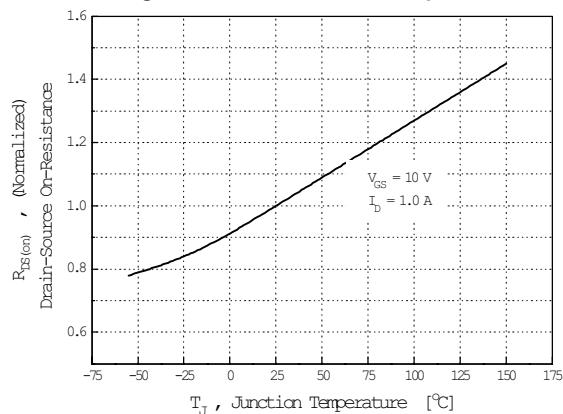


**( N-Channel )**

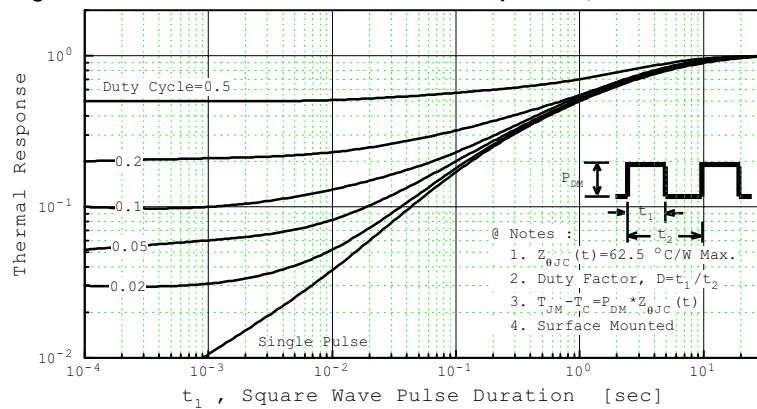
**Fig 7. Breakdown Voltage vs. Temperature**



**Fig 8. On-Resistance vs. Temperature**

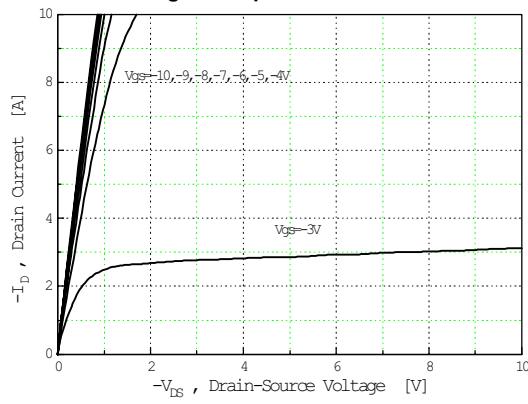


**Fig 9. Normalized Effective Transient Thermal Impedance, Junction-to-Ambient**

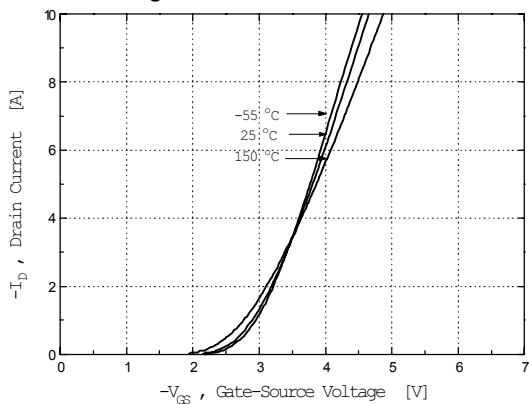


## ( P-Channel )

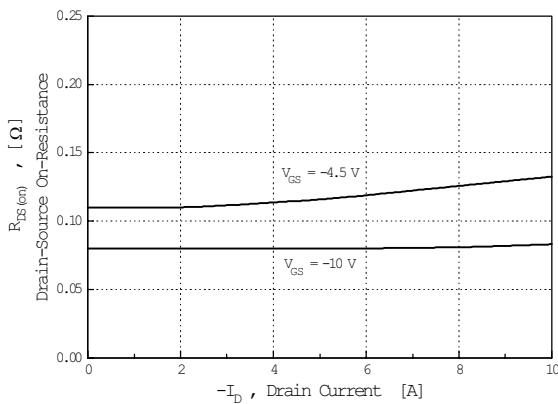
**Fig 1. Output Characteristics**



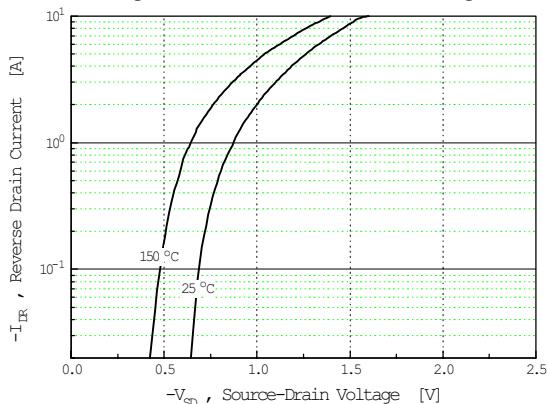
**Fig 2. Transfer Characteristics**



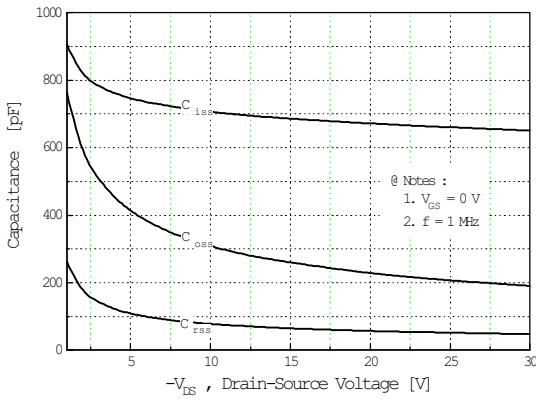
**Fig 3. On-Resistance vs. Drain Current**



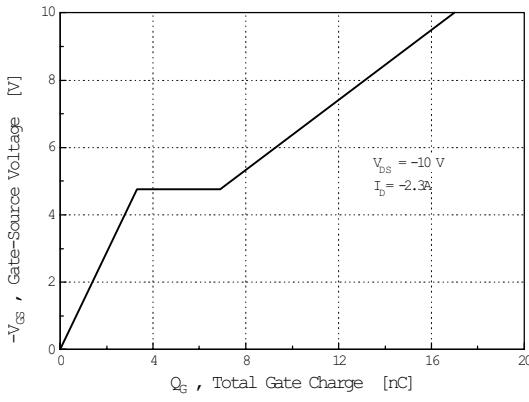
**Fig 4. Source-Drain Forward Voltage**



**Fig 5. Capacitance vs. Drain-Source Voltage**



**Fig 6. Gate Charge vs. Gate-Source Voltage**



( P-Channel )

Fig 7. Breakdown Voltage vs. Temperature

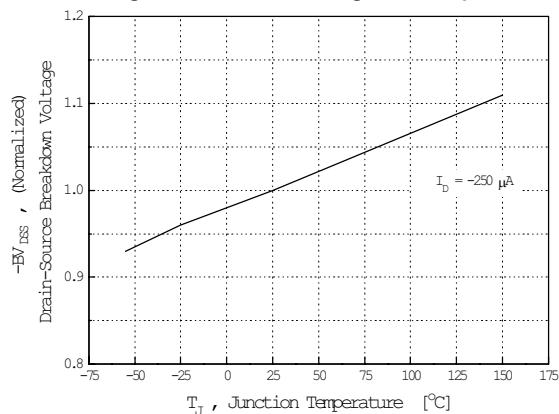


Fig 8. On-Resistance vs. Temperature

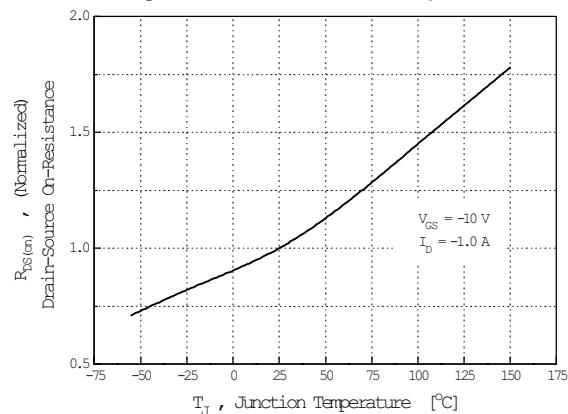


Fig 9. Normalized Effective Transient Thermal Impedance, Junction-to-Ambient

