

SL6140

400MHz WIDEBAND AGC AMPLIFIER

(Supersedes Edition in May 1991 Professional Products IC Handbook)

The SL6140 is an integrated broadband AGC amplifier, designed on an advanced bipolar process. The amplifier provides over 15dB of linear gain into 50W at 400MHz. Gain control is also provided with over 70dB of dynamic range. The SL6140 offers over 45dB of voltage gain with an R_L of 1kW.

FEATURES

- 400MHz Bandwidth ($R_L=50W$)
- High voltage Gain 45dB ($R_L=1kW$)
- 70dB Gain Control Range
- High Output Level at Low Gain
- Surface Mount Plastic Package
- Low Cost

APPLICATIONS

- RF/IF Amplifier
- High Gain Mixers
- Video Amplifiers

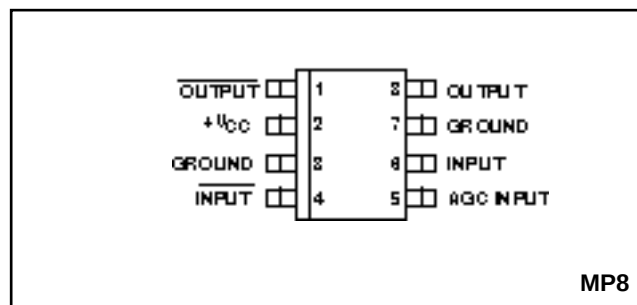


Fig.1 Pin connections top view

ORDERING INFORMATION

SL6140/NA/MP	Industrial temperature range miniature plastic package.
SL6140/NA/MPTC	Tape and Reel

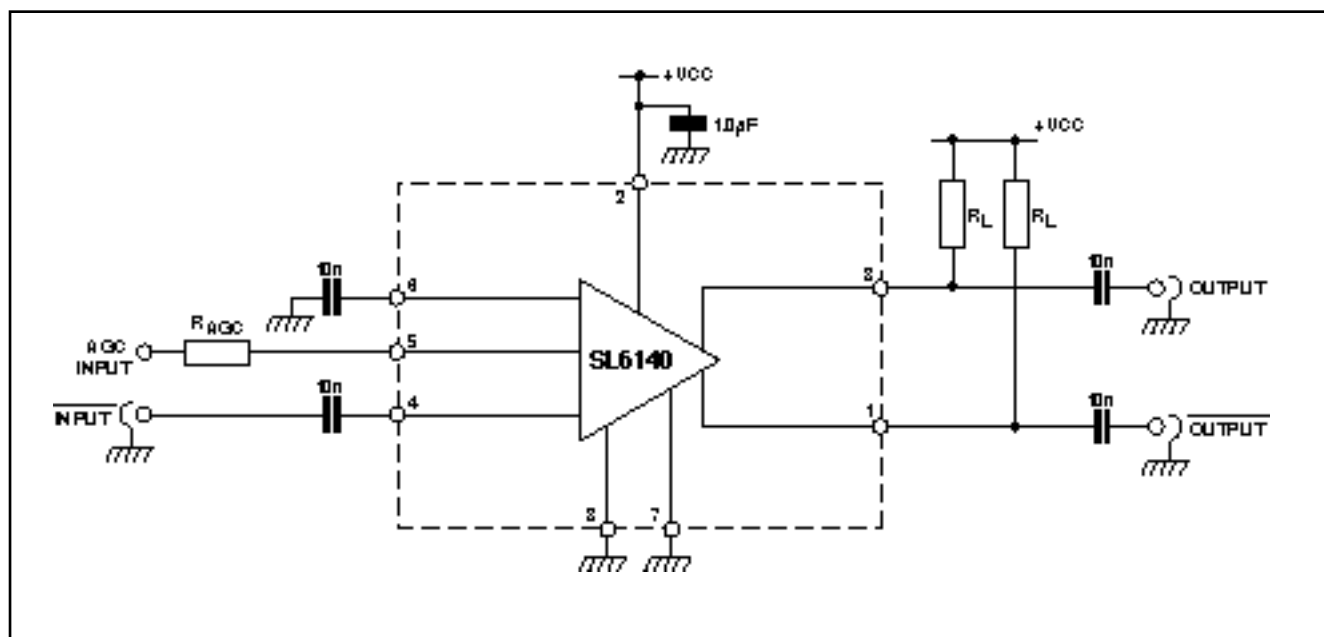


Fig.2 Typical application

SL6140

ELECTRICAL CHARACTERISTICS

These characteristics are guaranteed over the following conditions (unless otherwise stated)

$T_{amb} = 25^{\circ}\text{C}$, $V_{CC} = 12\text{V} \pm 5\%$, $V_{IN} = 1\text{mV}_{RMS}$, Frequency = 6MHz, Load (R_L) = 10KOHms, $R_{AGC} = 22\text{KOHm}$

Characteristic	Pin	Value			Units	Conditions
		Min	Typ	Max		
Supply current	5,6,7		19	23	mA	No input signal
Output stage current	5,6 (sum)	5	7	9	mA	No input signal
Output current matching (magnitude of difference of output currents)	5,6		1.0		mA	See Note 2
AGC range	2	60	75		dB	See Fig. 4 & Note 1 ($V_{AGC} = 0\text{V}$ to 10V)
Voltage gain (single ended)	5,6	40	45		dB	$R_L = 1\text{kW}$ See Fig. 5 & Note 1
			55		dB	Tuned input and output
	5,6		15		dB	$R_L = 50\text{W}$
Bandwidth (-3dB)	5,6		25		MHz	$R_L = 1\text{kW}$ See Fig. 5. See Note 2
			400			$R_L = 50\text{W}$
Maximum output level (single ended)						
0dB AGC	5,6		3.5		V p-p	Note 1
-30dB AGC	5,6		3.5		V p-p	$R_L = 1\text{kW}$. Note 1
Noise figure	5,6		5		dB	Test CCT Fig. 13

Note. 1 Guaranteed but not tested.

DESCRIPTION

The SL6140 (Fig. 3) is a high gain amplifier with an AGC control capable of reducing the gain of the amplifier by over 70dB. The gain is adjustable by applying a voltage to the AGC input via an external resistor (R_{AGC}), the value of which adjusts the curve of gain reduction versus control voltage (see Fig. 4). As the output stage of the amplifier is an open collector the maximum voltage gain is determined by R_L . With load resistance of 1kW the single ended voltage gain is 45dB and with a load resistance of 50W the voltage gain is 15dB ($20\log_{10} V_{OUT}/V_{IN}$). Another parameter that depends on the load resistance is the bandwidth: 25MHz for $R_L = 1\text{kW}$, as compared with 400MHz for $R_L = 50\text{W}$. R_L is chosen to give either the required bandwidth or voltage gain for the circuit.

Figs. 7 through to 10 show the typical S parameters for the device. Figs. 11 and 12 show the typical variation in 3rd order intercept performance with AGC.

In any application, the substrate should be connected to the most negative point in the circuit, usually to the same point as pin 3.

ABSOLUTE MAXIMUM RATINGS

Supply voltage, V_{CC}	+18V
Input voltage (differential)	+5V
AGC supply	V_{CC}
Storage temperature	-55°C to +150°C
Operating temperature range	
SL6140 MP	-40°C to +85°C
	at 200mW
Chip operating temperature	
SL6140 MP	+150°C

THERMAL RESISTANCE

Chip-to-ambient	
SL6140 MP	163°C/W
Chip-to-case	
SL6140 MP	57°C/W

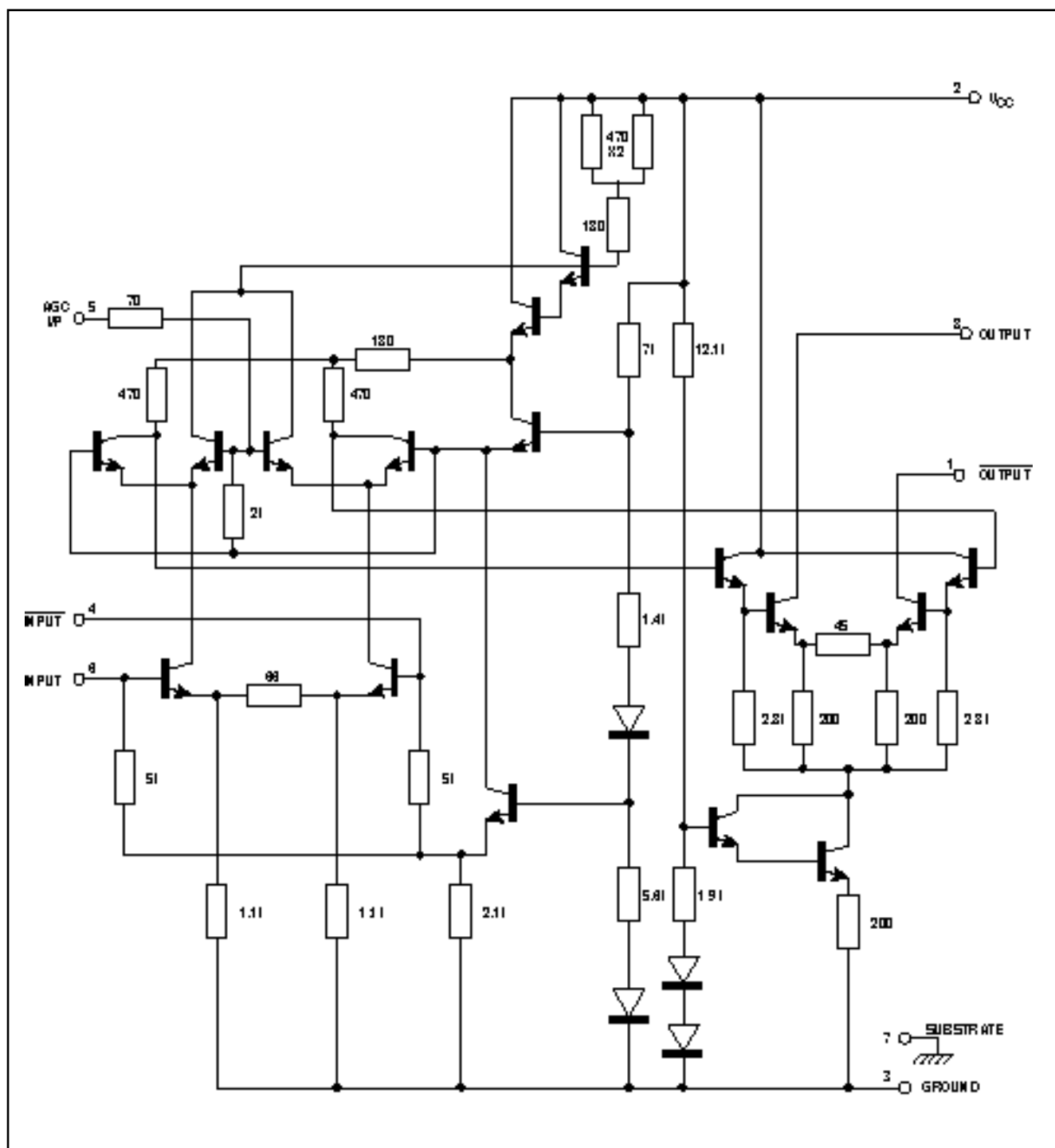


Fig. 3 Full circuit diagram of SL6140

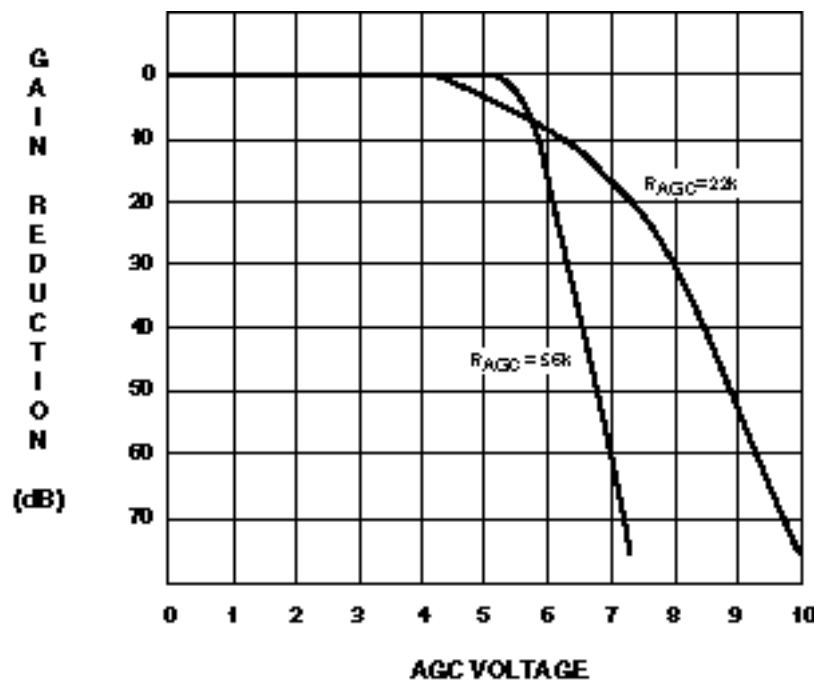


Fig. 4 Gain reduction v. AGC voltage

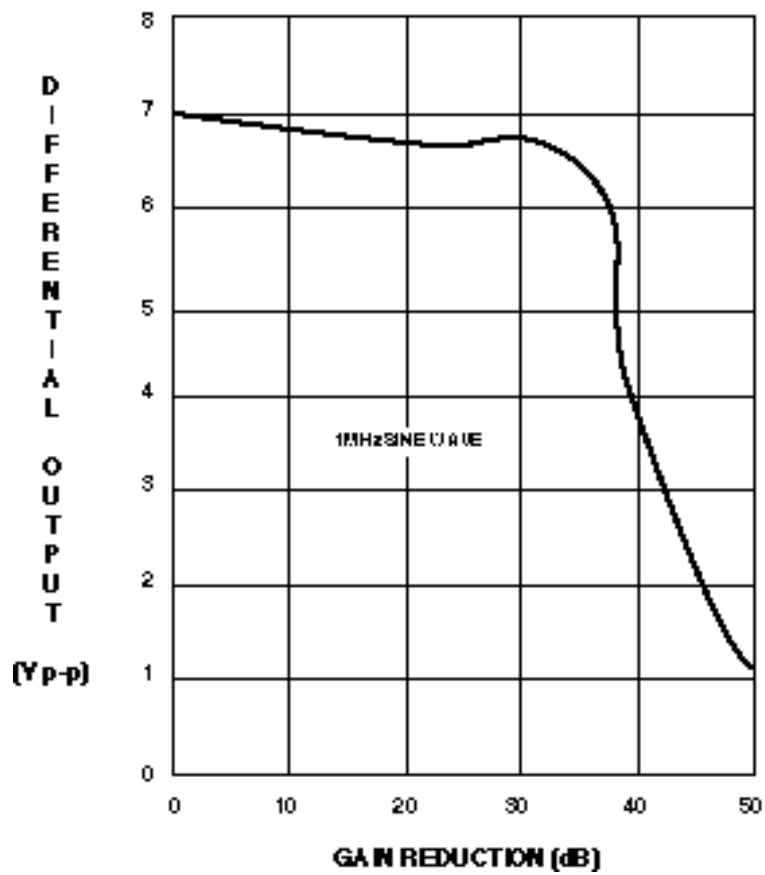


Fig. 5 Max differential O/P voltage v. gain reduction

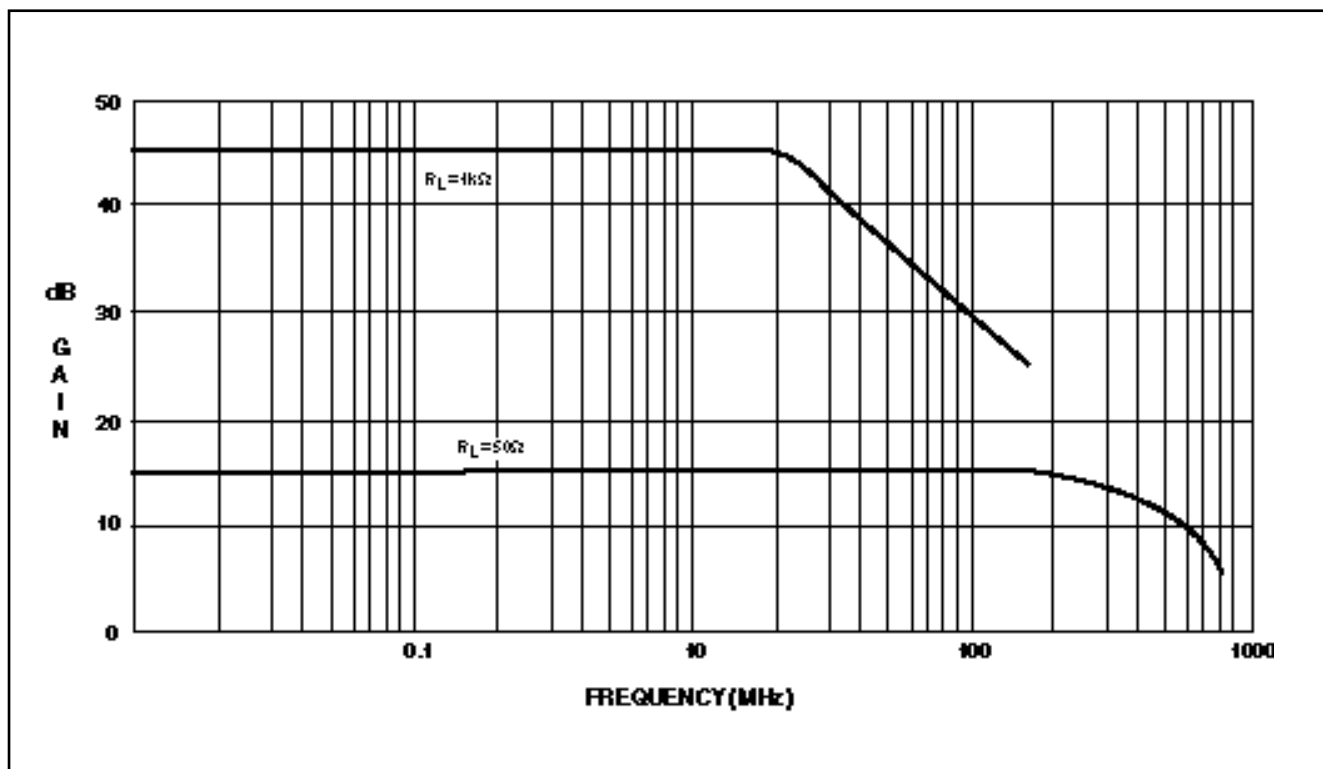


Fig. 6 Voltage Gain v. Frequency

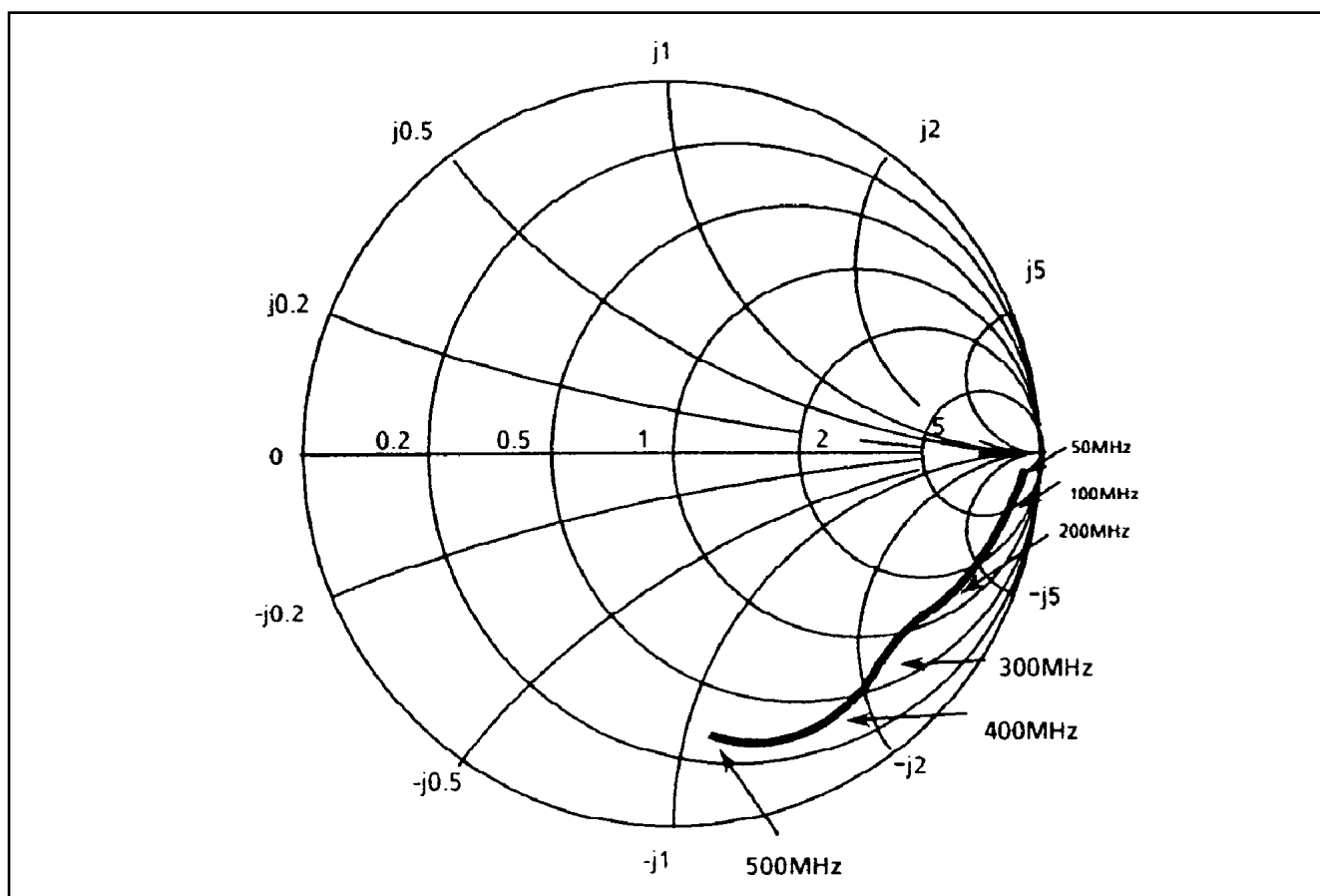


Fig. 7 Input impedance 50W system

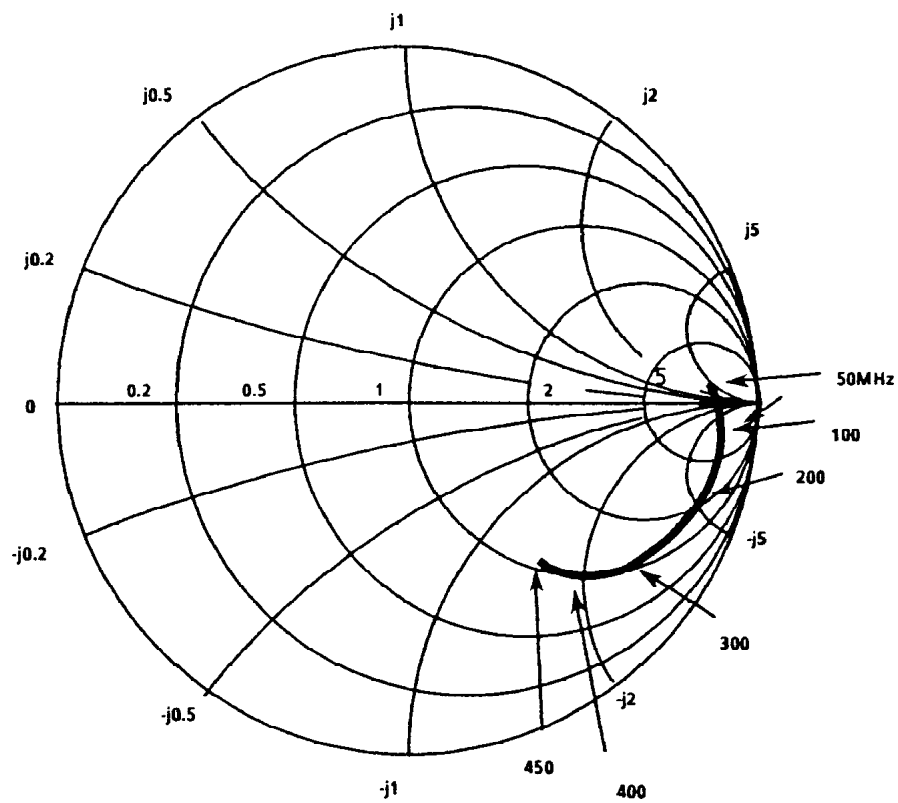
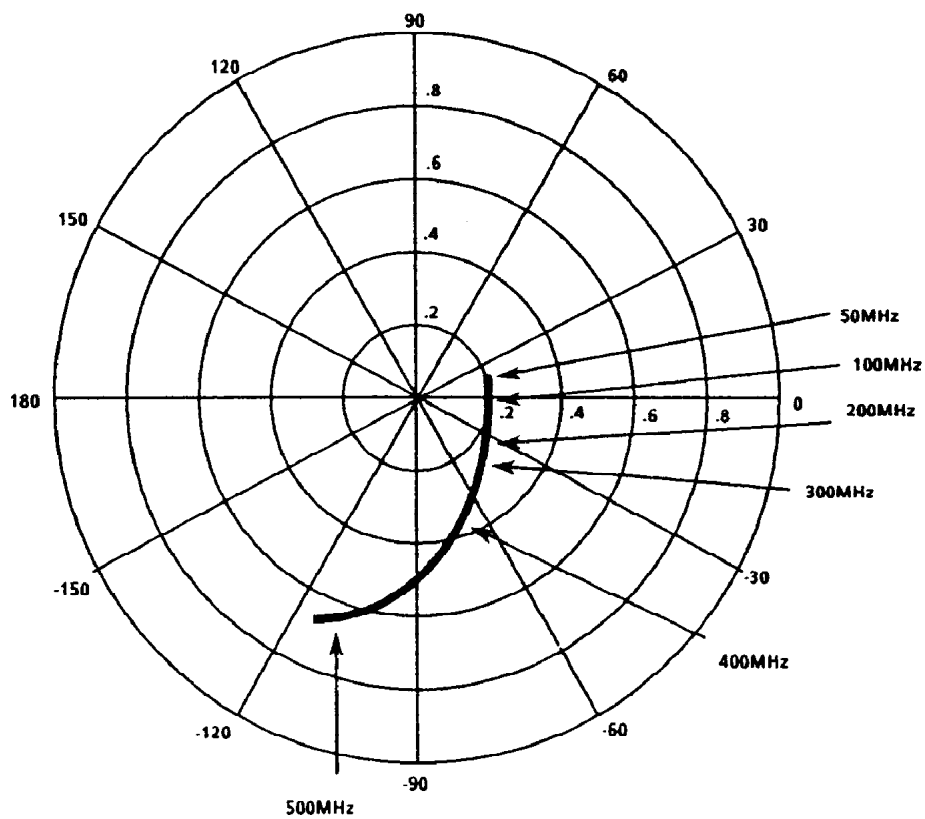


Fig. 8 Output impedance 50W system

Fig. 9 Reverse transmission coefficient S_{12} SL6140

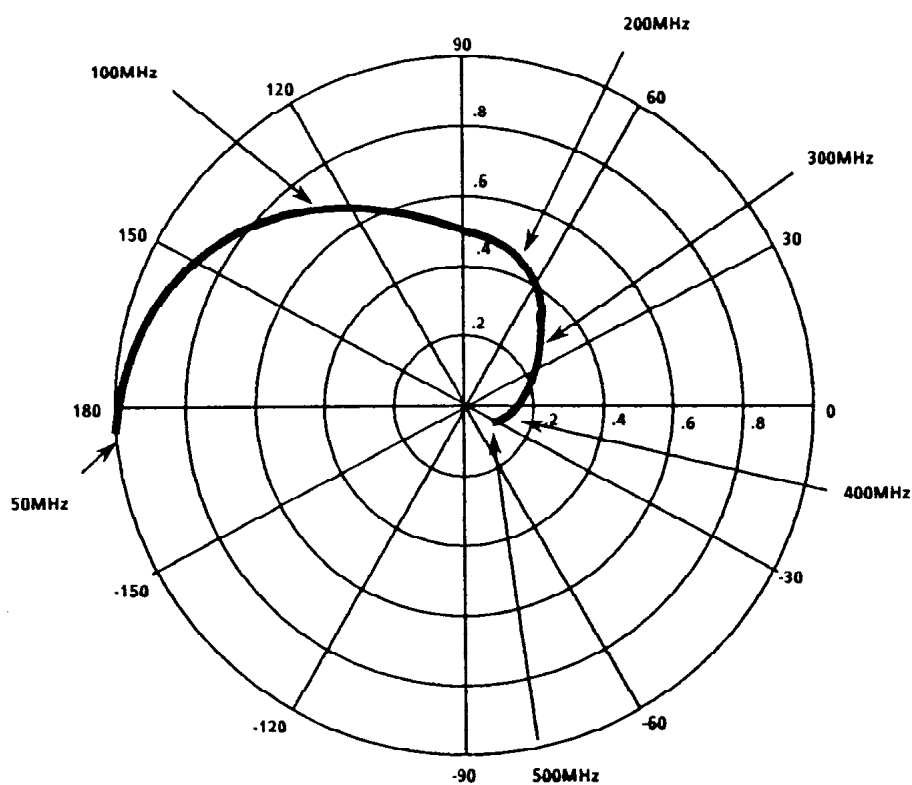
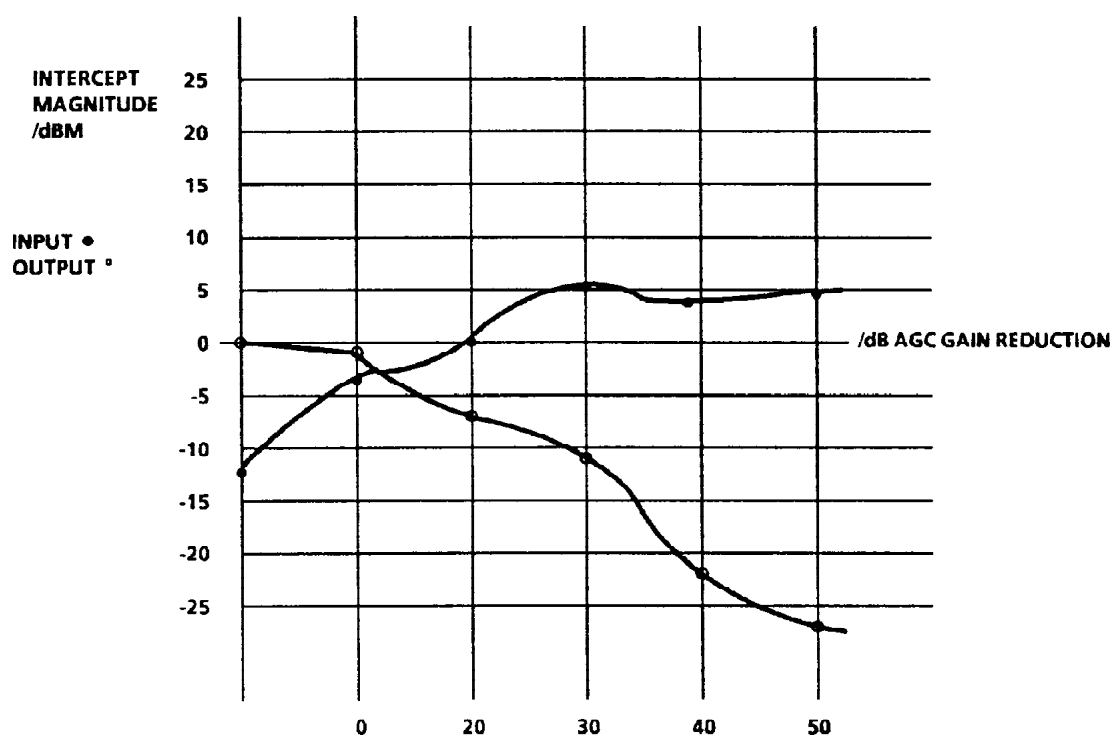
Fig. 10 Forward transmission coefficients S_{21} SL6140

Fig. 11 3rd Order intercept point against gain reduction at 250.0MHz and 254.0MHz

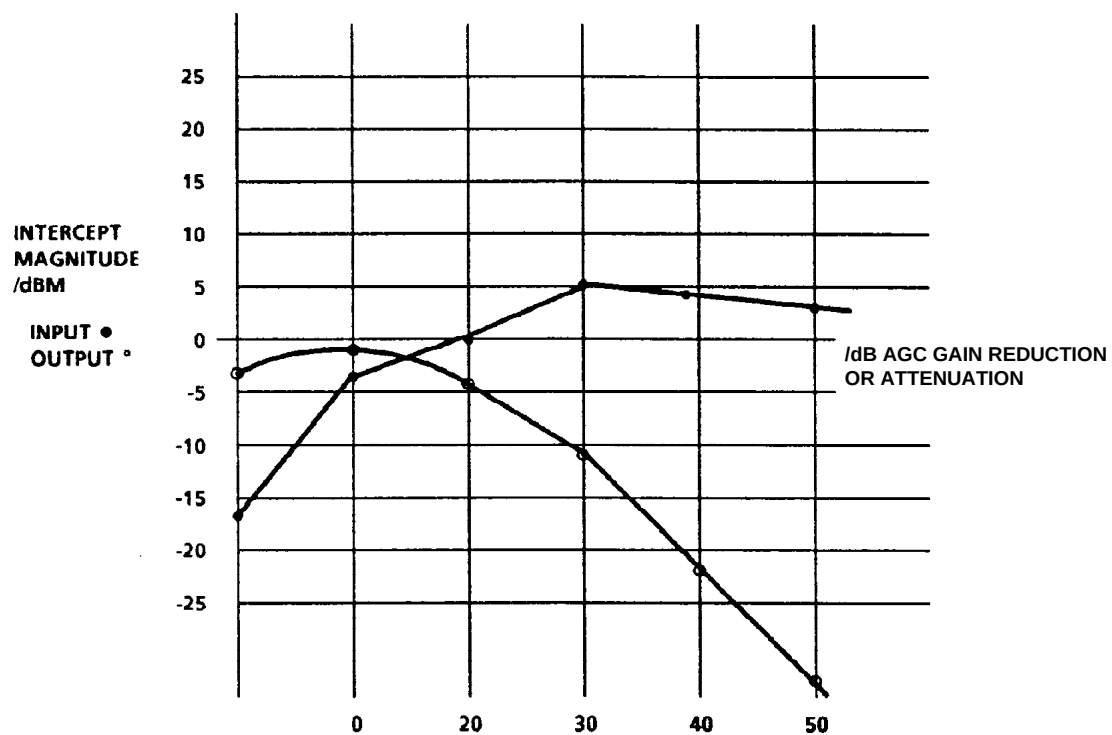
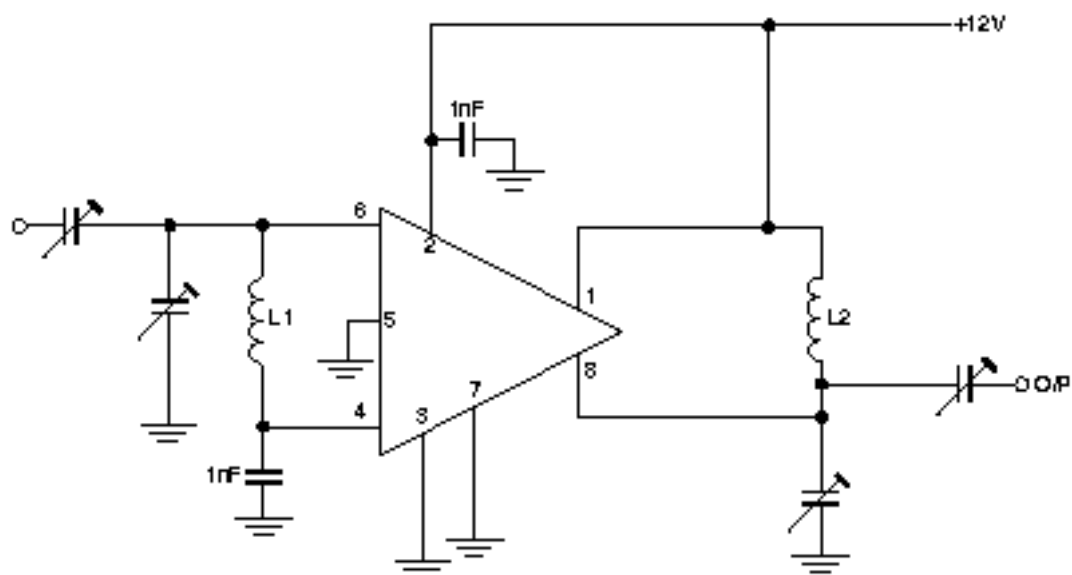


Fig. 12 3rd Order intercept point against gain reduction at 100.0MHz and 104.0MHz



VARIABLE CAPS 3-25pF

L1 7 TURNS # 18 S.V.G 8mm DIA 16mm LONG
 L2 6 TURNS # 16 S.V.G 14mm DIA 19mm LONG

Fig. 13 50MHz Noise figure test circuit

SL6140

PACKAGE DETAILS

4-80/5-00
(0-183/0-197)

8

SP OT REF.

CHAMFER REF.

PIN 1

0-05/0-49
(0-014/0-019)

0-63 (0-027)
MAX

8 LEADS AT
1-27 (0-050)
NOM SPACING

0-10/0-25
(0-004/0-010)

1-05/1-91
(0-053/0-075)

0-18/0-25
(0-007/0-010)

0-25/0-51
(0-010/0-020)
x 45°

0-8°

0-41/1-27
(0-016/0-050)

NOTES
1. Controlling dimensions are inches.
2. This package outline diagram is for guidance only. Please contact your GPS Customer Service Centre for further information.

8-LEAD MINIATURE PLASTIC DIL - MP8



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