

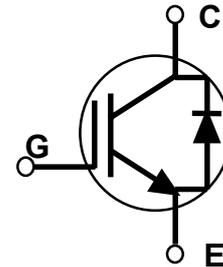
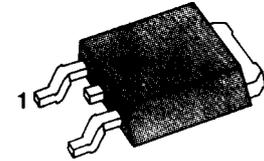
FEATURES

- * Short Circuit rated 10uS @Tc=100°C
- * High Speed Switching
- * Low Saturation Voltage
: $V_{CE(sat)} = 2.0\text{ V @ } I_c=5\text{A}$
- * High Input Impedance
- * CO-PAK, IGBT with FRD
: $T_{rr} = 37\text{nS (Typ.)}$

APPLICATIONS

- * AC & DC Motor controls
- * General Purpose Inverters
- * Robotics , Servo Controls
- * Power Supply
- * Lamp Ballast

D²-PAK



ABSOLUTE MAXIMUM RATINGS

Symbol	Characteristics	Rating	Units
V_{CES}	Collector-Emitter Voltage	600	V
V_{GES}	Gate-Emitter Voltage	± 20	V
I_C	Collector Current @ $T_c = 25^\circ\text{C}$	8	A
	Collector Current @ $T_c = 100^\circ\text{C}$	5	A
$I_{CM(1)}$	Pulsed Collector Current	15	A
I_F	Diode Continuous Forward Current @ $T_c = 100^\circ\text{C}$	8	A
I_{FM}	Diode Maximum Forward Current	56	A
P_D	Maximum Power Dissipation @ $T_c = 25^\circ\text{C}$	60	W
	Maximum Power Dissipation @ $T_c = 100^\circ\text{C}$	25	W
Tsc	Short Circuit Withstand Time	10	uS
T_j	Operating Junction Temperature	-55 ~ 150	°C
Tstg	Storage Temperature Range	-55 ~ 150	°C
T_L	Maximum Lead Temp. For Soldering	300	°C
	Purposes, $\frac{1}{8}$ " from case for 5 seconds		

Notes: (1) Repetitive rating : Pulse width limited by max. junction temperature

ELECTRICAL CHARACTERISTICS (IGBT PART)

(Tc=25°C, Unless Otherwise Specified)

Symbol	Characteristics	Test Conditions	Min	Typ	Max	Units
BV_{CES}	C - E Breakdown Voltage	$V_{GE} = 0V, I_C = 250\mu A$	600	-	-	V
$\Delta V_{CES}/\Delta T_J$	Temperature Coeff. of Breakdown Voltage	$V_{GE} = 0V, I_C = 1mA$	-	0.6	-	V/°C
$V_{GE(th)}$	G - E threshold voltage	$I_C = 5mA, V_{CE} = V_{GE}$	5.0	6.0	8.0	V
I_{CES}	Collector cutoff Current	$V_{CE} = V_{CES}, V_{GE} = 0V$	-	-	250	μA
I_{GES}	G - E leakage Current	$V_{GE} = V_{GES}, V_{CE} = 0V$	-	-	100	nA
$V_{CE(sat)}$	Collector to Emitter saturation voltage	$I_C = 5A, V_{GE} = 15V$	-	2.0	2.7	V
		$I_C = 8A, V_{GE} = 15V$	-	2.5	-	V
C_{ies}	Input capacitance	$V_{GE} = 0V, f = 1MHz$ $V_{CE} = 30V$	-	337	-	pF
C_{oes}	Output capacitance		-	60	-	pF
C_{res}	Reverse transfer capacitance		-	13	-	pF
$t_{d(on)}$	Turn on delay time	$V_{CC} = 300V, I_C = 5A$ $V_{GE} = 15V$ $R_G = 40 \Omega$ Inductive Load	-	9	-	nS
t_r	Turn on rise time		-	18	-	nS
$t_{d(off)}$	Turn off delay time		-	46	75	nS
t_f	Turn off fall time		-	140	280	nS
E_{on}	Turn on Switching Loss		-	80	-	μJ
E_{off}	Turn off Switching Loss		-	100	-	μJ
E_{ts}	Total Switching Loss		-	180	270	μJ
T_{sc}	Short Circuit withstand Time	$V_{CC} = 300V, V_{GE} = 15V$ @Tc = 100°C	10	-	-	μS
Q_g	Total Gate Charge	$V_{CC} = 300V$ $V_{GE} = 15V$ $I_C = 5A$	-	24	36	nC
Q_{ge}	Gate-Emitter Charge		-	7	10	nC
Q_{gc}	Gate-Collector Charge		-	8	12	nC

ELECTRICAL CHARACTERISTICS (DIODE PART)

(Tc=25°C, Unless Otherwise Specified)

Symbol	Characteristics	Test Conditions		Min	Typ	Max	Units
VFM	Diode Forward Voltage	IF=8.0A	Tc =25°C	-	1.4	1.7	V
			Tc =100°C	-	1.3	-	
Trr	Diode Reverse Recovery Time		Tc =25°C	-	37	55	nS
			Tc =100°C	-	55	-	
Irr	Diode Peak Reverse Recovery Current	IF=8.0A, VR=200V -di/dt=200A/uS	Tc =25°C	-	3.5	5.0	A
			Tc =100°C	-	4.5	-	
Qrr	Diode Reverse Recovery Charge		Tc =25°C	-	65	138	nC
			Tc =100°C	-	124	-	

THERMAL RESISTANCE

Symbol	Characteristics	Min	Typ	Max	Units
R _θ JC	Junction-to-Case (IGBT)	-	-	2.0	°C/W
R _θ JC	Junction-to-Case (DIODE)	-	-	3.5	°C/W
R _θ JA	Junction-to-Ambient (PCB mount)	-	-	40	°C/W

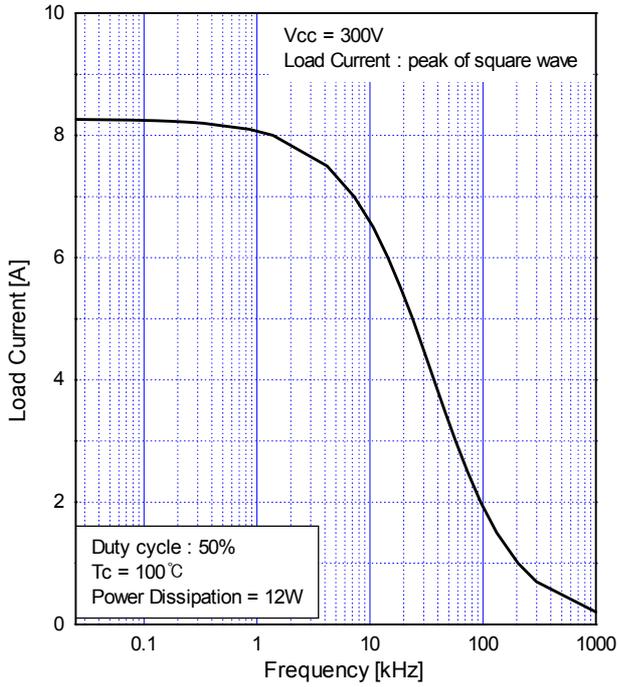


Fig.1 Typical Load Current vs. Frequency

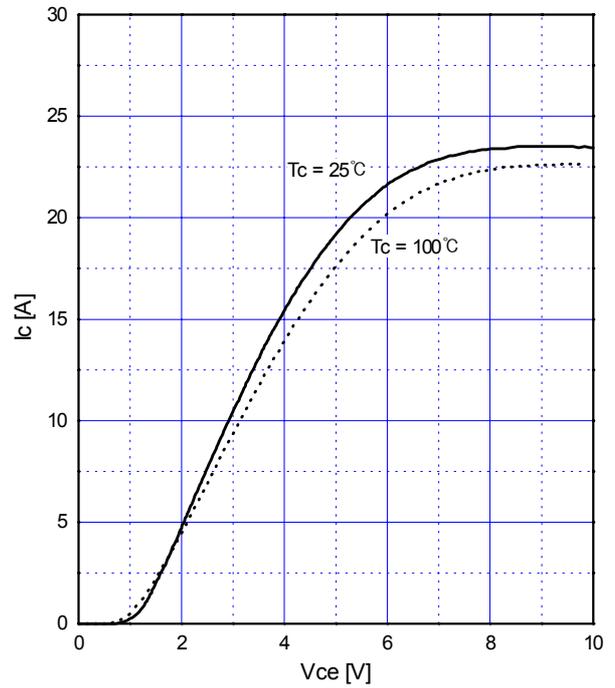


Fig.2 Typical Output Characteristics

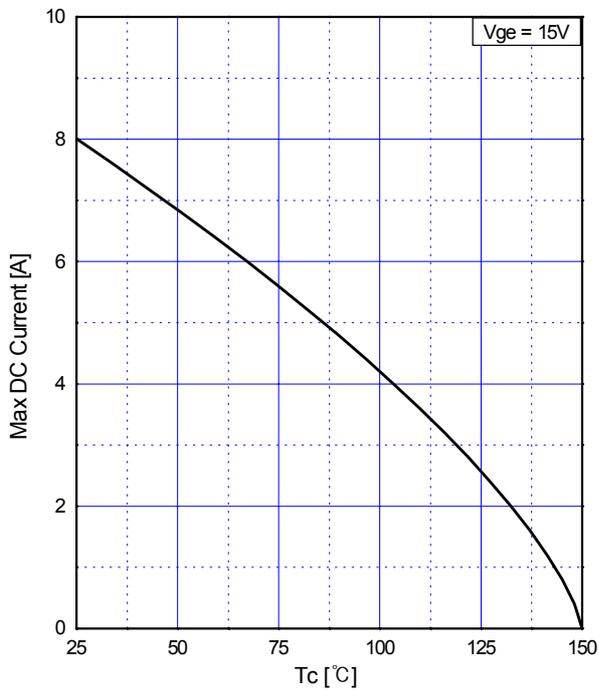


Fig.3 Maximum Collector Current vs. Case Temperature

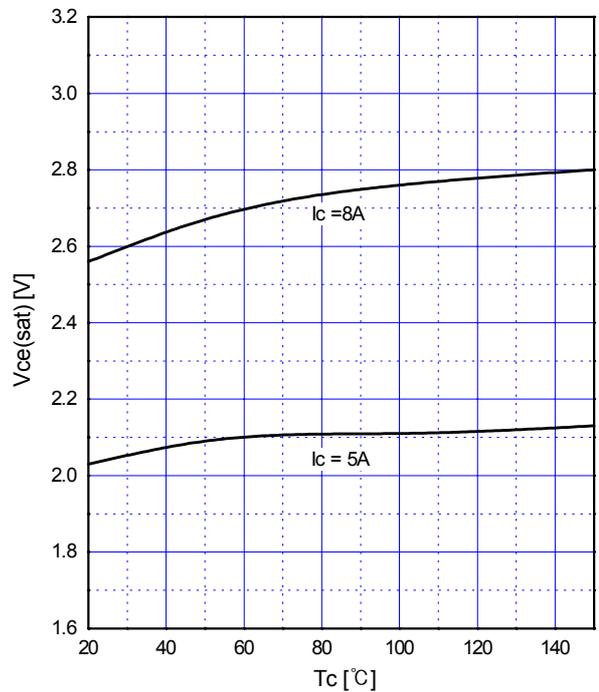


Fig.4 Collector to Emitter Voltage vs. Case Temperature

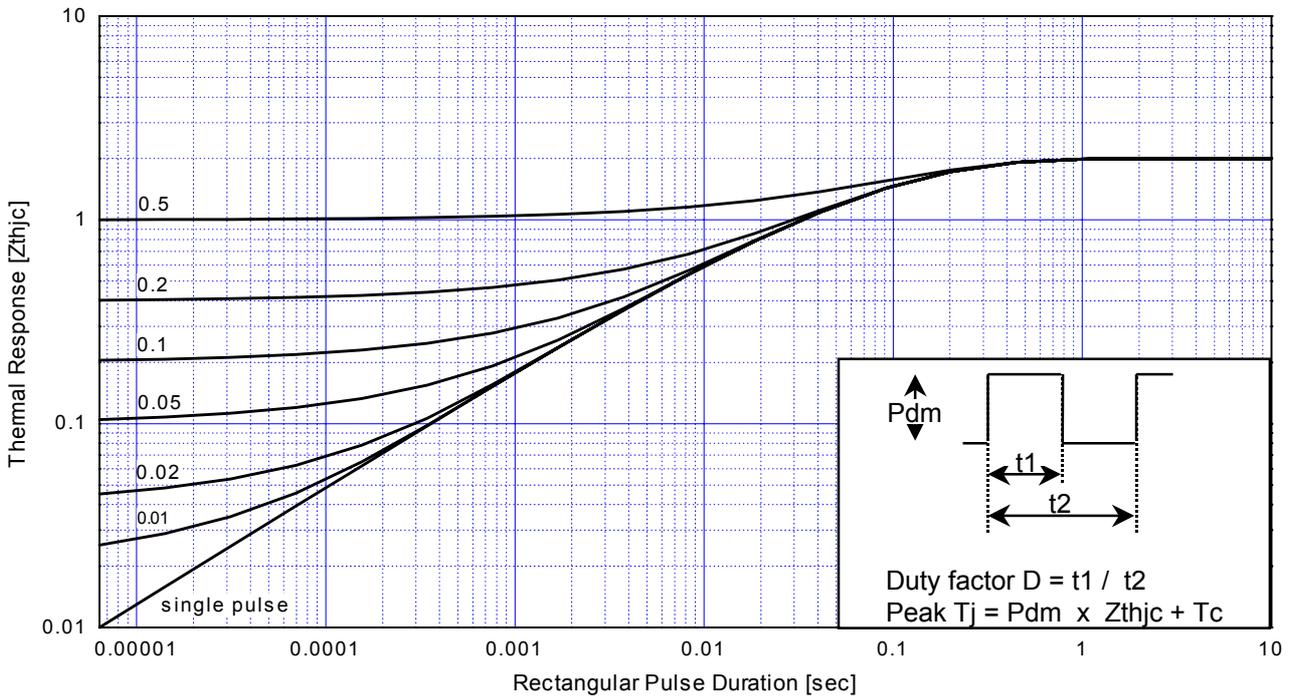


Fig.5 Maximum Effective Transient Thermal Impedance, Junction to Case

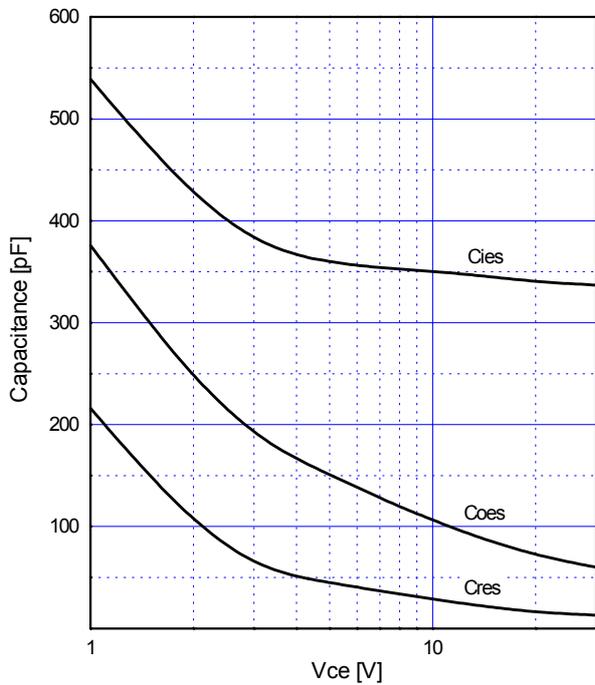


Fig.6 Typical Capacitance vs. Collector to Emitter Voltage

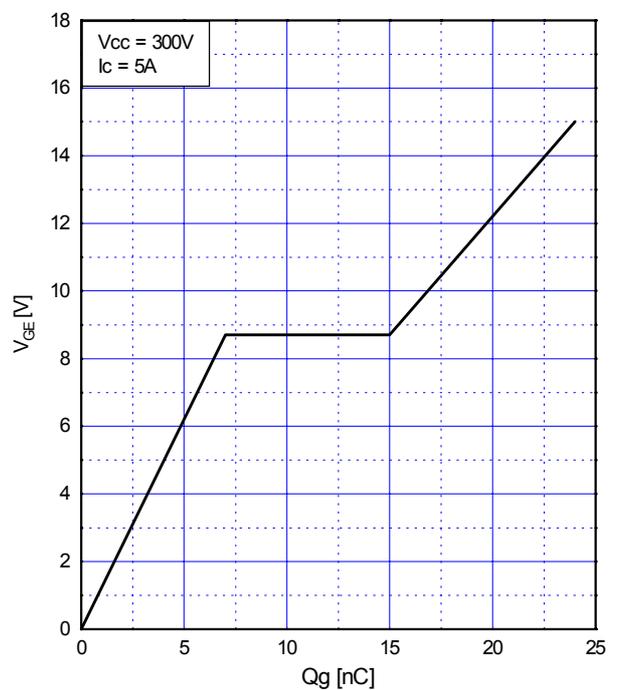


Fig.7 Typical Gate Charge vs. Gate to Emitter Voltage

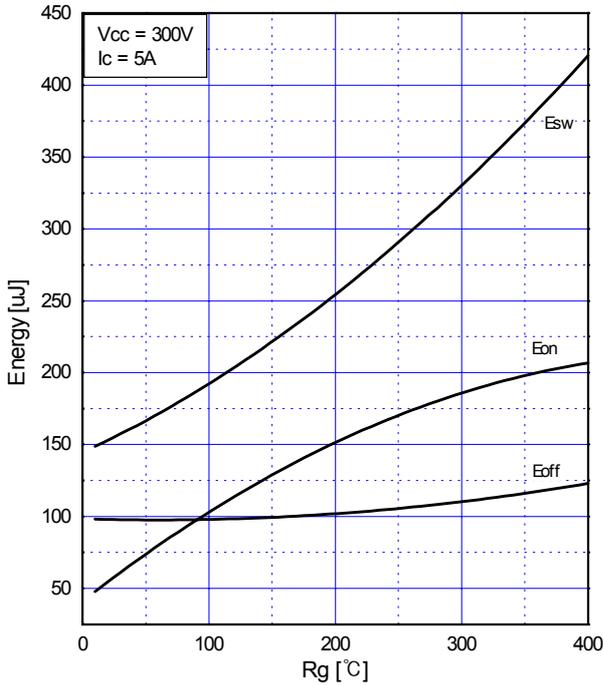


Fig.8 Typical Switching Loss vs. Gate Resistance

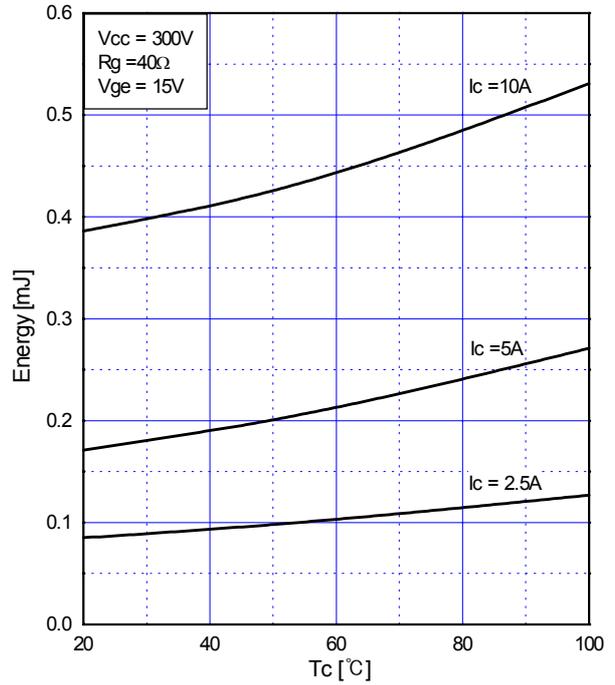


Fig.9 Typical Switching Loss vs. Case Temperature

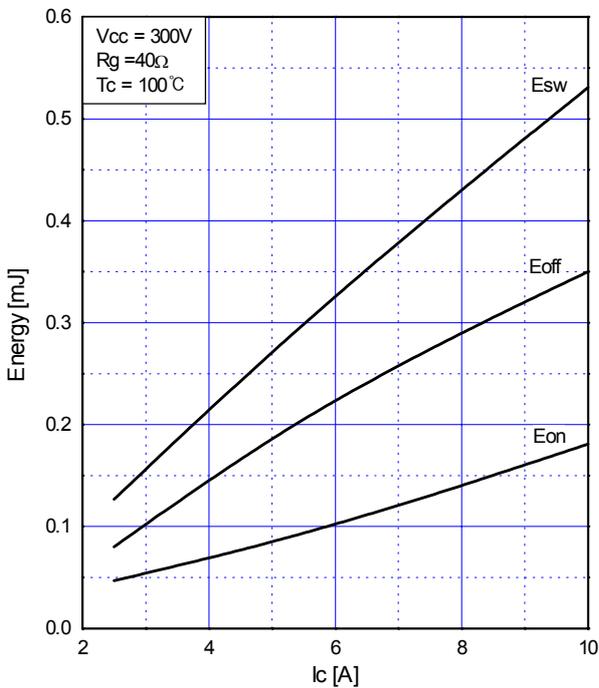


Fig.10 Typical Switching loss vs. Collector to Emitter Current

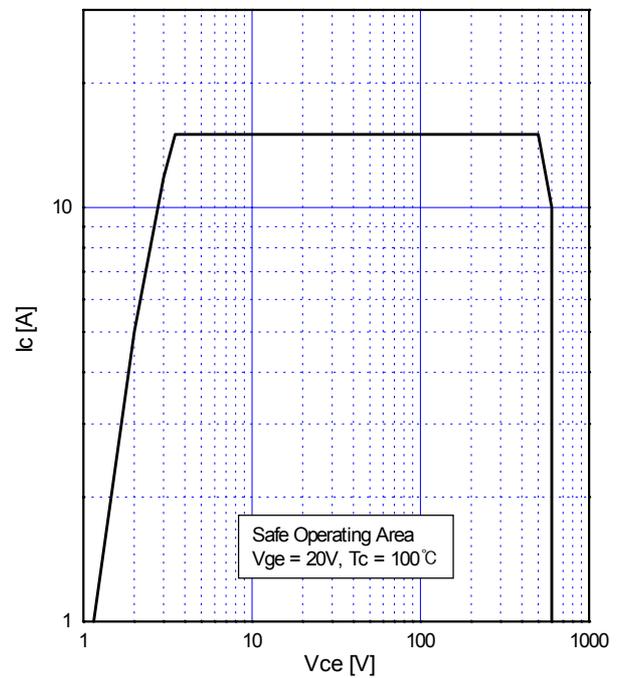


Fig.11 Turn-off SOA

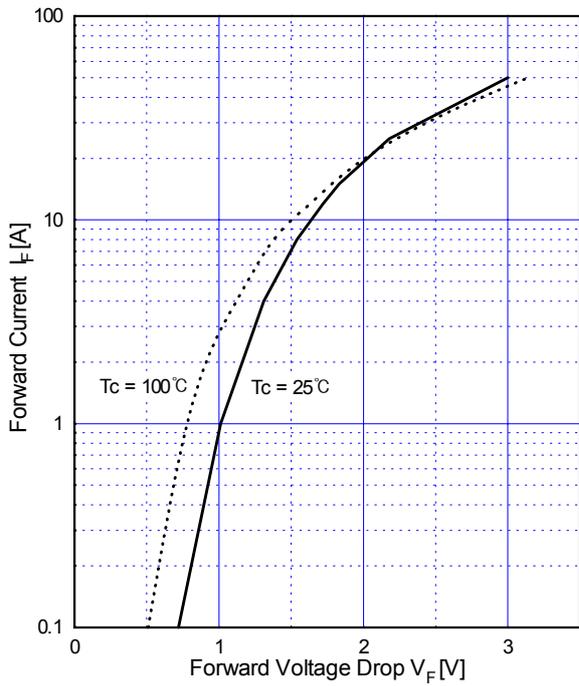


Fig.12 Typical Forward Voltage Drop vs. Forward Current

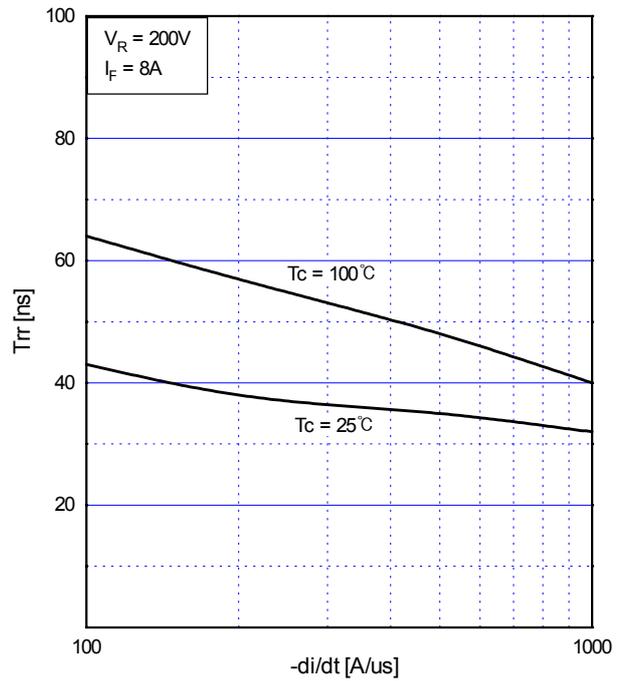


Fig.13 Typical Reverse Recovery Time vs. di/dt

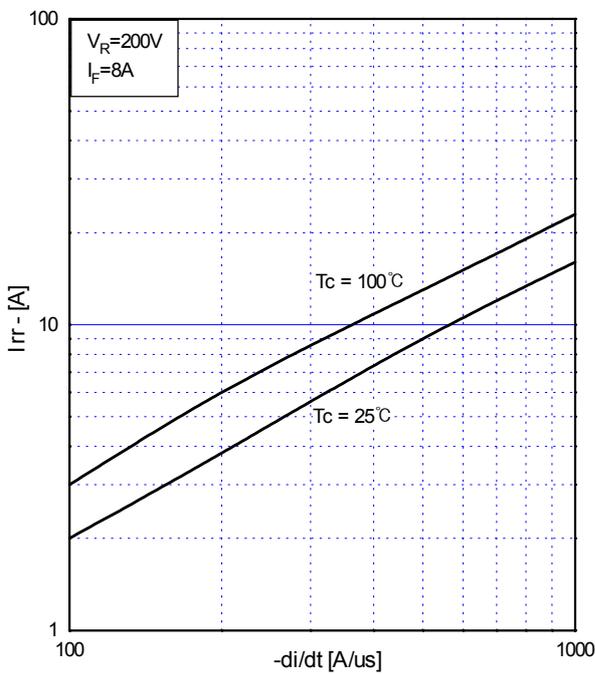


Fig.14 Typical Reverse Recovery Current vs. di/dt

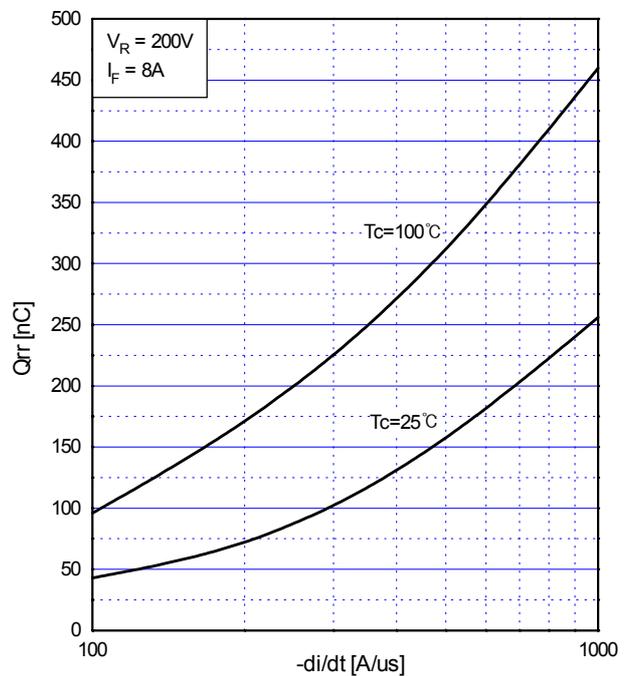


Fig.15 Typical Stored Charge vs. di/dt