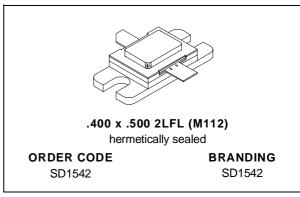


SD1542

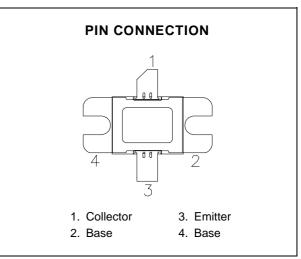
RF & MICROWAVE TRANSISTORS AVIONICS APPLICATIONS

- DESIGNED FOR HIGH POWER PULSED IFF AND DME APPLICATIONS
- 600 WATTS (typ.) IFF 1030/1090 MHz
- 550 WATTS (min.) DME 1025 1150 MHz
- 5.6 dB MIN. GAIN
- REFRACTORY GOLD METALLIZATION
- BALLASTING AND LOW THERMAL RESISTANCE FOR RELIABILITY AND RUGGEDNESS
- 30:1 LOAD VSWR CAPABILITY AT SPECIFIED OPERATING CONDITIONS
- INTERNAL INPUT/OUTPUT MATCHED, COMMON BASE CONFIGURATION



DESCRIPTION

The SD1542 is a hermetically sealed, gold metallized, silicon NPN power transistor. The SD1542 is designed for applications requiring high peak power and low duty cycles such as IFF and DME. The SD1542 is packaged in a hermetic metal/ceramic package with internal input/output matching, resulting in improved broadband performance and a low thermal resistance.



ABSOLUTE MAXIMUM RATINGS $(T_{case} = 25^{\circ}C)$

Symbol	Parameter	Value	Unit
V _{CBO}	Collector-Base Voltage	65	V
V _{CES}	Collector-Emitter Voltage	65	V
V _{EBO}	Emitter-Base Voltage	3.5	V
Ic	Device Current	40	Α
P _{DISS}	Power Dissipation	1350	W
TJ	Junction Temperature	+200	°C
T _{STG}	Storage Temperature	- 65 to +200	°C

THERMAL DATA

R _{TH(j-c)}	Junction-Case Thermal Resistance	0.06	°C/W

November 1992 1/4

ELECTRICAL SPECIFICATIONS (T_{case} = 25°C)

STATIC

Symbol	Took Conditions	Value			lln:4		
Symbol	Test Conditions		Min.	Тур.	Max.	Unit	
ВУсво	I _C = 25mA	$I_E = 0mA$		65	_		٧
BVces	I _C = 50mA	$V_{BE} = 0V$		65	_	_	V
BV _{EBO}	I _E = 10mA	$I_C = 0mA$		3.5	_	_	V
Ices	V _{CE} = 50V	I _E = 0mA			_	35	mA
hFE	Vce = 5V	Ic = .25A		5	_	200	_

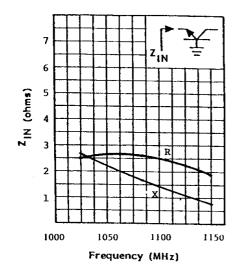
DYNAMIC

Symbol	Symbol Test Conditions		Value		
Syllibol			Тур.	Max.	Unit
Pout	$f = 1025 - 1150MHz$ $P_{IN} = 150 W$ $V_{CE} = 50 V$	550	_	_	W
G _P	$f = 1025 - 1150MHz$ $P_{IN} = 150 W$ $V_{CE} = 50 V$	5.6	_	_	dB

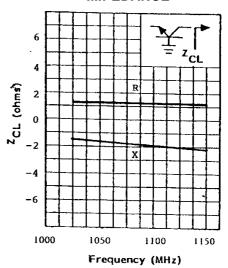
Note: Pulse Width = $10\mu Sec$, Duty Cycle = 1%

IMPEDANCE DATA

TYPICAL INPUT IMPEDANCE



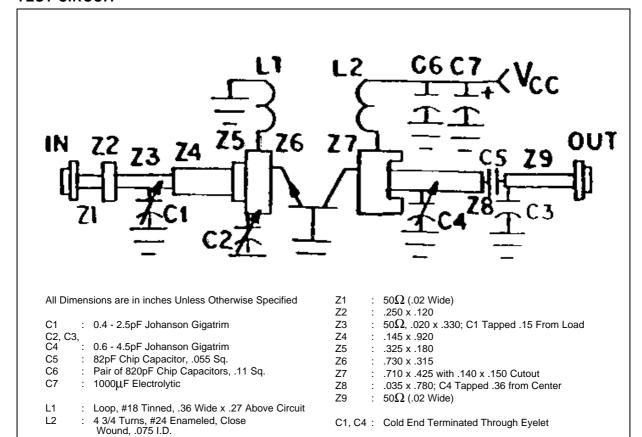
TYPICAL COLLECTOR LOAD IMPEDANCE



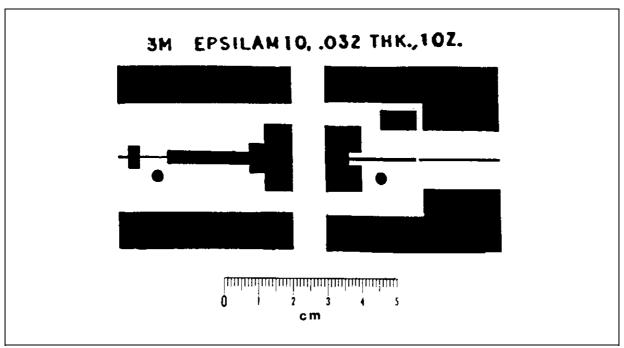
FREQ.	Z _{IN} (Ω)	$Z_{CL}\;(\Omega)$
1020 MHz	1.78 + j 3.0	1.33 – j 2.7
1090 MHz	1.57 + j 2.1	1.64 – j 3.4
1150 MHz	1.55 + j 1.4	1.93 – j 4.0

 $P_{IN} = 150 \text{ W}$ $V_{CE} = 50 \text{ V}$

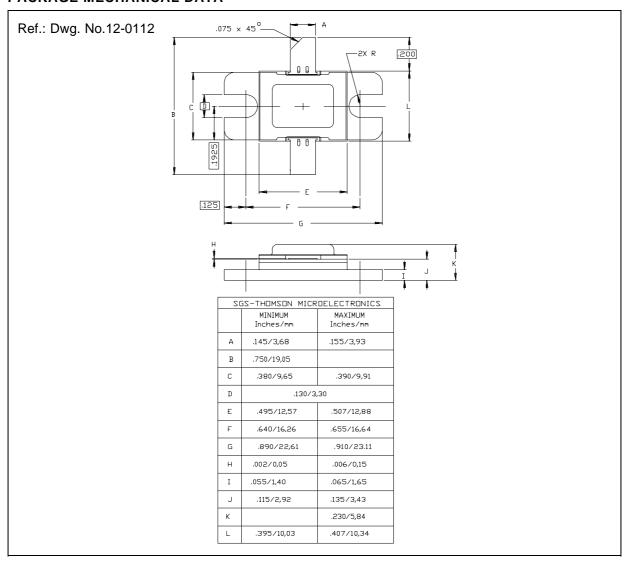
TEST CIRCUIT



PC BOARD LAYOUT



PACKAGE MECHANICAL DATA



Information furnished is believed to be accurate and reliable. However, SGS-THOMSON Microelectronics assumes no responsability for the consequences of use of such information nor for any infringement of patents or other rights of third parties which may results from its use. No license is granted by implication or otherwise under any patent or patent rights of SGS-THOMSON Microelectronics. Specifications mentioned in this publication are subject to change without notice. This publication supersedes and replaces all information previously supplied. SGS-THOMSON Microelectronics products are not authorized for use as critical components in life support devices or systems without express written approval of SGS-THOMSON Microelectonics.

© 1994 SGS-THOMSON Microelectronics - All Rights Reserved

SGS-THOMSON Microelectronics GROUP OF COMPANIES

Australia - Brazil - France - Germany - Hong Kong - Italy - Japan - Korea - Malaysia - Malta - Morocco - The Netherlands - Singapore - Spain - Sweden - Switzerland - Taiwan - Thailand - United Kingdom - U.S.A

