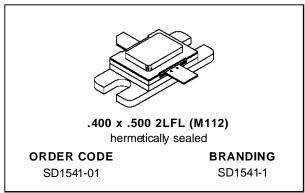


SD1541-01

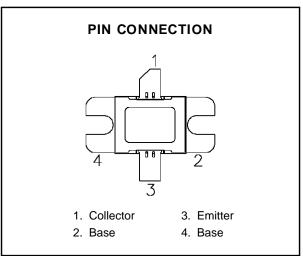
RF & MICROWAVE TRANSISTORS AVIONICS APPLICATIONS

- DESIGNED FOR HIGH POWER PULSED IFF AND DME APPLICATIONS
- 400 (min.) DME 1025 1150 MHz
- 6.5 dB MIN. GAIN
- REFRACTORY GOLD METALLIZATION
- EMITTER BALLASTING AND LOW THERMAL RESISTANCE FOR RELIABILITY AND RUGGEDNESS
- 30:1 LOAD VSWR CAPABILITY AT SPECIFICIED OPERATING CONDITIONS
- INPUT/OUTPUT MATCHED, COMMON BASE CONFIGURATION



DESCRIPTION

The SD1541-01 is a hermetically sealed, gold metallized, silicon NPN power transistor. The SD1541-01 is designed for applications requiring high peak power and low duty cycles such as DME. The SD1541-01 is packaged in a hermetic metal/ceramic package with internal input/output matching, resulting in improved broadband performance and a low thermal resistance.



ABSOLUTE MAXIMUM RATINGS $(T_{case} = 25^{\circ}C)$

Symbol	Parameter	Value	Unit	
V _{CBO}	Collector-Base Voltage	65		
V _{CES}	Collector-Emitter Voltage	65		
V_{EBO}	Emitter-Base Voltage	3.5	V	
Ic	Device Current	22	А	
Poiss	Power Dissipation	1458	W	
TJ	Junction Temperature	+200		
T _{STG}	Storage Temperature	– 65 to +150 °		

THERMAL DATA

R _{TH(j-c)} Junction-Case Thermal Resistance	0.12	°C/W
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ELECTRICAL SPECIFICATIONS (Tcase = 25°C)

STATIC

Symbol	Test Conditions	Value			Unit		
		Min.	Тур.	Max.			
ВУсво	I _C = 25mA	$I_E = 0mA$		65	_	_	V
BVces	I _C = 50mA	V _{BE} = 0V		65	_	_	V
BV _{EBO}	I _E = 10mA	$I_C = 0mA$		3.5	_	_	V
I _{CES}	V _{CE} = 50V	$I_E = 0mA$		_		25	mA
hFE	V _{CE} = 5V	Ic = .25A		5	_	200	_

DYNAMIC

Symbol	Symbol Test Conditions		Value			Unit
Symbol			Min.	Тур.	Max.	Unit
Pout	f = 1025 — 1150MHz P _{IN} = 90 W	$V_{CE} = 50 V$	400	_		W
G _P	f = 1025 — 1150MHz P _{IN} = 90 W	V _{CE} = 50 V	6.5	_	_	dB

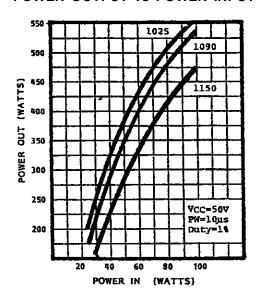
Note: Pulse Width = 10μ Sec, Duty Cycle = 1%

This device is suitable for use under other pulse width/duty cycle conditions.

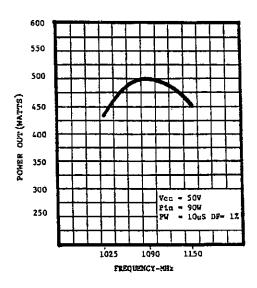
Please contact the factory for specific applications assistance.

TYPICAL PERFORMANCE

POWER OUTPUT vs POWER INPUT



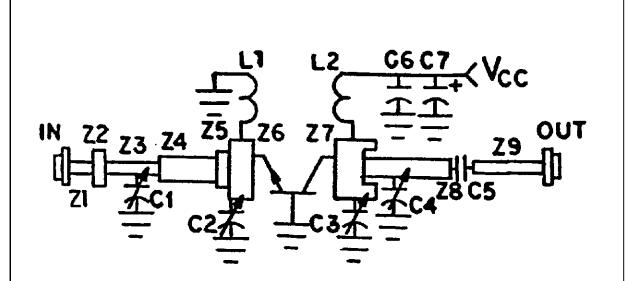
POWER OUTPUT vs FREQUENCY



IMPEDANCE DATA

FREQ.	Z _{IN} (Ω)	Z _{CL} (Ω)		
1020 MHz	2.898 + j 4.1	1.382 – j 3.2		
1090 MHz	2.325 + j 3.4	1.338 – j 2.8		
1150 MHz	1.994 + j 2.8	1.269 – j 2.5		

TEST CIRCUIT



All Dimensions in Inches Unless Otherwise specified Z3 : 50Ω .020 x .330; C1 tapped .15 from Load

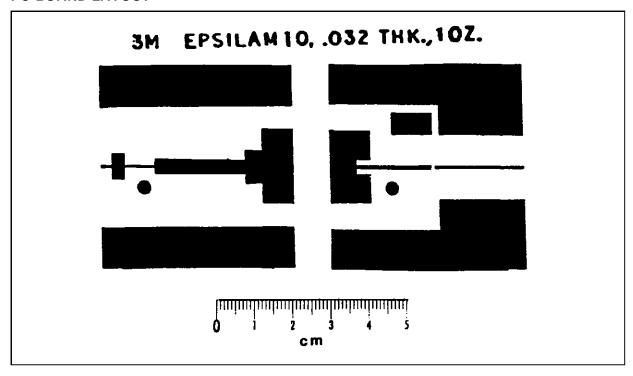
Z4 : .145 x .920 C1 : 0.4 - 2.5pF Johanson Gigatrim Z5 : .325 x .180 C2, C3, Z6 : .730 x .315

L1 : Loop, #18 Tinned, .36 Wide x .27 above Circuit

L2 : 4 3/4 Turns, #24 En., C.W., .075 I.D. C1, C4 : Cold End Terminated Through Eyelet.

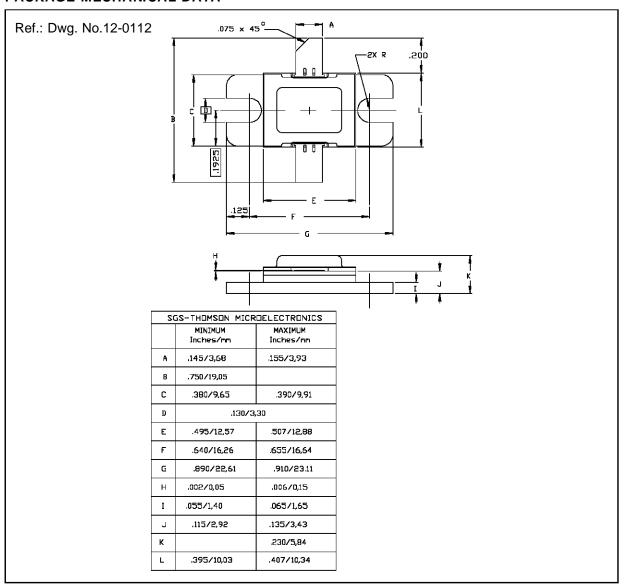
Z1 : 50Ω (.02 Wide) Z2 : .250 x .120

PC BOARD LAYOUT



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PACKAGE MECHANICAL DATA



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