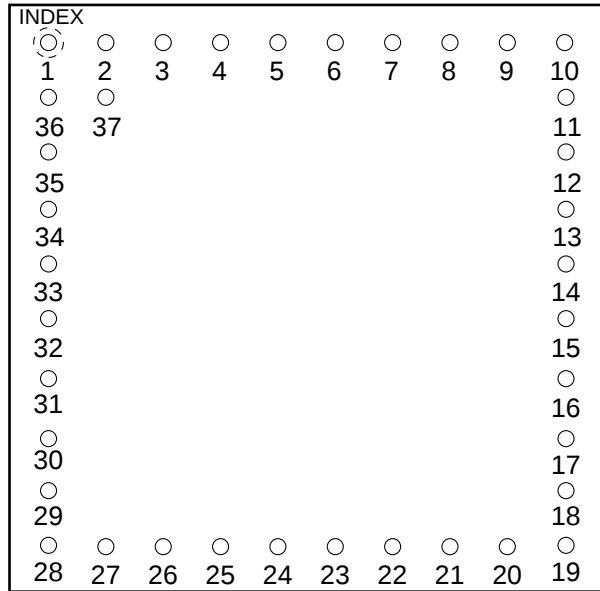


8- OR 10-BIT PARALLEL-TO-SERIAL CONVERTER

—BOTTOM VIEW—

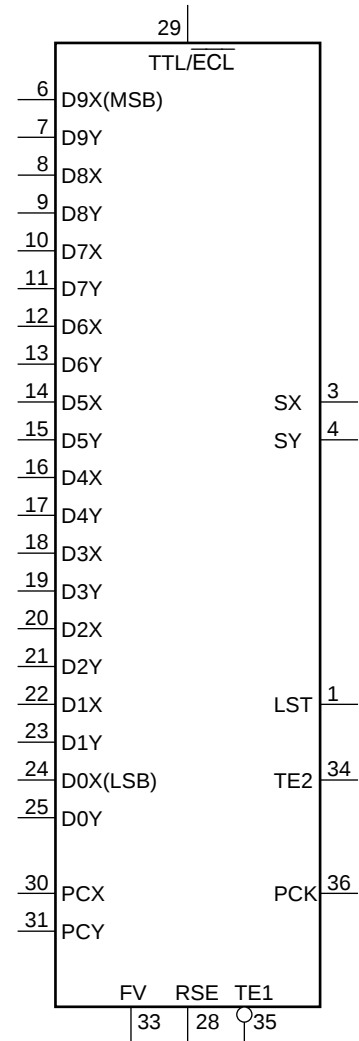


INPUT

D9X - D0X, D9Y - D0Y : PARALLEL DATA
 PCX, PCY : PARALLEL CLOCK
 FV : VCO FREQ. ADJ.
 RSE : VCO RANGE SELECT (H : HIGH RANGE)
 TE1 : TEST TERMINAL (LOW = TEST)
 TTL/ECL : VCC FOR INPUT LEVEL SELECT
 (+5 V = TTL, GND = ECL)

OUTPUT

LST : PLL LOCK DETECT (H : LOCK)
 PCK : PARALLEL CLOCK
 SX, SY : SERIAL DATA
 TE2 : TEST TERMINAL



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	O	LST	11	I	D7Y	21	I	D2Y	31	I	PCY
2	—	GND	12	I	D6X	22	I	D1X	32	—	GND
3	O	SX	13	I	D6Y	23	I	D1Y	33	I	FV
4	O	SY	14	I	D5X	24	I	D0X(LSB)	34	O	TE2
5	—	GND	15	I	D5Y	25	I	D0Y	35	I	TE1
6	I	D9X(MSB)	16	I	D4X	26	—	VEE1	36	O	PCK
7	I	D9Y	17	I	D4Y	27	—	VEE2	37	—	NC
8	I	D8X	18	I	D3X	28	I	RSE			
9	I	D8Y	19	I	D3Y	29	—	TTL/ECL			
10	I	D7X	20	I	D2X	30	I	PCX			