

ICs for Communications

Enhanced Serial Communications Controller
ESCC1

SAB 82531 Version 3.2

SAF 82531 Version 3.2

Delta Sheet 01.98 (to User's Manual 07.96)

SAB 82531 SAF 82531 Revision History: Current Version: 01.98		
Previous Version: 02.97		
Page (in previous Version)	Page (in new Version)	Subjects (major changes since last revision)
		Update of pin configuration

**Enhanced Serial Communications Controller
ESCC1**

**SAB 82531
SAF 82531**

Delta Sheet for the Version 3.2

This delta sheet describes the differences between the ESCC2 Version V3.2A and the ESCC1 Version V3.2. It refers to the ESCC2 User's Manual 07.96 in combination with the Addendum 02.97.

The ESCC1 V3.2 provides the same functionality as the ESCC2 V3.2A without serial channel B. Thus, the pin configuration, the address range and the register set of the ESCC1 are different. Figure 1 shows the pin configuration of the ESCC1 V3.2. The address range of the ESCC1 is 00_{Hex} to 3F_{Hex}, thus, eliminating the register set of serial channel B. Single bits referring to channel B (GIS:ISB1...ISB0) are invalid.

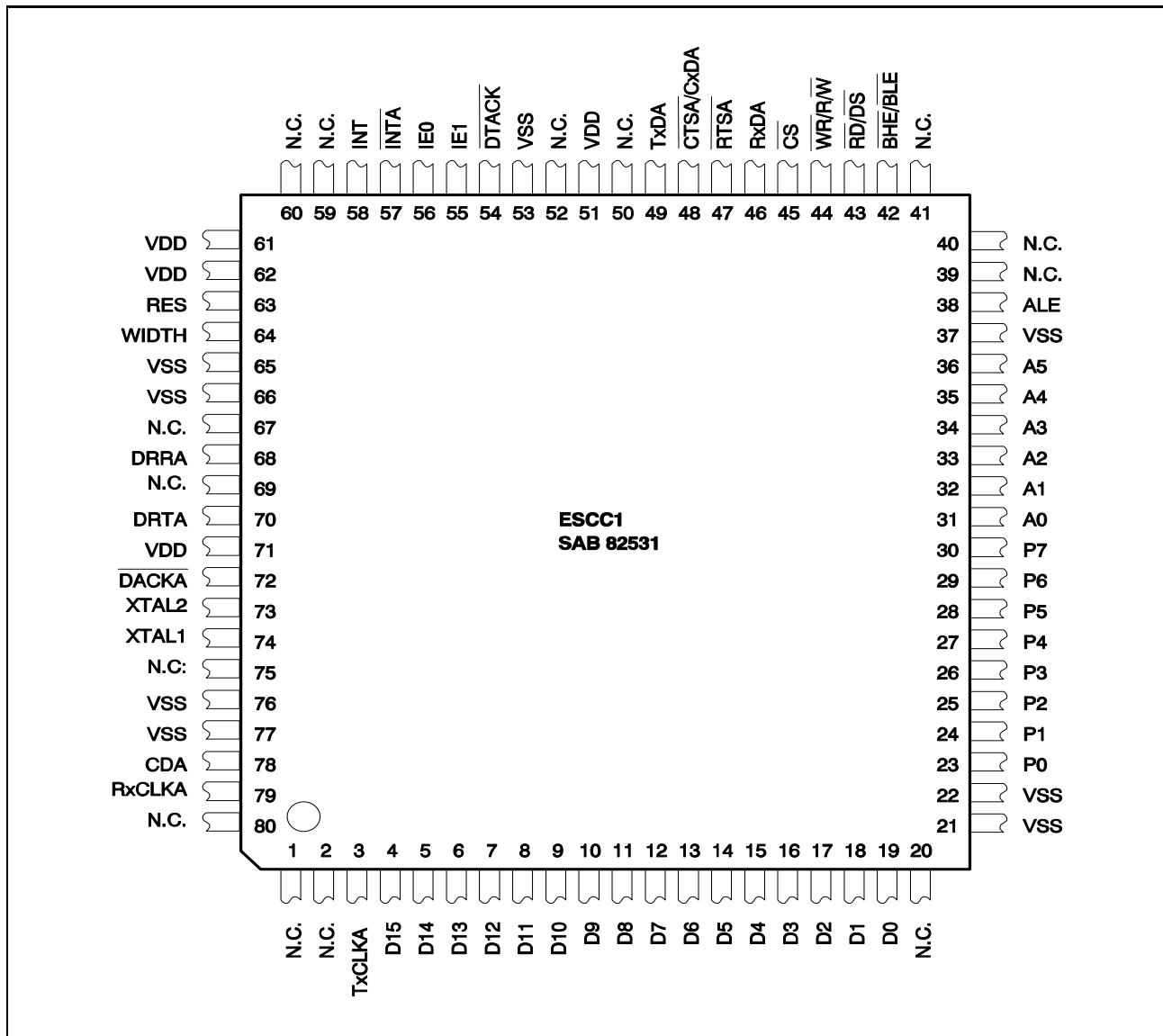


Figure 1: Pin Configuration