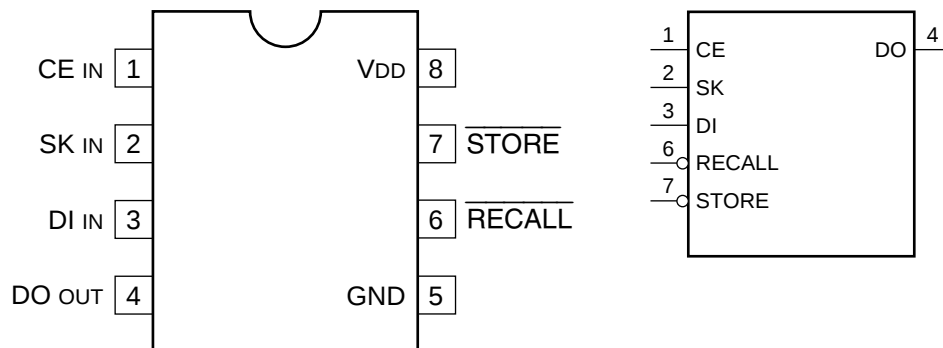


C-MOS 256-BIT SERIAL NVRAM

—TOP VIEW—

**INPUT**

CE : CHIP ENABLE
 SK : SERIAL CLOCK
 DI : SERIAL DATA
 RECALL : RECALL
 STORE : STORE

OUTPUT

DO : SERIAL DATA

