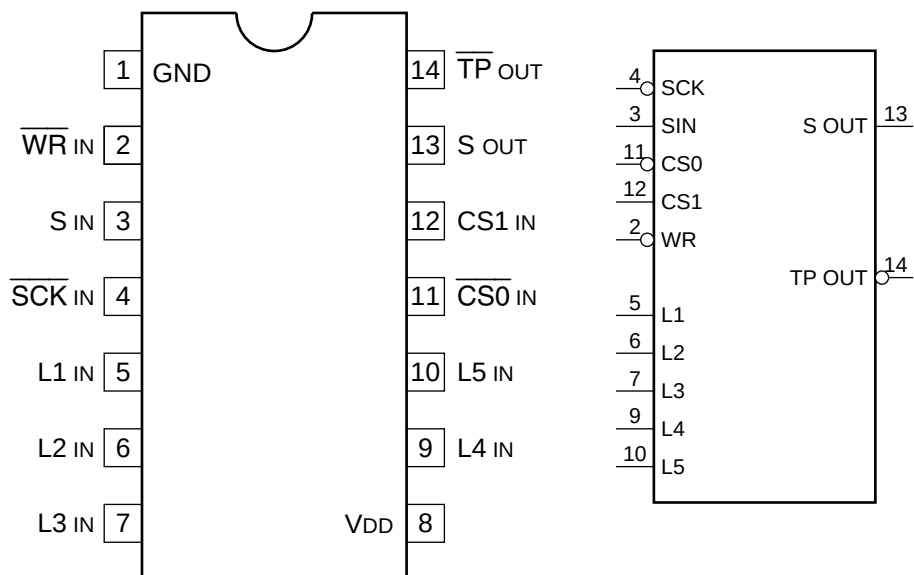


C-MOS REAL TIME CLOCK

—TOP VIEW—

**INPUT** $\overline{CS0}$: CHIP SELECT (L : ACCESS ENABLE, H : SOUT HIGH Z)

CS1 : POWER DOWN DETECTION

L1 - L5 : TEST IN

 $\overline{SCK}$: SERIAL SYNC SIGNAL

SIN : SERIAL ADDRESS/DATA

 $\overline{WR}$: WRITING SELECT (L : WRITING, H : READING)**OUTPUT**

SOUT : SERIAL ADDRESS/DATA

TPOUT : REFERENCE SIGNAL

