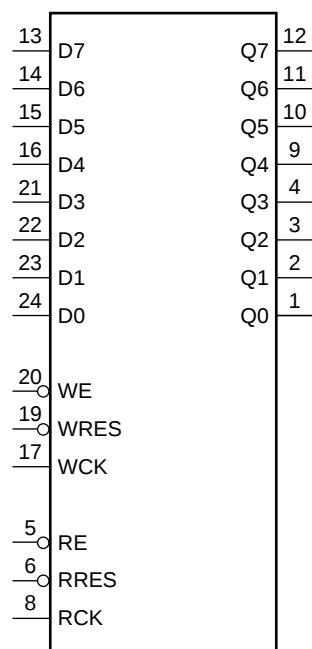
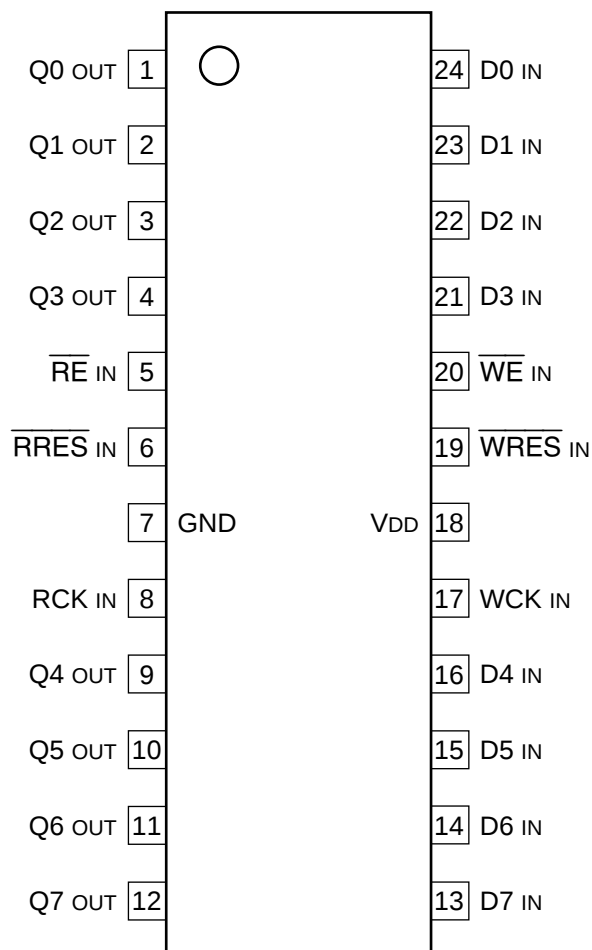


C-MOS 40 M (5120 × 8)-BIT LINE MEMORY (FIFO)

—TOP VIEW—

**INPUT**

D0 - D7 : DATA

RCK : READ CLOCK

RE : READ ENABLE

RRES : READ RESET

WCK : WRITE CLOCK

WE : WRITE ENABLE

WRES : WRITE RESET

OUTPUT

Q0 - Q7 : DATA

