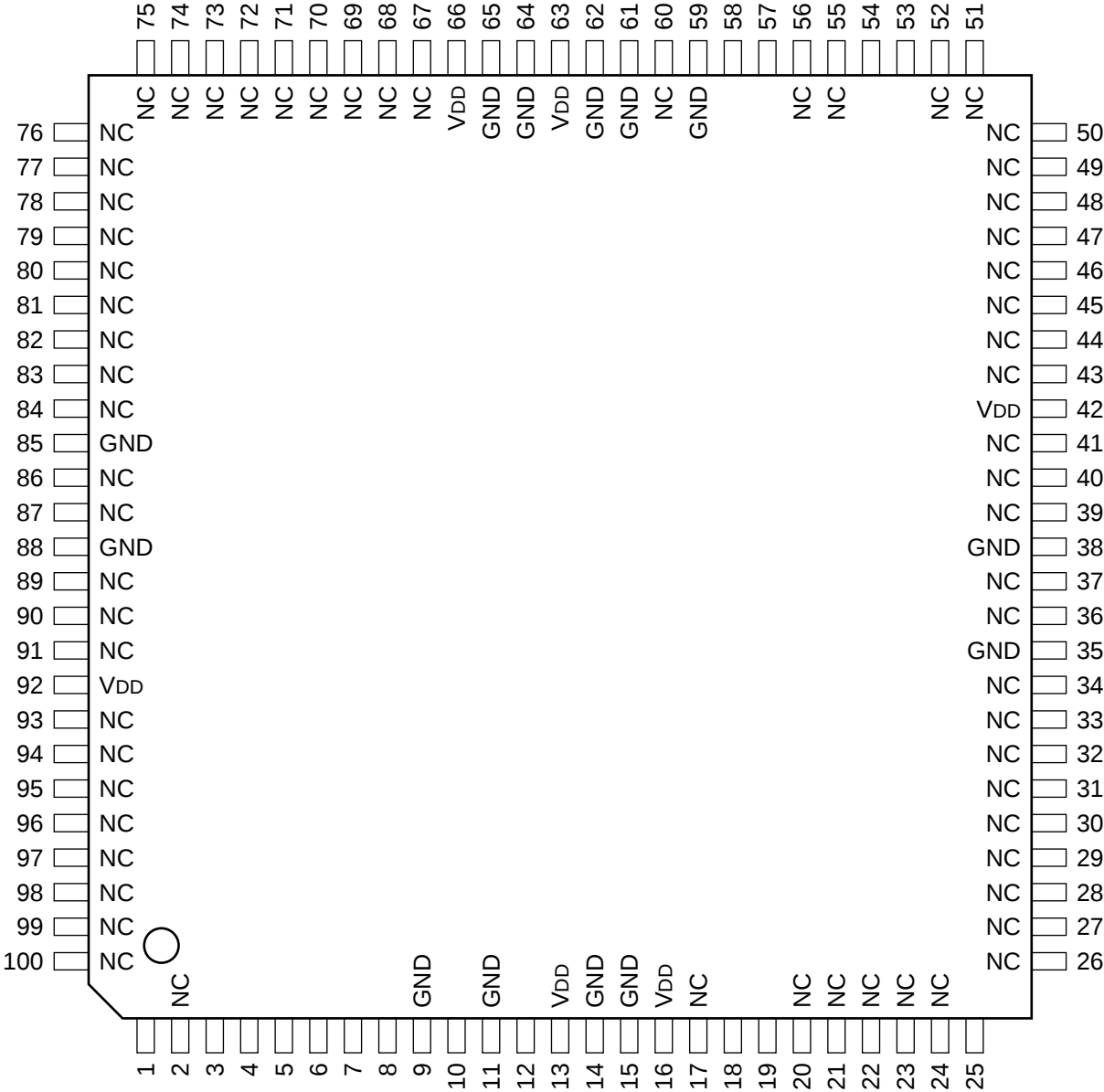


C-MOS FIELD PROGRAMMABLE GATE ARRAY
—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I/O	CLR	26	—	NC	51	—	NC	76	—	NC
2	—	NC	27	—	NC	52	—	NC	77	—	NC
3	I/O	PAL NTSC	28	—	NC	53	I/O	BLKG	78	—	NC
4	I/O	FULL NORMAL	29	—	NC	54	I/O	XBLKG	79	—	NC
5	I/O	CLKSEL	30	—	NC	55	—	NC	80	—	NC
6	I/O	CLK2IN	31	—	NC	56	—	NC	81	—	NC
7	I/O	CLK2OUT	32	—	NC	57	I/O	WIDE ID	82	—	NC
8	I/O	CLK1IN	33	—	NC	58	I/O	XWIDE ID	83	—	NC
9	—	GND	34	—	NC	59	—	GND	84	—	NC
10	I/O	CLK1 2OUT	35	—	GND	60	—	NC	85	—	GND
11	—	GND	36	—	NC	61	—	GND	86	—	NC
12	I	CLK	37	—	NC	62	—	GND	87	—	NC
13	—	V _{DD}	38	—	GND	63	—	V _{DD}	88	—	GND
14	—	GND	39	—	NC	64	—	GND	89	—	NC
15	—	GND	40	—	NC	65	—	GND	90	—	NC
16	—	V _{DD}	41	—	NC	66	—	V _{DD}	91	—	NC
17	—	NC	42	—	V _{DD}	67	—	NC	92	—	V _{DD}
18	I/O	HDIN	43	—	NC	68	—	NC	93	—	NC
19	I/O	VDIN	44	—	NC	69	—	NC	94	—	NC
20	—	NC	45	—	NC	70	—	NC	95	—	NC
21	—	NC	46	—	NC	71	—	NC	96	—	NC
22	—	NC	47	—	NC	72	—	NC	97	—	NC
23	—	NC	48	—	NC	73	—	NC	98	—	NC
24	—	NC	49	—	NC	74	—	NC	99	—	NC
25	I/O	MODE FULL	50	—	NC	75	—	NC	100	—	NC