

## ICs for Communications

Subscriber Access Controller for Upn-Interface Terminals  
SmartLink-P

PSB 2197 Version 1.1

PSF 2197 Version 1.1

Addendum / Corrections to User's Manual 02.95 09.97

<b>PSB 2197</b>		
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Page (in previous Version)	Page (in current Version)	Subjects (major changes since last revision)

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### Addendum / Corrections to User's Manual 02.95

CMOS

The following corrections are valid for the SmartLink-P version V1.1 with respect to the values given in the User's Manual 02.95 and the Addendum 08.95:

**Table 1**

Parameter	Symbol	Limit Values		Unit
		min.	max.	
Chip select setup time	$t_{CSs}$	15		ns
Chip select hold time	$t_{CSH}$	25		ns
$\overline{CS}$ high to MISO-tristate	$t_{SDXt}$		70	ns
Bit Clock delay	$t_{BCD}$	-20	100	ns