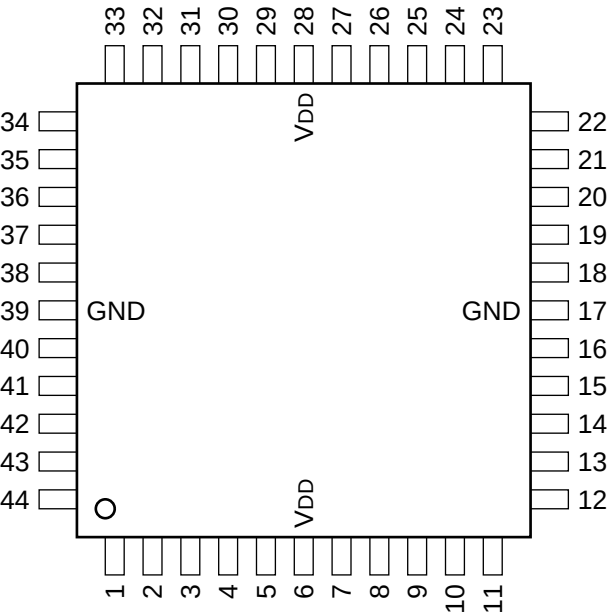


C-MOS HIGH-DENSITY PROGRAMMABLE LOGIC
—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I/O	I/O28	12	I/O	I/O3	23	I/O	I/O12	34	I/O	I/O19
2	I/O	I/O29	13	I/O	I/O4	24	I/O	I/O13	35	I/O	I/O20
3	I/O	I/O30	14	I/O	I/O5	25	I/O	I/O14	36	I/O	I/O21
4	I/O	I/O31	15	I/O	I/O6	26	I/O	I/O15	37	I/O	I/O22
5	I	Y0	16	I/O	I/O7	27	I	Y2/SCLK	38	I/O	I/O23
6	—	VDD	17	—	GND	28	—	VDD	39	—	GND
7	I	ISPEN/NC	18	I/O	SDO/IN1	29	I	Y1/RESET	40	I	IN3
8	I	SDI/IN0	19	I/O	I/O8	30	I	MODE/IN2	41	I/O	I/O24
9	I/O	I/O0	20	I/O	I/O9	31	I/O	I/O16	42	I/O	I/O25
10	I/O	I/O1	21	I/O	I/O10	32	I/O	I/O17	43	I/O	I/O26
11	I/O	I/O2	22	I/O	I/O11	33	I/O	I/O18	44	I/O	I/O27

8	SDI/IN0	I/O0	9
18	SDO/IN1	I/O1	10
30	MODE/IN2	I/O2	11
40	IN3	I/O3	12
		I/O4	13
5	Y0	I/O5	14
29	Y1/RESET	I/O6	15
27	Y2/SCLK	I/O7	16
		I/O8	19
7	ISPEN/NC	I/O9	20
		I/O10	21
		I/O11	22
		I/O12	23
		I/O13	24
		I/O14	25
		I/O15	26
		I/O16	31
		I/O17	32
		I/O18	33
		I/O19	34
		I/O20	35
		I/O21	36
		I/O22	37
		I/O23	38
		I/O24	41
		I/O25	42
		I/O26	43
		I/O27	44
		I/O28	1
		I/O29	2
		I/O30	3
		I/O31	4

INPUT

IN0 - IN3 ; INPUT
 ISPEN ; IN-SYSTEM PROGRAMMING ENABLE
 MODE ; MODE
 RESET ; ACTIVE LOW (0) RESET
 SCLK ; SERIAL CLOCK
 SDI ; SERIAL DATA IN
 Y0 - Y3 ; INPUT CLOCK

OUTPUT

SDO ; SERIAL DATA OUT

INPUT/OUTPUT

I/O0 - I/O31 ; INPUT/OUTPUT

