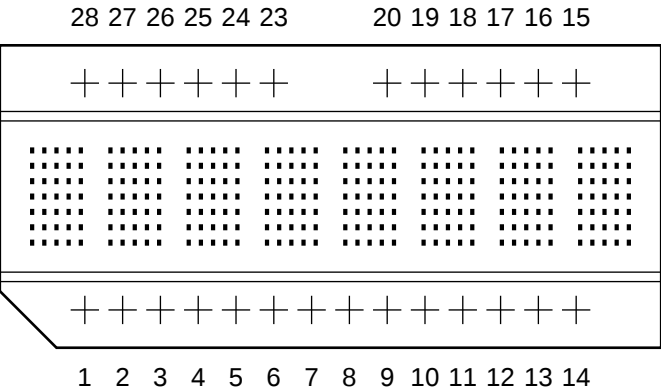

C-MOS 8-CHARACTER DISPLAY (5x7-DOT)
MATRIX ALPHANUMERIC PROGRAMMABLE
- TOP VIEW -



(V_{DD}=+5V)

PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	I	$\overline{\text{RST}}$	15	—	GND
2	I	$\overline{\text{FL}}$	16	—	GND
3	I	A0	17	I	$\overline{\text{CE}}$
4	I	A1	18	—	NC
5	I	A2	19	I	D0
6	I	A3	20	I	D1
7	I	STR	21	—	—
8	I	STR	22	—	—
9	I	STR	23	I	D2
10	—	NC	24	I	D3
11	I	$\overline{\text{CLKSEL}}$	25	I	D4
12	I	CLK I/O	26	I	D5
13	I	$\overline{\text{WR}}$	27	I	D6
14	—	V _{DD}	28	I	D7

A0-A3 ; ADDRESS INPUT
 $\overline{\text{CE}}$; CHIP ENABLE
CLK I/O ; CLOCK INPUT/OUTPUT
 $\overline{\text{CLKSEL}}$; CLOCK SELECT
D0-D7 ; DATA INPUT
 $\overline{\text{FL}}$; FLASH RAM
 $\overline{\text{RST}}$; RESET
STR ; SUBSTR BIAS
 $\overline{\text{WR}}$; WRITE

