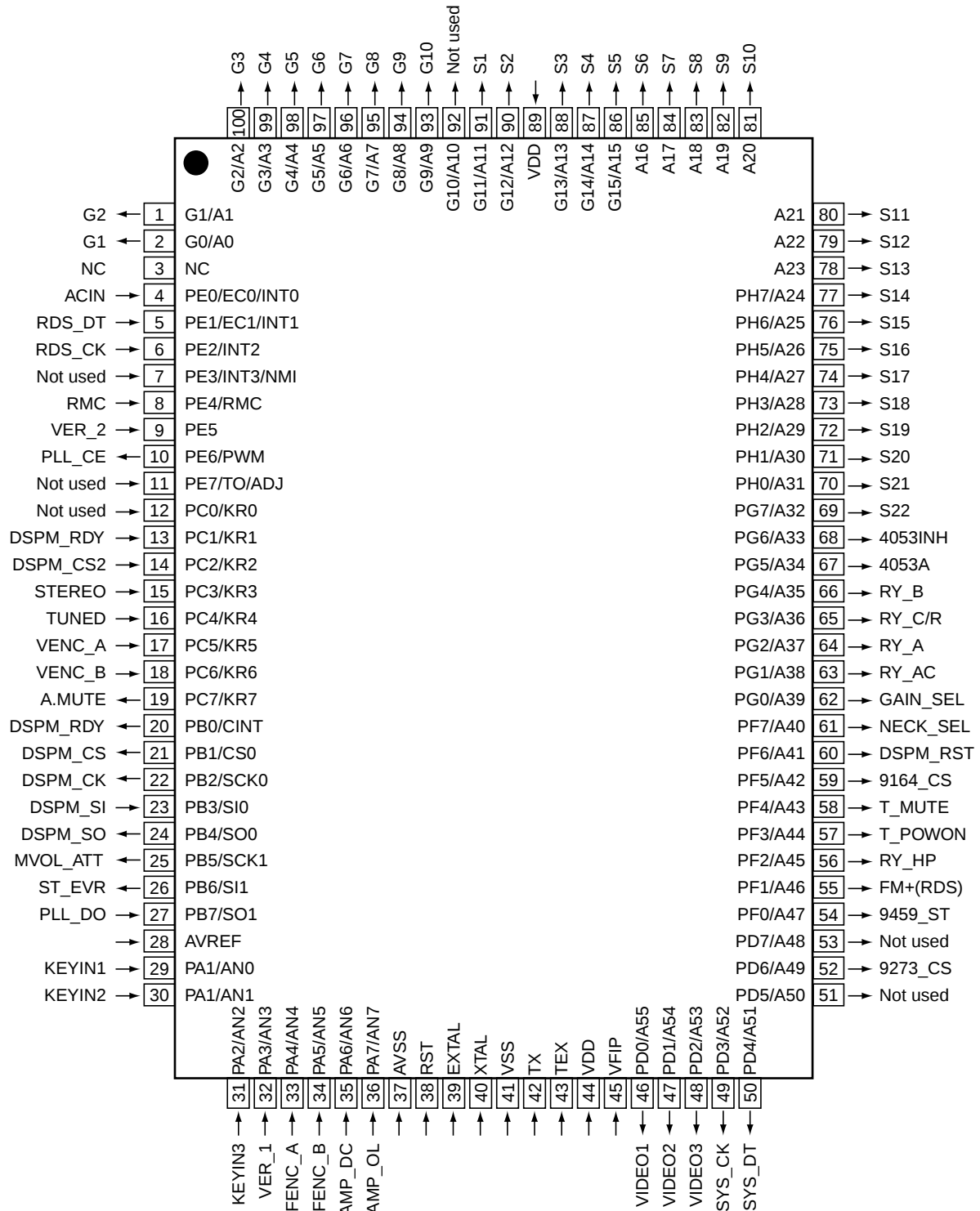


IC INFORMATION		PDG261A		
Function	System Control MCU			1/3
Type	C MOS	Model	VSX-D810S	E

● Pin Arrangement (Top View)



IC INFORMATION		PDG261A		
Function	System Control MCU			2/3
Type	C MOS	Model	VSX-D810S	E

● Pin Function

No.	Pin Name	I/O	Pin Function	Active
1	G2	O	Grid output 2	H
2	G1	O	Grid output 1	H
3	NC	–	Connect to VDD	
4	ACIN	I	AC pulse input	
5	RDS_DT	I	Serial control DATA signal of RDS communication	
6	RDS_CK	I	Serial control CLOCK signal of RDS communication	
7	Not used	I	Connect to GND	
8	RMC	I	Remote control signal input (no-carrier signal)	
9	VER_2	I	Destination switch 2	
10	PLL_CE	O	Chip select signal for communication with LC72131 (tuner)	H
11	Not used	O		
12	Not used	O		
13	DSPM_RDY	I	DSP Micro Computer READY input	
14	DSPM_CS2	I	CS input for DSP Micro Computer	
15	STEREO	I	Stereo/Monoral signal judgment signal	
16	TUNED	I	TUNED information	
17	ENC_A	I	EVOL Rotary encoder signal input A	
18	ENC_B	I	EVOL Rotary encoder signal input B	
19	AMUTE	O	Audio mute	L
20	DSPM_RDY	O	RADY of DSP Micro Computer communication	
21	DSPM_CS1	O	Chip select output for control of DSP Micro Computer	L
22	DSPM_CK	O	Clock signal for communication with DSP Micro Computer	H
23	DSPM_SI	I	DATA input signal for communication with DSP Micro Computer	
24	DSPM_SO	O	DATA output signal for communication with DSP Micro Computer	H
25	MVRATT	O	ATT control of master volume (L : Less than -15dB)	H
26	ST_EVR	O	Strobe signal for communication with electric volume IC	H
27	PLL_DO	I	Data input signal for communication with LC72131 (tuner)	
28	AVref	–	Connect to VDD	
29	KEYIN1	I	Key input A/D conversion port 1	
30	KEYIN2	I	Key input A/D conversion port 2	
31	KEYIN3	I	Key input A/D conversion port 3	
32	VER_1	I	Destination switch (A/D input)	
33	FENC_A	I	FUNC Rotary encoder signal input A	
34	FENC_B	I	FUNC Rotary encoder signal input B	
35	AMP_DC	I	DC abnormality detection of protection circuit (L : Abnormality detection) *1	L
36	AMP_OL	I	Over-load detection of protection circuit (L : Abnormality detection) *2	L
37	AVSS	–	Connect to VSS	
38	RST	–	Reset	
39	EXTAL	–	Connect to the oscillator (7.2MHz)	
40	XTAL	–		
41	VSS	–	Connect to VSS	
42	TX	–	Open	
43	TEX	–	Connect to VSS	
44	VDD	–	+5V	
45	VFDP	–	-30V	
46	VIDEO1	O	NJM2296D control	H
47	VIDEO2			
48	VIDEO3			
49	SYS_DT	O	Data signal for communication with M62446, TC9163, TC9164 and PLL	H
50	SYS_CK	O	Clock signal for communication with M62446, TC9163, TC9164 and PLL	H

*1 : When DC voltage was output from amplifier, "POWER OFF" is displayed at FL display.

*2 : When the output of amplifier became overload state, "OVER LOAD" is displayed at FL display.

IC INFORMATION		PDG261A		
Function	System Control MCU			3/3
Type	C MOS	Model	VSX-D810S	E

No.	Pin Name	I/O	Pin Function	Active
51	Not used			
52	9273_CS	O	TC9273 Chip select	H
53	Not used	O		
54	9459_ST	O	Electronic volume TC5496 Strobe for SB	H
55	FM+(RDS)	O	Tr switch ON/OFF for power supply of RDS decoder (L : AM, power OFF , H : Other)	H
56	RY_HP	–	HP relay ON/OFF	H
57	T_POWON	O	Tuner module ON/OFF (North America model only)	H
58	T_MUTE	O	Tuner mute	H
59	9164 CS	O	TC9163, TC9164 Chip select	
60	DSP_RST	O	DSP Micro Computer reset	
61	NECK_SEL	O	5.1ch, surround mode and A+B Stereo : H / Stereo : L	
62	GAIN_SEL	O	Gain select (5.1ch and Stereo of analog input : H)	H
63	RY_AC	O	AC relay ON/OFF	H
64	RY_A	O	Speaker A relay ON/OFF	H
65	RY_C/R	O	Rear/Center Speaker relay ON/OFF	H
66	RY_B	O	Speaker B relay ON/OFF	H
67	4053A	O	Component terminal control	H
68	4053INH	O	Component terminal control	H
69	S22	O	Segment output 22	H
70	S21		Segment output 21	
71	S20		Segment output 20	
72	S19		Segment output 19	
73	S18		Segment output 18	
74	S17		Segment output 17	
75	S16		Segment output 16	
76	S15		Segment output 15	
77	S14		Segment output 14	
78	S13		Segment output 13	
79	S12		Segment output 12	
80	S11		Segment output 11	
81	S10		Segment output 10	
82	S9		Segment output 9	
83	S8		Segment output 8	
84	S7		Segment output 7	
85	S6		Segment output 6	
86	S5		Segment output 5	
87	S4		Segment output 4	
88	S3		Segment output 3	
89	VDD	–	5V	
90	S2	O	Segment output 2	H
91	S1		Segment output 1	
92	Not used	O	Not used (Fixed Vfdp)	
93	G10	O	Grid output 10	H
94	G9		Grid output 9	
95	G8		Grid output 8	
96	G7		Grid output 7	
97	G6		Grid output 6	
98	G5		Grid output 5	
99	G4		Grid output 4	
100	G3		Grid output 3	