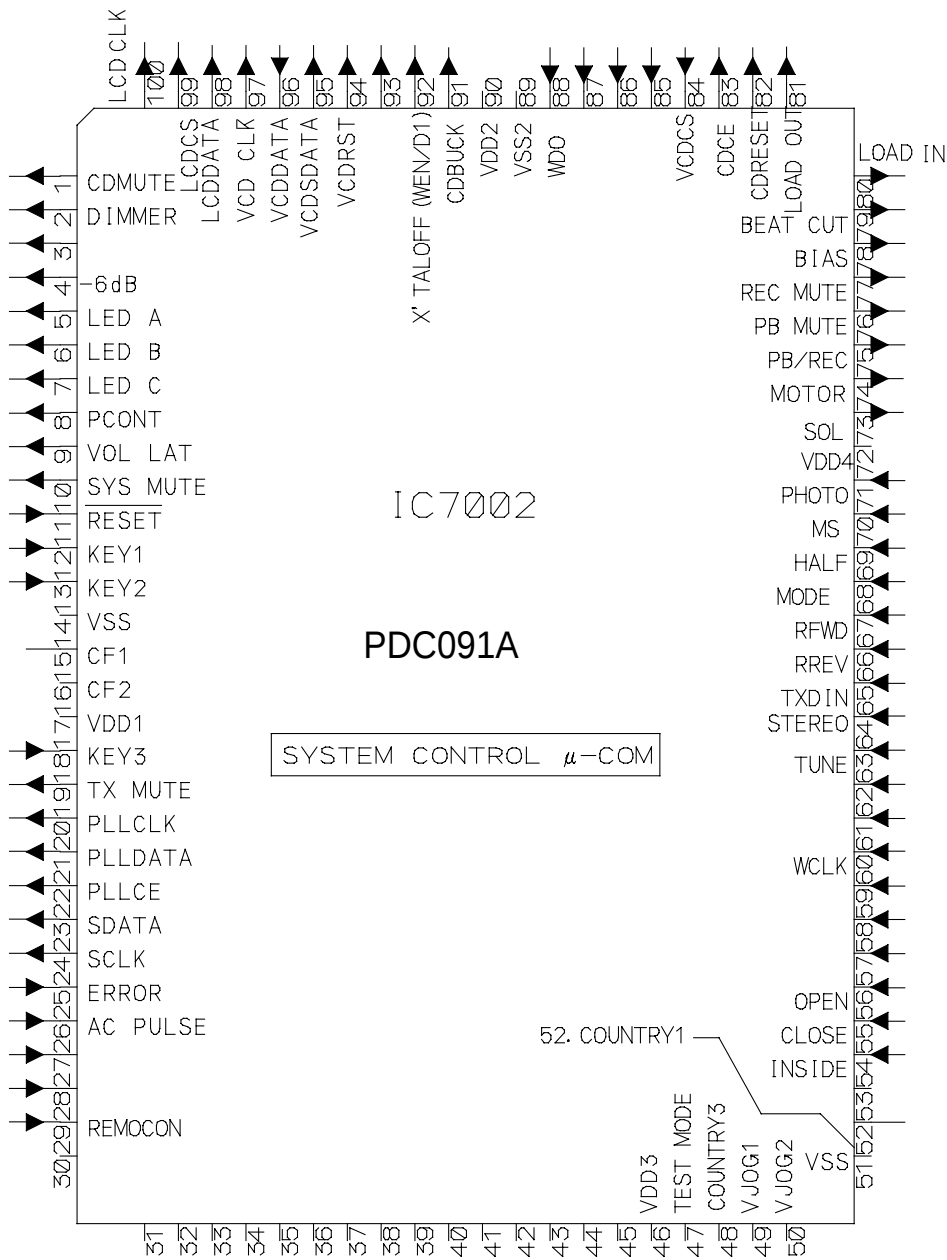


IC INFORMATION		PDC091A		1/3	E
Function	Main Control IC				
Type	C MOS	Model	XR-NM3V		

• Pin Assignment



IC INFORMATION		PDC091A		
Function	Main Control IC			2/3
Type	C MOS	Model	XR-NM3V	E

● Pin Function

No.	Mark and In/Out	Pin Name	I/O	Function
1	P16/T1PWML	CD MUTE	O	CD AUDIO MUTE Output (VCD MODEL : NONE)
2	P17/T1PWMH/BUZ	DIMMER	O	LCD BACK LIGHT DIMMER Control Signal Output
3	P30	-	O	
4	P31	- 6dB	O	AUX Input 6dB Down
5	P32	FUNC LED1	O	FUNCTION IND LED CONTROL 1
6	P33	FUNC LED 2	O	FUNCTION IND LED CONTROL2
7	P34	FUNC LED 3	O	FUNCTION IND LED CONTROL 3
8	P35	PCONT	O	AC RELAY Control Signal Output
9	P36	VOL LAT	O	LC75396(E.VOL IC) LATCH Output
10	P37	SYSMUTE	O	SYSTEM MUTE Control Signal Output
11	RES	RESET	I	HARD Reset Signal Input
12	XT1 / AN10	KEY 1	A/D	KEY-1 Input
13	XT2 / AN11	KEY 2	A/D	KEY-2 Input
14	VSS1	GND	-	GND
15	CF1	CF1	-	Crystal input for system clock
16	CF2	CF2	-	Crystal output for system clock
17	VDD1	VDD	-	VDD
18	P80 / AN0	KEY3	A/D	KEY-3 Input
19	P81 / AN1	TX MUTE	O	TX MUTE/IF REQ Controller output
20	P82 / AN2	PLL CLK	O	LC72131 (PLL IC) Controller CLK output
21	P83 / AN3	PLL DATA	O	LC72131 (PLL IC) Controller DATA output
22	P84 / AN4	PLL CE	O	LC72131 (PLL IC) Controller CE output
23	P85 / AN5	SDATA	O	LC75396(E. VOL IC)/4094 Controller DATA output
24	P86 / AN6	SCLK	O	LC75396(E. VOL IC)/4094 Controller CLK output
25	P87 / AN7 / MICIN	AMP ERROR	A/D	POWER (AC) Detection Signal input
26	P70 / INTO / TOLCP / AN8	AC PULSE	I	POWER / DOAWN AC CLOCK Signal input
27	P71 / INT1 / TOHCP / AN9	-	-	
28	P72 / INT2 / TOIN	-	-	
29	P73 / INT3 / TOIN	SR IN	I	REMOCON Signal input
30	S0 / T0	-	I	
31	S1 / T1	-	I	
32	S2 / T2	-	I	
33	S3 / T3	-	I	
34	S4 / T4	-	I	
35	S5 / T5	-	I	
36	S6 / T6	-	I	
37	S7 / T7	-	I	
38	S8 / T8	-	I	
39	S9 / T9	-	I	
40	S10 / T10	-	I	
41	S11 / T11	-	I	
42	S12 / T12	-	I	
43	S13 / T13	-	I	
44	S14 / T14	-	I	
45	S15 / T15	-	I	
46	VDD3	VDD		VDD
47	S16 / PC0	TESTMODE	I	TEST MODE
48	S17 / PC1	COUNTRY3	I	MODE input
49	S18 / PC2	VJOG1	I	Volume Encoder Signal input 1
50	S19 / PC3	VJOG2	I	Volume Encoder Signal input 2

IC INFORMATION		PDC091A		
Function	Main Control IC			3/3
Type	C MOS	Model	XR-NM3V	
				E

No.	Mark & In/Out	Pin name	I/O	Function
51	VP	GND	-	
52	S20 / PC4	COUNTRY1	I	MODE Input
53	S21 / PC5			
54	S22 / PC6	INSIDE	I	CD MECHA TRAVERSE INSIDE Place SW input
55	S23 / PC7	OPEN	I	CD MECHA TRAY OPEN Place SW input
56	S24 / PD0	CLOSE	I	CD MECHA TRAY CLOSE Place SW input
57	S25 / PD1	NONE	I	NONE
58	S29 / PD2	NONE	I	NONE
59	S27 / PD3	NONE	I	NONE
60	S28 / PD4	WCLK	I	Flash Only
61	S29 / PD5	NONE	I	NONE
62	S30 / PD6	NONE	I	NONE
63	S31 / PD7	TUNE	I	BA1451 (MPX IC) TUNE Signal input for Tuner
64	S32 / PE0	STEREO	I	BA1451 (MPX IC) STEREO Signal input for Tuner
65	S33 / PE1	TXDIN	I	BA1421(MPX IC) Serial DATA Signal input
66	S34 / PE2	RREV	I	DECK MECHA REVERSE REC statute SW input
67	S35 / PE3	RFWD	I	DECK MECHA FOWARD REC statute SW input
68	S36 / PE4	MODE	I	DECK MECHA HEAD MODE SW input
69	S37 / PE5	HALF	I	DECK MECHA HALF Detection SW input
70	S38 / PE6	MS	I	TAPE MS Signal input
71	S39 / PE7	PHOTO	I	DECK MECHA PHOTO TR Detector Signal input
72	VDD4	VDD		VDD
73	S40 / PF0	SDL	O	DECK SOLENOID Control Signal output
74	S41 / PF1	MOTOR	O	DECK MOTOR Control Signal output
75	S42 / PF2	PB/REC	O	TAPE REC/PB MODE Control Signal output
76	S43 / PF3	PB MUTE	O	TAPE PB MUTE Control output
77	S44 / PF4	REC MUTE	O	TAPE REC MUTE Control output
78	S45 / PF5	BIAS CONT	O	TAPE BIAS Control Signal output
79	S46 / PF6	BEAT CUT	O	TAPE BEAT CUT Control Signal output
80	S47 / PF7	LOAD IN	O	CD TRAY LOADING IN Control Signal iutput
81	S48 / PG0	LOAD OUT	O	CD TRAY LOADING OUT Control Signal iutput
82	S49 / PG1	CDRESET	O	CD_ RESET Control Signal output
83	S50 / PG2	CDCE	O	CD_DSP Reset Control Signal output
84	S51 / PG3	VCD CS	I	VCD MECHA CS Signal input
85	P00	-	I/O	
86	P01	-	I/O	
87	P02	-	I/O	
88	P03	WD0	I/O	Flash ROM WEN/D0
89	VSS2	GND		GND
90	VDD2	VDD		VDD
91	P04	-	O	-
92	P05	X'TALOFF	O	CD DSP OUTSIDE X'TAL Control output (Flash ROM WEN/D1)
93	P06		O	
94	P07	VCD RST	O	VCD MECHA RESET Signal output for VCD
95	P10 / SO0	VCD SDATA	O	VCD MECHA Control DATA Ontput
96	P11 / SI0 / SB0	VCD DATA	I	VCD MECHA Control DATA input
97	P12 / SCK0	VCD SCLK	O	VCD MECHA Control CLOCK Ontput
98	P13 / SO1	LCDDATA	O	LCD COG DRIVER Serial DATA output
99	P14 / SI1 / SB1	LCDCS	O	LCD COG DRIVER CHIP SELECT
100	P15 / SCK1	LCDCLK	O	LCD COG DRIVER Serial CLOCK output