

IC INFORMATION		PDC076A		
Function	Microcomputer IC	Model	XV-HTD510	1/2
Type	C MOS			E

● Pin Function

No.	Mark	Pin Name	I/O	Function
1	P16/T1PWML	XDSP MUTE	O	DSP mute output
2	P17/T1PWMH/BUZ	XCDC RST	O	CODEC reset
3	P30	CDC DO	O	CODEC Data
4	P31	CDC CK	O	CODEC Clock
5	P32	XCDC CS	O	CODEC Chip Select
6	P33	XDSP RST	O	DSP Reset
7	P34	XDSP SUB CS	O	DSP Chip Select (Sub)
8	P35	XDSP MAIN CS	O	DSP Chip Select (Main)
9	P36	WEN/DI	I/O	for DOWN LOAD
10	P37	WDO		
11	RES	XRESET		u-P Reset
12	XT1/AN10	XTESTMODE	I	system test mode input
13	XT2/AN11	3.3DETECT (A/D)	I	(DVD) 3.3V Detection
14	VSS1	GNDD	–	–
15	CF1	CF1		Crystal Oscillator
16	CF2	CF2		
17	VDD1	V+5E	–	–
18	P80/AN0	KEY 0	A/D	Button control (Disp. Assy)
19	P81/AN1	ST/TUNE	A/D	TUNER STEREO TUNED detection
20	P82/AN2	MODEL 0 (A/D)	A/D	Determine Model intended for
21	P83/AN3	FAN DET	A/D	Fan Detection
22	P84/AN4	KEY 1	A/D	Button control (Disp. Assy)
23	P85/AN5	JOG	A/D	(Disp. Assy)
24	P86/AN6	XSTBY LED	O	
25	P87/AN7	NC		
26	P70/INT0/T0LCP/AN8	AC	I	AC pulse detection
27	P71/INT1/T0HCP/AN9	LT11	I	(DVD)
28	P72/INT2/T0IN	RDS CK	I	RDS Clock
29	P73/INT3/T0IN	REMOCON	I	Remote control (Disp. Assy)
30	S0/T0	G01	O	Grid
31	S1/T1	G02		
32	S2/T2	G03		
33	S3/T3	G04		
34	S4/T4	G05		
35	S5/T5	G06		
36	S6/T6	G07		
37	S7/T7	G08		
38	S8/T8	G09		
39	S9/T9	G10		
40	S10/T10	G11		
41	S11/T11	G12		
42	S12/T12	G13		
43	S13/T13	NC		
44	S14/T14	NC		
45	S15/T15	NC		
46	VDD3	V+5E	–	–
47	S16/PC0	S01	O	Segment
48	S17/PC1	S02		
49	S18/PC2	S03		
50	S19/PC3	S04		

IC INFORMATION		PDC076A		
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Type	C MOS	Model	XV-HTD510	

No.	Mark	Pin Name	I/O	Function
51	VP	GNDD	—	—
52	S20/PC4	S05	O	Segment
53	S21/PC5	S06		
54	S22/PC6	S07		
55	S23/PC7	S08		
56	S24/PD0	S09		
57	S25/PD1	S10		
58	S26/PD2	S11		
59	S27/PD3	S12		
60	S28/PD4	S13		
61	S29/PD5	S14		
62	S30/PD6	S15		
63	S31/PD7	S16		
64	S32/PE0	S17		
65	S33/PE1	S18		
66	S34/PE2	NC		
67	S35/PE3	EXP CLK	O	LED driver IC clock
68	S36/PE4	EXP DATA	O	LED driver IC data
69	S37/PE5	EXP CE	O	LED driver IC CE
70	S38/PE6	DIR ERR	I	DIR Error
71	S39/PE7	RDS DATA	I	RDS Data
72	VDD4	V+5E	—	—
73	S40/PF0	RYDE	I	SP Relay's ENABLE
74	S41/PF1	HP DETECT	I	Headphone Detect
75	S42/PF2	DIR DI	I	DIR Data In
76	S43/PF3	TX DATA	I	Data from PLL IC
77	S44/PF4	RDS ON/OFF	O	RDS IC oscillator ON/OFF
78	S45/PF5	TX MUTE	O	Tuner MUTE
79	S46/PF6	X AOUT MUTE	O	Audio Out mute
80	S47/PF7	NC	O	NC
81	S48/PG0	XDIR RST	O	DIR Reset
82	S49/PG1	POWER	O	system power control
83	S50/PG2	XRDY1	O	DVD MCU interface READY output
84	S51/PG3	DVD ON/OFF	O	DVD power control
85	P00	SYS CK	O	System Clock
86	P01	SYSDATA	O	System Data
87	P02	XDVDRST	O	DVD Reset
88	P03	XLINE MUTE	O	system audio MUTE
89	VSS2	GNDD	—	—
90	VDD2	V+5E	—	—
91	P04	MAIN VOL CE	O	MAIN VOL IC M62446 CE
92	P05	PLL CE	O	PLL IC CE
93	P06	DIR CE	O	DIR Chip Enable
94	P07	WCLK	O	for DOWNLOAD
95	P10/SO0	DSP SO	O	DATA to YSS912
96	P11/SI0/SB0	DSP SI	I	DATA from YSS912
97	P12/SCK0	DSP CK	O	YSS912 (DSP) Clock
98	P13/SO1	SSI	O	Data to DVD MCU
99	P14/SI1/SB1	SSO1	I	Data from DVD MCU
100	P15/SCK1	SSCK	O	DVD MCU clock