

IC INFORMATION		PD5693A		
Function	VCD micro computer			1/2
Type	C MOS	Model	XR-VS400	E

●Pin Function

No.	Mark	Pin Name	Type	Function
1	P96/ANEX1/Sout4	SDI	O	STA013 bit stream Communication data output
2	P95/ANEX0/CLK4	SCKR	O	STA013 bit stream Communication clock output
3	P94/DA1/TB4in	HRDY	I/O	VCD IC communication permission input
4	P93/DA0/TB3in	HSEL	O	VCD IC communication data selection output
5	P92/TB2in/Sout3	HDI	O	VCD IC communication data output
6	P91/TB1in/Sin3	HDO	I	VCD IC communication data input
7	P90/TB0in/CLK3	HCLK	O	VCD IC communication clock output
8	BYTE	BYTE	I	External data bus change terminal
9	CNVss	CNVSS	I	The terminal for rewriting distinction
10	P87/Xcin	LOADIN	O	\$M Loading output
11	P86/Xcout	LOADOUT	O	\$M Loading output
12	RESET	XVCRST	I	Reset input
13	Xout	XOUT	O	Main clock oscillation output
14	Vss	VSS	I	GND
15	Xin	XIN	I	Main clock oscillation input
16	Vcc	VCC	I	Power supply voltage (2.7-5.5V)
17	P85/NMI	NM1	I	Power supply voltage (2.7-5.5V)
18	P84/INT2	DATA_REQ	I	The data transmission demand from STA013
19	P84/INT2	XINT1	I	The interruption demand from LC895199
20	P82/INT0	XINT0	I	The interruption demand from LC895199
21	P81/TA4in/U	OPEN	I	\$M OPEN SW input
22	P80/TA4out/U	CLAMP	I	\$M CLAMP SW input
23	P77/TA3in	DACLT	O	PCM1716E latch output
24	P76/TA3out	DACDATA	O	PCM1716E data output
25	P75/TA2in/W	DACCLK	O	PCM1716E clock output
26	P74/TA2out/W	MP3/CD	O	DAC (PCM1742) incoming signal selection output
27	P73/CTS2/RTS2/TA1in/V	XST_RST	O	STA013 reset output
28	P72/CLK2/TA1out/V	DISC	I	\$M DISC position detection SW input
29	P71/RxD2/SCL/TA0in/TB5in	I2CCLK	O	The I2C clock output for STA013
30	P70/TxD2/SDA/TA0out	I2CDATA	I/O	The I2C data output for STA013
31	P67/TxD1	TXD1	O	The time data output of flash writing
32	P66/RxD1	RXD1	I	The time data input of flash writing
33	P65/CLK1	CLK1	I	The time clock input of flash writing
34	P64/CTS1/RTS1/CLKS1	RTS	O	The reset output of flash writing
35	P63/TxD0	VDV	O	The communication data output with a system microcomputer
36	P62/RxD0	VDS	I	The communication data input with a system microcomputer
37	P61/CLK0	VCLK	I/O	The communication clock input with a system microcomputer
38	P60/CTS0/RTS0	VCS	I/O	A communication chip selection with a system microcomputer
39	P57/RDY/CLKout	NC	I/O	-
40	P56/ALE	NC	I/O	-
41	P54/HOLD	NC	I/O	-
42	P54/HLDA	NC	I/O	-
43	P53/BCLK	NC	I/O	-
44	P52/RD	XRD	O	The data read-out signal output of LC895199
45	P51/WRH/BHE	NC	I/O	-
46	P50/WRL/WR	XWR	O	The data write-in signal output of LC895199
47	P47/CS3	XCS	O	LC895199 chip selection
48	P46/CS2	XSWAIT	I	WAIT signal from LC895199
49	P45/CS1	CDBUS3	I/O	TC9495F data BUS communication data input and output
50	P44/CS0	CRBUS2	I/O	TC9495F data BUS communication data input and output

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Type	C MOS	Model	XR-VS400	
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No.	Mark	Pin Name	Type	Function
51	P43/A19	CVBUS1	I/O	TC9495F data BUS communication data input and output
52	P43/A18	CVBUS0	I/O	TC9495F data BUS communication data input and output
53	P43/A17	CDBCE	O	TC9495F chip enable output
54	P43/A16	CDBCLK	O	TC9495F BUS clock output
55	P43/A15	NC	I/O	-
56	P43/A14	NC	I/O	-
57	P43/A13	NC	I/O	-
58	P43/A12	NC	I/O	-
59	P43/A11	NC	I/O	-
60	P43/A10	NC	I/O	-
61	P43/A9	NC	I/O	-
62	Vcc	VCC	I	Power supply voltage (2.7-5.5V)
63	P30/A8(I-I7)	NC	I/O	-
64	Vss	VSS	I	GND
65	P27/A7(D7/D6)	NC	I/O	-
66	P26/A6(D6/D5)	SUA6	O	LC895199 adress signal output
67	P25/A5(D5/D4)	SUA5	O	LC895199 adress signal output
68	P24/A4(D4/D3)	SUA4	O	LC895199 adress signal output
69	P24/A4(D4/D2)	SUA3	O	LC895199 adress signal output
70	P24/A4(D4/D1)	SUA2	O	LC895199 adress signal output
71	P24/A4(D4/D0)	SUA1	O	LC895199 adress signal output
72	P20/A0(D0/-)	SUA0	O	LC895199 adress signal output
73	P17/D15/INT5	NC	I/O	-
74	P17/D14/INT5	WAKEUP	I/O	NC
75	P17/D13/INT5	HINT	I	VCD IC communication interruption demand
76	P14/D12	VNR	O	VNR ON/OFF output
77	P14/D11	CD INSIDE	I	CD INSIDE SW input
78	P14/D10	CD MUTE	O	CD MUTE output
79	P11/D9	XRSTOUT	O	CD DSP and MPEG DECODER reset output
80	P10/D8	XRESET	O	LC895199 reset output
81	P07/D7	D7	I/O	The data bus with LC895199
82	P06/D6	D6	I/O	The data bus with LC895199
83	P05/D5	D5	I/O	The data bus with LC895199
84	P04/D4	D4	I/O	The data bus with LC895199
85	P03/D3	D3	I/O	The data bus with LC895199
86	P02/D2	D2	I/O	The data bus with LC895199
87	P01/D1	D1	I/O	The data bus with LC895199
88	P00/D0	D0	I/O	The data bus with LC895199
89	P107/AN7/KI3	DETECT5	I	5[V] DETECT
90	P106/AN6/KI2	DETECT3R3	I	3.3[V] DETECT
91	P105/AN5/KI1	TEST KEY IN	I	Key input for Unit check
92	P104/AN4/KI0	XL0MUTE	O	Driver MUTE output
93	P103/AN3	TEST1	I	CD TEST input
94	P102/AN2	MODEL	I	MODEL distinction input
95	P101/AN1	NC	I/O	Output for change input or output of TC74HC32
96	AVss	AVSS	I	A-D conversion machine power supply voltage (Vss)
97	P100/AN0	BIT EN	O	STA013 bit sutorumu DATA Permission output
98	Vref	VREF	I	A-D conversion machine standard voltage input
99	AVcc	AVCC	I	A-D conversion machine power supply voltage (Vcc)
100	P97/ADtrg/Sin4	NC	I/O	-