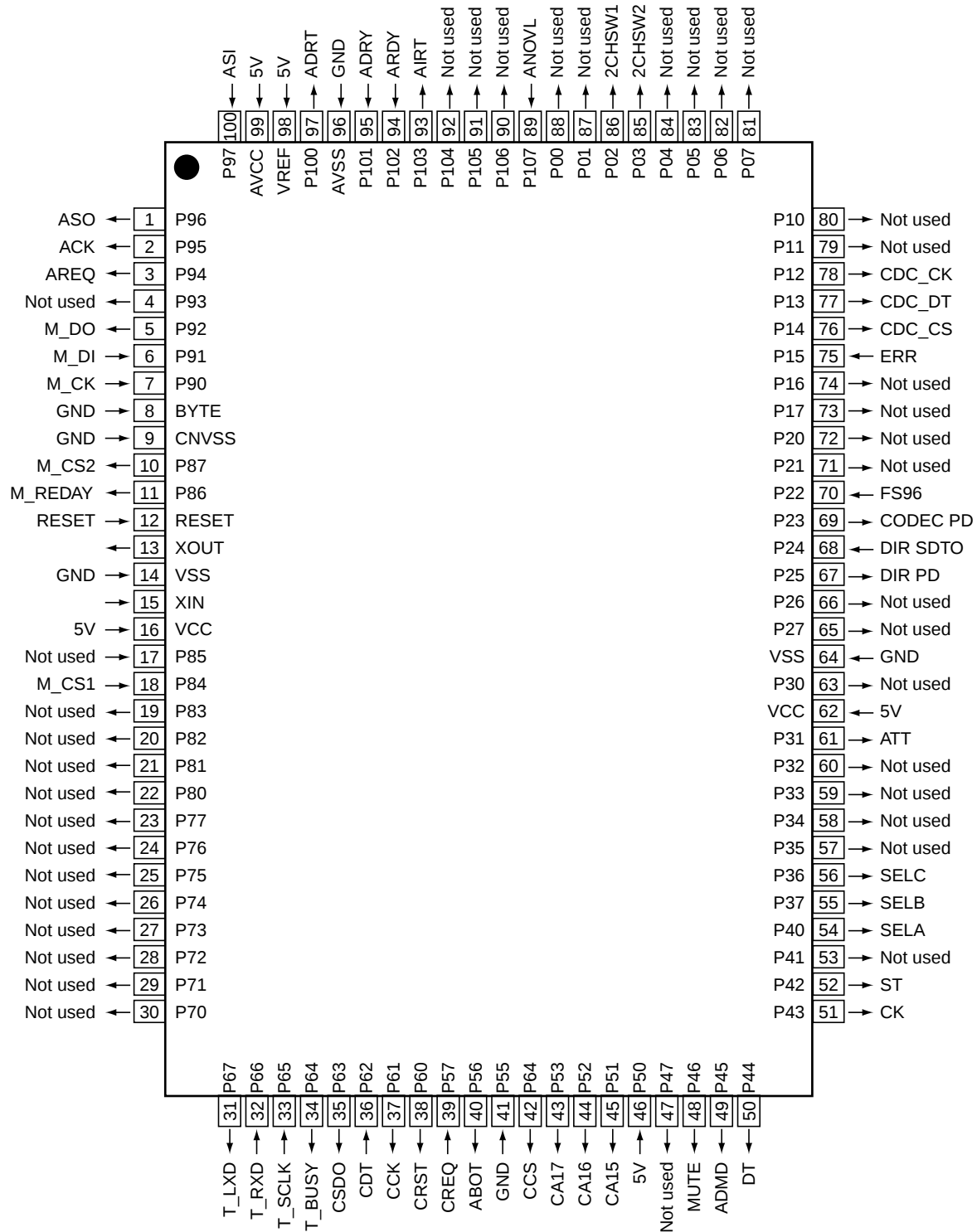


IC INFORMATION		PD5634A		
Function	Receiver Control MCU			1/3
Type	C MOS	Model	VSX-D810S	E

● Pin Arrangement (Top View)



IC INFORMATION		PD5634A		
Function	Receiver Control MCU			2/3
Type	C MOS	Model	VSX-D810S	E

● Pin Function

No.	Pin Name	I/O	Pin Function	Active
1	ASO	O	AK7706 DATA output	
2	ACK	O	AK7706 CLOCK output	
3	AREQ	O	AK7706 REQ	
4	Not used	O		
5	M_DO	O	DATA OUT to Main microcomputer	
6	M_DI	I	DATA IN from Main microcomputer	
7	M_CK	I	CLOCK IN for Main microcomputer communication	
8	BYTE	I	GND	
9	T_PGM/OE	I	Pull down to CNVss, GND (For FLASH rewriting)	
10	M_CS2	O	CS output for Main microcomputer communication	
11	M_READY	O	READY output for Main microcomputer communication	
12	RESET	–	Reset (from Main microcomputer)	
13	XOUT	–	Oscillator	
14	VSS	–	GND	
15	XIN	–	Oscillator	
16	VCC	–	5V	
17	MN1	I	5V	
18	M_CS1	I	CS input for Main microcomputer communication	
19	Not used	O		
20	Not used	O		
21	Not used	O		
22	Not used	O		
23	Not used	O		
24	Not used	O		
25	Not used	O		
26	Not used	O		
27	Not used	O		
28	Not used	O		
29	Not used	O		
30	Not used	O		
31	TXD	O	For FLASH rewriting	
32	RXD	I	For FLASH rewriting	
33	T-SCLK	I	For FLASH rewriting	
34	T-BUSY	O	For FLASH rewriting	
35	CSDO	O	DATA OUT for Crystal DSP	
36	CDT	I	DATA IN for Crystal DSP	
37	CCK	O	CLK for Crystal DSP	
38	CRST	O	RESET for Crystal DSP	
39	CREQ	I	REQ for Crystal DSP	
40	ABOT	O	ABOOT for Crystal DSP	
41	VSS	–		
42	CCS	O	CS for Crystal DSP	
43	CA17	O	For Crystal DSP	
44	CA16	O	For Crystal DSP	
45	CA15	O	For Crystal DSP	
46	T-Vdd	I	5V	
47	Not used	O		
48	MUTE	O	For E-SW (TC9164)	
49	ADMD	O	For E-SW (TC9164)	
50	DT	O	For E-SW (TC9164)	

IC INFORMATION		PD5634A		
Function	Receiver Control MCU			3/3
Type	C MOS	Model	VSX-D810S	E

No.	Pin Name	I/O	Pin Function	Active
51	CK	O	For E-SW (TC9164)	
52	ST	O	For E-SW (TC9164)	
53	Not used	O		
54	SELA	O	For LOGIC IC, TC74ACT151F	
55	SELB	O	For LOGIC IC, TC74ACT151F	
56	SELC	O	For LOGIC IC, TC74ACT151F	
57	Not used	O		
58	Not used	O		
59	Not used	O		
60	Not used	O		
61	ATT	O	ATT output	
62	VCC	–	5V	
63	Not used	O		
64	VSS	–	GND	
65	Not used	O		
66	Not used	O		
67	DIR_PD	O	Power down for DIR	
68	DIR_SDTO	I	DIR DATA IN	
69	CDC_PD	O	Power down for CODEC	
70	FS96	I	96K	
71	Not used	O		
72	Not used	O		
73	Not used	O		
74	Not used	O		
75	ERR	I	DIR	
76	CDC_CS	O	CS for CODEC communication	
77	CDC_DT	O	DATA OUT for CODEC communication	
78	CDC_CK	O	Clock for CODEC communication	
79	Not used	O		
80	Not used	O		
81	Not used	O		
82	Not used	O		
83	Not used	O		
84	Not used	O		
85	2CHSW2	O	For TC9215 control (SW H : PASSIV, L : POWERED)	
86	2CHSW1	O	For TC9215 control (SB H : DSP, L : 6CH)	
87	Not used	O		
88	Not used	O		
89	ANOV	I	Analog input overload detection (A/D)	
90	Not used	O		
91	Not used	O		
92	Not used	O		
93	AIRT	O	AK7706DSP INIT reset (control it only in a power start)	
94	ARDY	I	AK7706 writing READY input	
95	ADRY	I	AK7706 output data READY input	
96	AVSS	–	GND	
97	ADRT	O	AK7706 DSP reset	
98	VREF	–	5V	
99	AVCC	–	5V	
100	ASI	I		