

IC INFORMATION		PD3410A		
Function	System Control IC			1/4
Type		Model	DV-343	E

● Pin Function

No.	Mark	Pin Name	I/O	Function
1	XCS3/XCASL	XCS3	O	PD4995A (MY CHIP) chip select signal output
2	GND	GND	–	GND
3	CK	HCPUCK	O	N.C.
4	VCC	V+3D	–	V+3D
5	PICLK	–	I/O	N.C.
6	PIDATA	–	I/O	N.C.
7	GND	GND	–	GND
8	PORTH0	–	O	N.C.
9	PORTH1	–	O	N.C.
10	PORTH2	36MVH	O	BU2158F (Clock generator) [WY model]
11	PORTH3	V_SEL2	O	Composite/S switching signal output of the skirt terminal
12	VCC	V+3D	–	V+3D
13	PORTH4	–	O	N.C.
14	PORTH5	–	O	N.C.
15	PORTH6	–	O	N.C.
16	PORTH7	–	O	N.C.
17	GND	GND	–	GND
18	EXTAL	EXTAL	I	Connect a ceramic resonator
19	XTAL	XTAL	O	
20	VCC	V+3D	–	V+3D
21	PORTG0	XCSDFO	O	DAC chip select signal output (←XLAT3)
22	PORTG1	–	O	N.C.
23	PORTG2	–	O	N.C.
24	PORTG3	–	O	N.C.
25	PORTG4	–	O	N.C.
26	GND	GND	–	GND
27	PORTG5	–	O	N.C.
28	PORTG6	–	O	N.C.
29	PORTG7	XAMUTE	O	Last stage mute signal output of the audio
30	PORTF0	44X48	O	DAC 44/48 FS switching signal output
31	PORTF1	–	I	N.C.
32	PORTF2	3DON	O	3D audio ON/bypass switching signal output
33	VCC	V+3D	–	V+3D
34	PORTF3	–	O	N.C.
35	PORTF4	XAVSRST	O	Sync. reset port
36	PORTF5	–	O	N.C.

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No.	Mark	Pin Name	I/O	Function
37	PORTF6	–	O	N.C.
38	PORTF7	XCSVE	O	Serial communication enable signal output of the video encoder [WY model]
39	GND	GND	–	GND
40	AVSS	GND	–	GND
41	AVCC	V+3D	–	V+3D
42	OUTA_P	LODRV	O	Loading drive output
43	VREF	V+3D	–	V+3D
44	OUTB_P	TEI	O	Tracking offset signal output
45	AVSS	GND	–	GND
46	AVSS	GND	–	GND
47	PORTE0	V_SEL	O	Component/composite switching signal output
48	PORTE1	–	I	N.C.
49	PORTE2	–	I	N.C.
50	PORTE3	FOFST1	I/O	Focus offset adjustment output 1
51	PORTE4	FOFST2	I/O	Focus offset adjustment output 2
52	PORTE5	XDFINH	I/O	Defect shunt signal output
53	PORTE6	DVD/XCD	O	DVD/CD switching signal output
54	PORTE7	LD1_ON	O	650 nm laser diode ON signal output
55	PORTD0	LD2_ON	O	780 nm laser diode ON signal output
56	VCC	V+3D	–	V+3D
57	PORTD1	DPD/TE	O	1 beam/3 beams switching signal output
58	PORTD2	AGOFF	O	AGC ON/OFF switching signal output of RF IC
59	PORTD3	XCD2X	O	Signal output for switching the double speed playback (VCD)
60	PORTD4	OEICG	O	OEIC gain switching signal output
61	GND	GND	–	GND
62	PORTD5	XMON	O	ON/OFF switching signal output of the spindle motor control output
63	PORTD6	–	O	N.C.
64	PORTD7	–	I	N.C.
65	PORTJ0	XDRVMUT	O	Driver mute output
66	PORTJ1	–	O	N.C.
67	PORTJ2	XDSPRST	O	Servo DSP reset
68	PORTJ3	–	I	N.C.
69	VCC	V+3D	–	V+3D
70	PORTJ4	TM_ENT	I	Test mode entry
71	PORTJ5	–	O	N.C.
72	PORTJ6	VSEL_SW	I	Component/composite SW input
73	PORTJ7	–	I	N.C.
74	PB0/TIOCA2	XCBUSY	I	Command busy input
75	PB1/TIOCB2	XABUSY	I	Auto-sequence busy input
76	PB2/TIOCA3	XINT2	I	Interrupt input 2 (AV-1)
77	VCC	V+3D	–	V+3D
78	PB3/TIOCB3	LT1	O	Communication response signal output to the FL controller
79	PB4/TIOCA4	SBSY	I	Subcode block sync. input
80	XMTEST	–	I	Test terminal (V+3D)
81	XCPUMD	–	I	Test terminal (V+3D)
82	XRES	XRESET	I	Reset input

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No.	Mark	Pin Name	I/O	Function
83	GND	GND	–	GND
84	AN0	LODPOS	I	Loading position input
85	AN1	SLDPOS	I	Slider position input
86	AN2	–	I	N.C.
87	AN3	NAP_SW	I	NTSC/AUTO/PAL SW input
88	AN4	XOEM	I	Input terminal of OEM model protection
89	AN5	LDDEAD	I	Input for LD current value display
90	AN6	–	I	N.C.
91	AN7	–	I	N.C.
92	Avref	V+3D	–	V+3D
93	AVCC	V+3D	–	V+3D
94	AVSS	GND	–	GND
95	PB5/TIOCB4	–	I	N.C.
96	PB6/TIOCA4/TCLKC	C2F	I	C2 error input
97	PB7/TIOCB4/TCLKD	XRDY	I	Communication request input from the FL controller
98	PB8/RxD0	SSI	I	Serial data input (FL controller)
99	PB9/TxD0	SSO	O	Serial data output (FL controller)
100	VCC	V+3D	–	V+3D
101	PB10/RxD1	RXD	I	Data input of the RS-232C
102	PB11/TxD1	TXD	O	Data output of the RS-232C
103	PB12/XIRQ4/SCK0	SSCK	I/O	Serial clock output (FL controller)
104	PB13/XIRQ5/SCK1	XIRQL10	I	Interrupt input #0 (MY CHIP)
105	GND	GND	–	GND
106	PB14/XIRQ6	XIRQL11	I	Interrupt input #1 (MY CHIP)
107	PB15/XIRQ7	XINT0	I	Interrupt input #0 (AV-1)
108	PA0/XCS4/TIOCA0	XCS4	O	Servo DSP chip select signal output
109	PA1/XCS5/XRAS	–	O	N.C.
110	PA2/XCS6/TIOCB0	XCS6	O	AV-1 chip select signal output
111	XWAIT	XWAIT	I	Wait signal input
112	XWRL	XWRL	O	Write pulse output L
113	GND	GND	–	GND
114	XWRH	XWRH	O	Write pulse output H
115	XRD	XRD	O	Read pulse output
116	PA7/XBACK	XCURDET	I	Over-current detection signal input
117	PA8/XBREQ	CTS	I	RS-232C transfer permit input
118	PA9/XAH/XIRQOUT/ XADTRG	DTR	O	RS-232C transfer permit output
119	PA10/DPL/TIOCA1	XINT1	I	Interrupt input 1 (AV-1)
120	PA11/DPH/TIOCB1	THLD	I	Tracking hold signal input
121	VCC	V+3D	–	V+3D
122	PA12/XIRQ0/DACK0/ TCLKA	DACK0	O	DMA response output (MY CHIP)
123	PA13/XIRQ1/ XDREQ0/TCLKB	XDREQ0	I	DMA request input (MY CHIP)
124	PA14/XIRQ2/XDACK1	XDACK1	O	DMA response output (AV-1)
125	PA15/XIRQ3/XDREQ1	XDREQ1	I	DMA request input (AV-1)
126	AD0	D0	I/O	Data bus 0

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No.	Mark	Pin Name	I/O	Function
127	GND	GND	–	GND
128	AD1	D1	I/O	Data bus 1
129	AD2	D2	I/O	Data bus 2
130	AD3	D3	I/O	Data bus 3
131	AD4	D4	I/O	Data bus 4
132	AD5	D5	I/O	Data bus 5
133	AD6	D6	I/O	Data bus 6
134	VCC	V+3D	–	V+3D
135	AD7	D7	I/O	Data bus 7
136	AD8	D8	I/O	Data bus 8
137	AD9	D9	I/O	Data bus 9
138	AD10	D10	I/O	Data bus 10
139	GND	GND	–	GND
140	AD11	D11	I/O	Data bus 11
141	AD12	D12	I/O	Data bus 12
142	AD13	D13	I/O	Data bus 13
143	AD14	D14	I/O	Data bus 14
144	VCC	V+3D	–	V+3D
145	AD15	D15	I/O	Data bus 15
146	A0 (XHBS)	A0	O	Address bus 0
147	A1	A1	O	Address bus 1
148	A2	A2	O	Address bus 2
149	GND	GND	–	GND
150	A3	A3	O	Address bus 3
151	A4	A4	O	Address bus 4
152	A5	A5	O	Address bus 5
153	A6	A6	O	Address bus 6
154	A7	A7	O	Address bus 7
155	A8	A8	O	Address bus 8
156	A9	A9	O	Address bus 9
157	A10	A10	O	Address bus 10
158	A11	A11	O	Address bus 11
159	A12	A12	O	Address bus 12
160	A13	A13	O	Address bus 13
161	A14	A14	O	Address bus 14
162	A15	A15	O	Address bus 15
163	A16	A16	O	Address bus 16
164	A17	A17	O	Address bus 17
165	VCC	V+3D	–	V+3D
166	A18	A18	O	Address bus 18
167	A19	A19	O	Address bus 19
168	A20	A20	O	Address bus 20 [RAM model]
169	A21	A21	O	N.C.
170	XNMI	XNMI	I	V+3D
171	GND	GND	–	GND
172	XCS10	–	O	N.C.
173	XCS20	XCS20	O	Chip select signal output of the flash ROM
174	XCS22	–	O	Chip select signal output of the GUI ROM [OEM model]
175	XCS23	XCS23	O	Chip select signal output of the SRAM
176	XCS2	–	O	N.C.