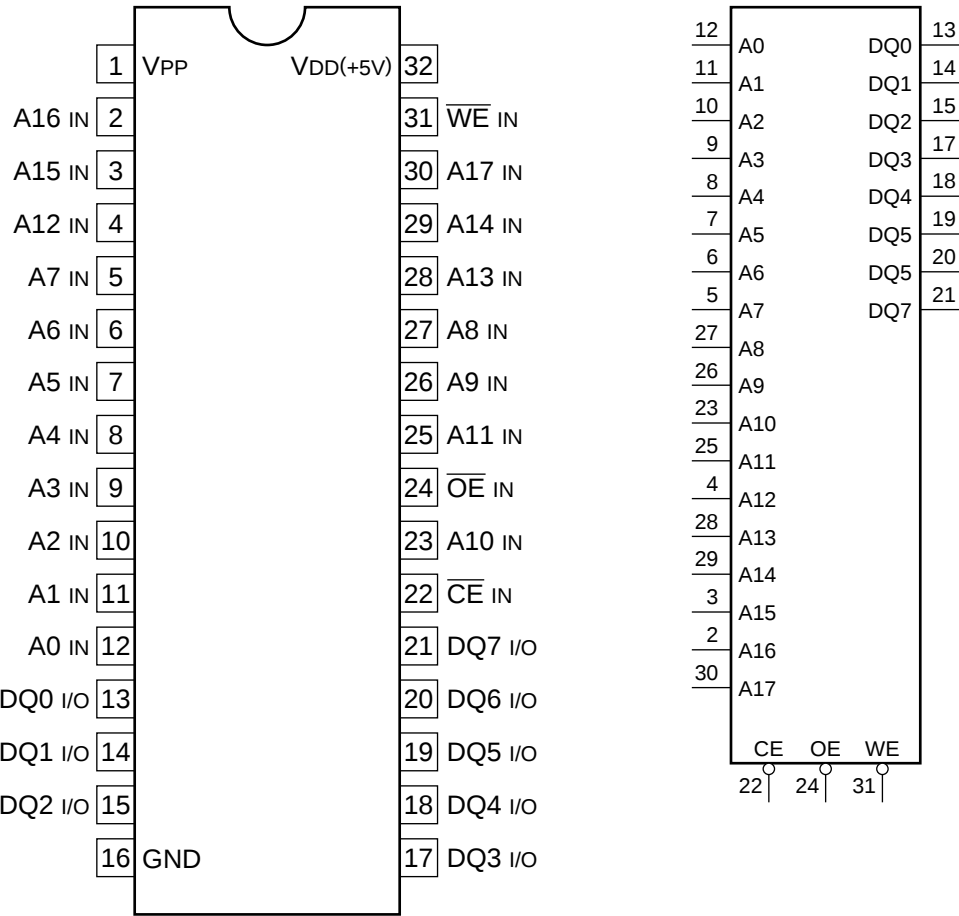

P28F020-150(1/2)
IL00

C-MOS 2048K-BIT FLASH MEMORY
-TOP VIEW-



A0 - A17 ; ADDRESS INPUTS
CE ; CHIP ENABLE INPUT
OE ; OUTPUT ENABLE INPUT
WE ; WRITE ENABLE INPUT
DQ0 - DQ7 ; DATA INPUTS / OUTPUTS
VPP ; ERASE / PROGRAM POWER SUPPLY

OPERATION		V _{PP}	A0	A9	$\overline{CE}$	$\overline{OE}$	$\overline{WE}$	DQ0 - DQ7
READ - ONLY	READ	0	A0	A9	0	0	1	DATA OUT
	OUTPUT DISABLE	0	X	X	0	1	1	TRI - STATE
	STANDBY	0	X	X	1	X	X	TRI - STATE
	INTELLIGENT IDENTIFIER(MFR)	0	0	V _{ID}	0	0	1	DATA = 89H
	INTELLIGENT IDENTIFIER(DEVICE)	0	1	V _{ID}	0	0	1	DATA = BDH
READ / WRITE	READ	1	A0	A9	0	0	1	DATA OUT
	OUTPUT DISABLE	1	X	X	0	1	1	TRI - STATE
	STANDBY	1	X	X	1	X	X	TRI - STATE
	WRITE	1	A0	A9	0	1	0	DATA - IN

0 ; LOW LEVEL

1 ; HIGH LEVEL

X ; DON'T CARE

V_{ID} ; V_{ID} is the intelligent identifier high voltage.(11.50 - 13.00V)