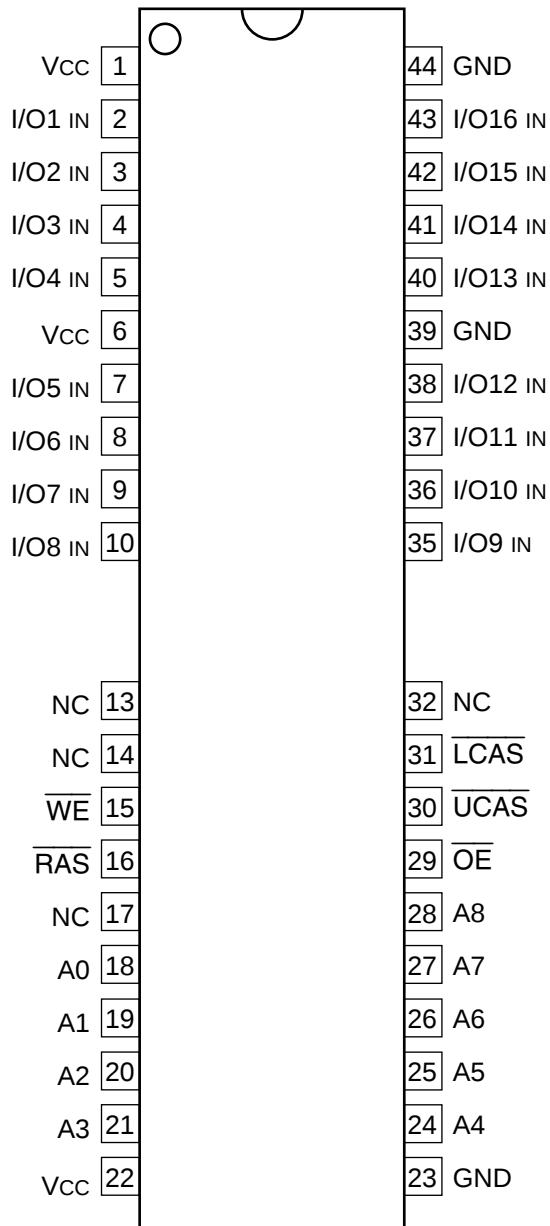


4 M (256 K × 16)-BIT DRAM

—TOP VIEW—

**INPUTS**

A0 - A8 : ADDRESS

 $\overline{\text{LCAS}}$: LOWER COLUMN ADDRESS STROBE $\overline{\text{OE}}$: DATA OUTPUT ENABLE $\overline{\text{RAS}}$: ROW ADDRESS STROBE $\overline{\text{UCAS}}$: UPPER COLUMN ADDRESS STROBE $\overline{\text{WE}}$: WRITE ENABLE**INPUTS/OUTPUTS**

I/O1 - I/O16 : DATA

OTHER

NC : NO CONNECTION

