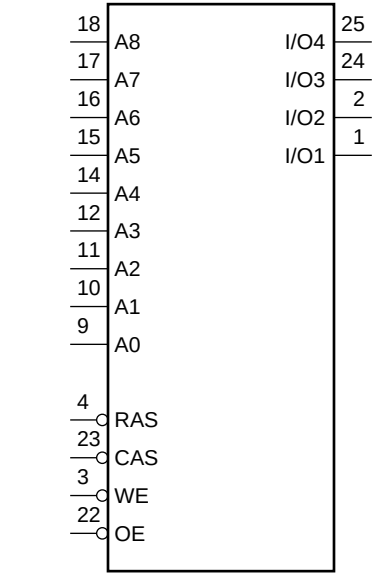
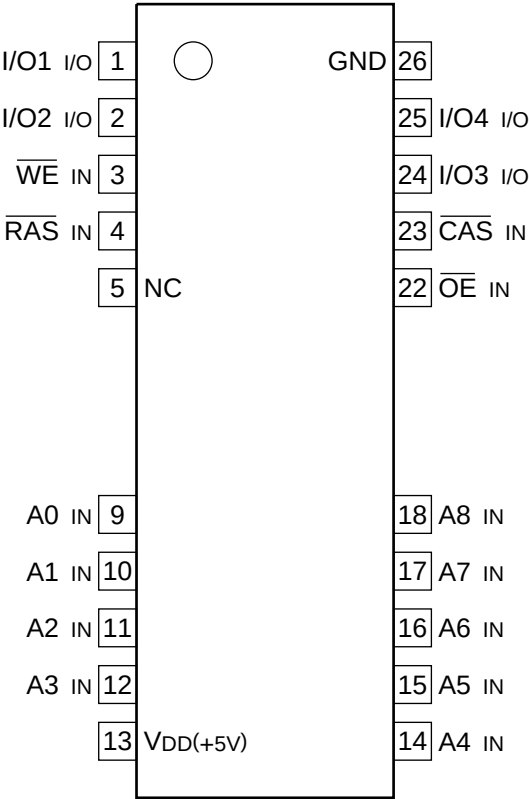


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C-MOS 1M (262,144 × 4)-BIT DYNAMIC RAM  
—TOP VIEW—



INPUT

A0 - A8 ; ADDRESS  
CAS ; COLUMN ADDRESS STROBE  
OE ; OUTPUT ENABLE  
RAS ; ROW ADDRESS STROBE  
WE ; WRITE ENABLE

INPUT/OUTPUT

I/O1 - I/O4 ; DATA

