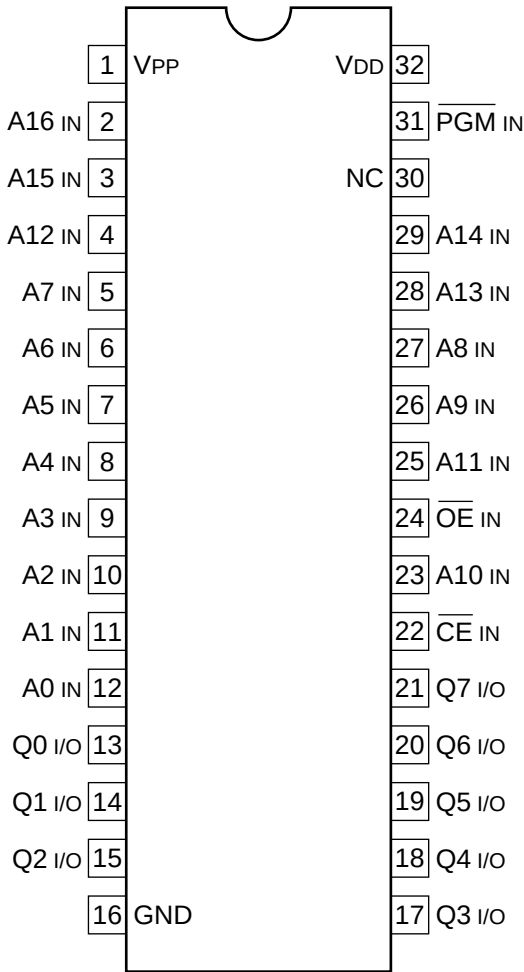


C-MOS 1M (128 K × 8)-BIT EPROM
—TOP VIEW—



INPUT

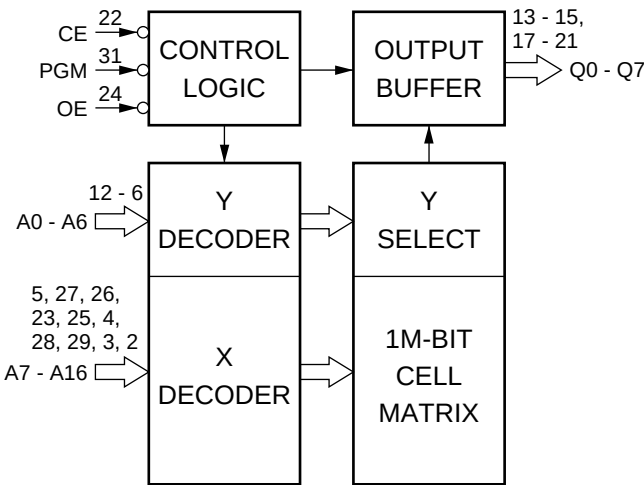
A0 - A16 ; ADDRESS
CE ; CHIP ENABLE
OE ; OUTPUT ENABLE
PGM ; PROGRAM ENABLE

OUTPUT

Q0 - Q7 ; DATA

OTHER

Vpp ; PROGRAM POWER SUPPLY



MODE \ TERMINAL	CE	OE	PGM	A0	A9	Vpp	OUTPUT
READ	0	0	x	x	x	+5 V	D OUT
OUTPUT DISABLE	0	1	x	x	x	+5 V	HI-Z
STANDBY (TTL)	1	x	x	x	x	+5 V	HI-Z
STANDBY (CMOS)	VDD±0.3 V	x	x	x	x	+5 V	HI-Z
PROGRAM	0	1	0	x	x	+12.5 V	D IN
PROGRAM VERIFY	0	0	1	x	x	+12.5 V	D OUT
PROGRAM INHIBIT	1	x	x	x	x	+12.5 V	HI-Z

0 : LOW LEVEL x : DON'T CARE
1 : HIGH LEVEL HI-Z : HIGH IMPEDANCE