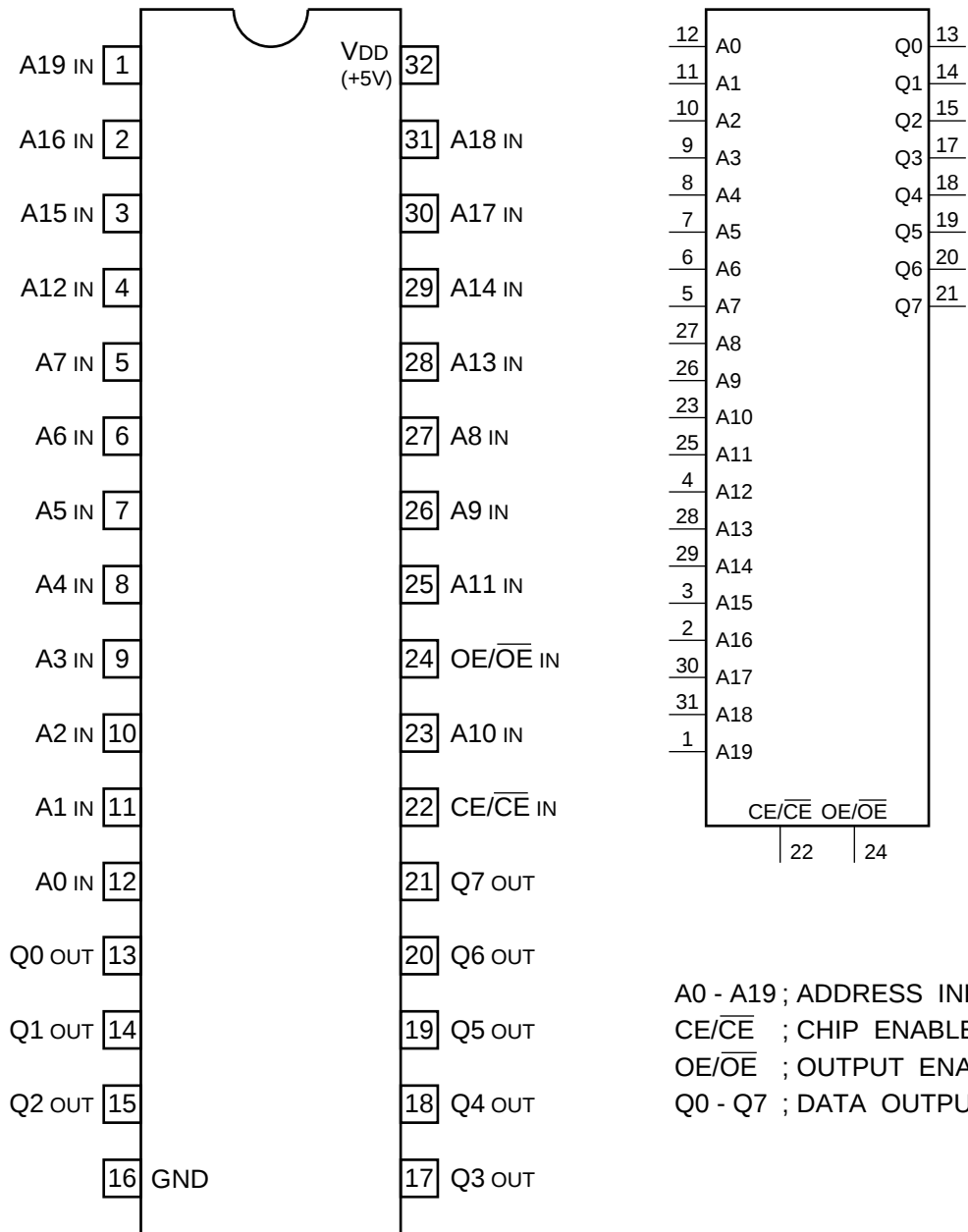

C-MOS 8M (1M·8) -BIT MASK ROM
— TOP VIEW —



A0 - A19 ; ADDRESS INPUTS
CE/ $\overline{\text{CE}}$; CHIP ENABLE INPUT
OE/ $\overline{\text{OE}}$; OUTPUT ENABLE INPUT
Q0 - Q7 ; DATA OUTPUTS

