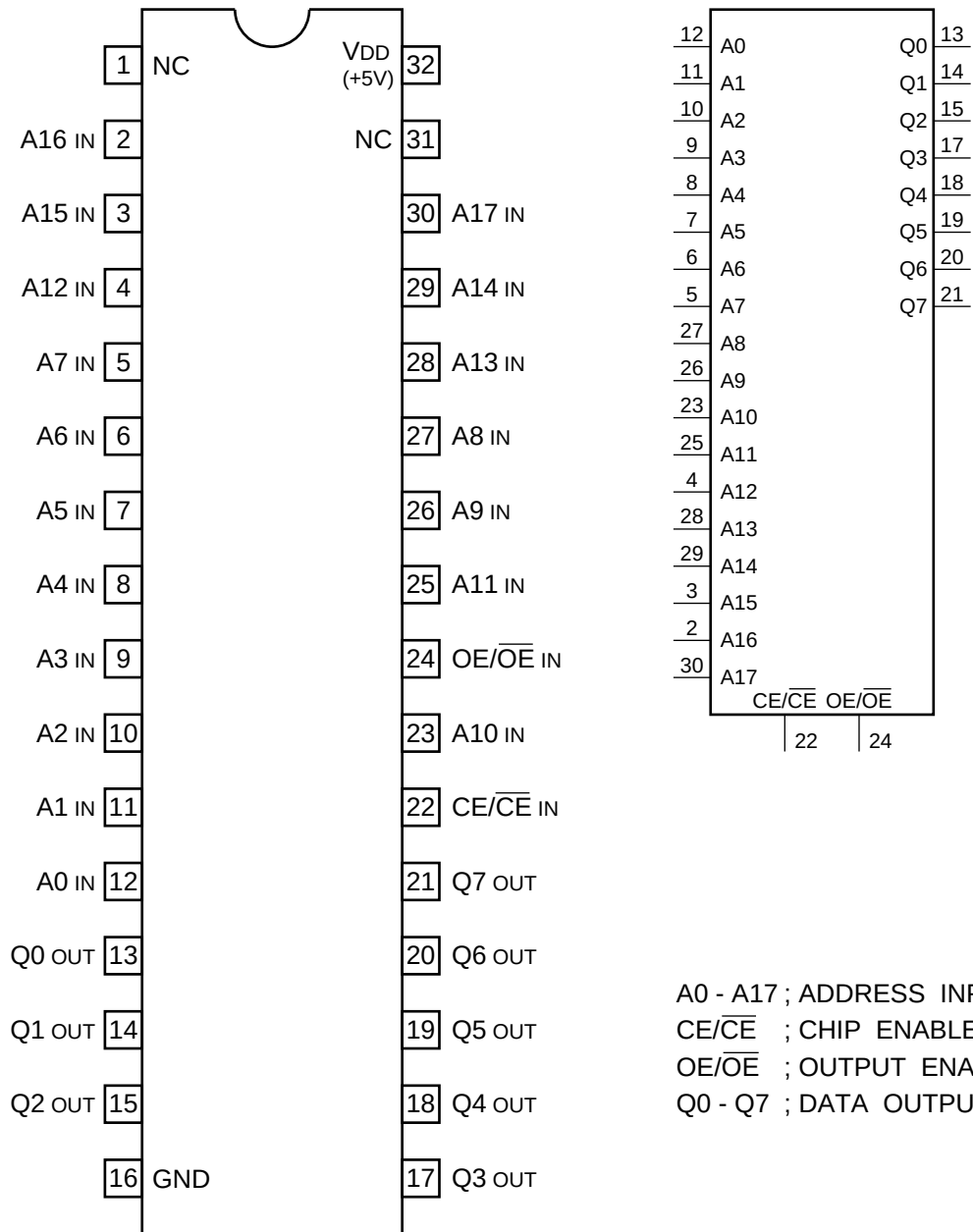

C-MOS 2M (256K·8) -BIT MASK ROM

— TOP VIEW —



A0 - A17 ; ADDRESS INPUTS

CE/CE ; CHIP ENABLE INPUT

OE/OE ; OUTPUT ENABLE INPUT

Q0 - Q7 ; DATA OUTPUTS

