

Preliminary Data Sheet Supplement

Subject:	MSP 34xxG Version History
Data Sheet Concerned:	All MSP 34xxG Data Sheets
Supplement:	No. 2/ 6251-525-2PDS
Edition:	Oct. 11, 2000

Version Changes within the MSP 34xxG Family:

For a detailed description of the below-mentioned items, see the corresponding data sheets. For quick reference, check the version history in the data sheet appendices.

	MSP 34x0G	A4	B4	B5	B6	B8
	MSP 34x1G		A1		A2	B8
	MSP 34x2G				A1	
	MSP 34x5G	A4		B5	B6	B8
	MSP 34x7G				B6	B8
technology		0.8 μ	0.5 μ	0.5 μ	0.5 μ	0.45 μ
power dissipation (typical) at 8 V operation	MSP 34x0/x1/x5/x7 MSP 34x2	740 mW	640 mW	640 mW	640 mW 690 mW	600 mW
digital input specification change				x	x	x
specification of max. analog high voltage (AHVSUP)		8.4 V	8.4 V	8.7 V	8.7 V	8.7 V
programmable A2 and carrier mute thresholds				x	x	x
new Standard Select Mode 0D _{hex} : D/K-NICAM together with HDEV3 FM mode				x	x	x
additional preference "color" for 4.5 MHz carrier in Automatic Standard Detection				x	x	x
improved AM-performance (better SNR and THD)					x	x
new Standard Select Mode 07 _{hex} : D/K3 for Poland					x	x
faster system D/K loop for stereo detection (standards 4, 5, 7, B with ASS = on)					x	x
improved I ² C hardware problem handling					x	x
extended features in the CONTROL register (readout hardware / reset status)					x	x
Micronas Dynamic Bass (MDB)	MSP 34x0/x1/x2				x	x
Micronas Dynamic Bass (improved MDB)	MSP 34x0/x1/x2					x
faster identification for all standards, major speedup of identification for EIA-J standard						x
faster carrier mute						x
J17 deemphasis						x