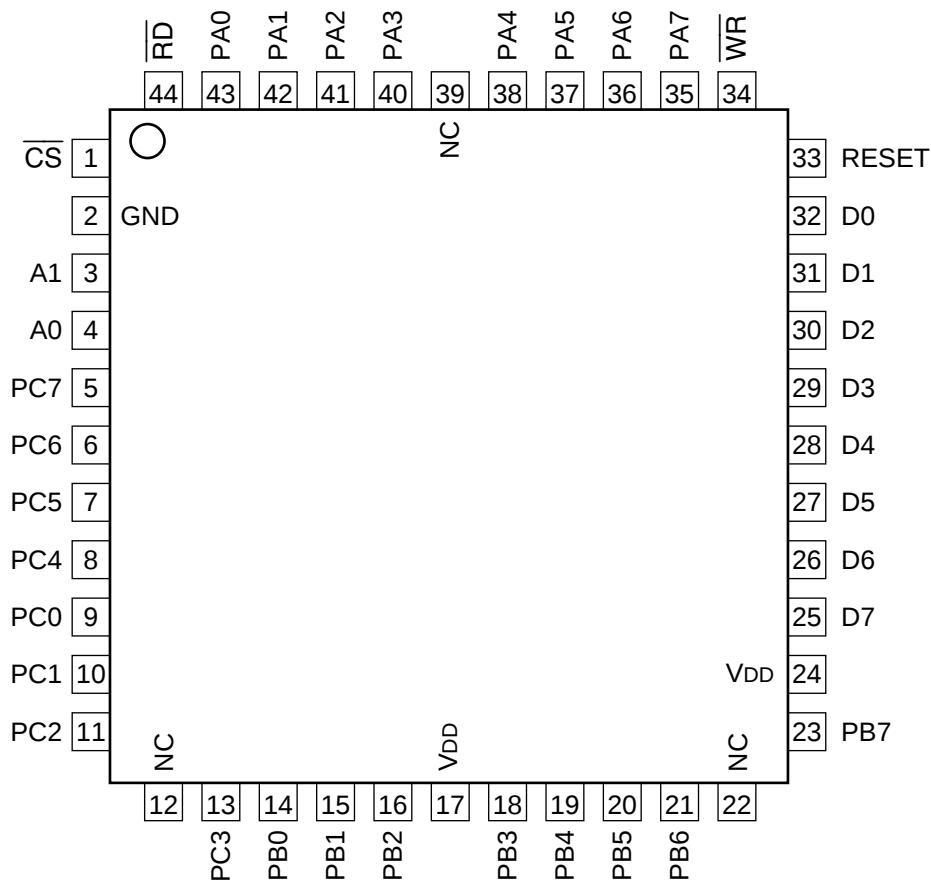


C-MOS PROGRAMMABLE PERIPHERAL INTERFACE

—TOP VIEW—



A1	A0	$\overline{CS}$	$\overline{WR}$	$\overline{RD}$	OPERATION
0	0	0	1	0	PORT A → DATA BUS
0	1	0	1	0	PORT B → DATA BUS
1	0	0	1	0	PORT C → DATA BUS
1	1	0	1	0	NO OPERATION
0	0	0	0	1	DATA BUS → PORT A
0	1	0	0	1	DATA BUS → PORT B
1	0	0	0	1	DATA BUS → PORT C
1	1	0	0	1	DATA BUS → CONTROL REGISTER
X	X	1	X	X	HIGH IMPEDANCE

0 : LOW LEVEL
 1 : HIGH LEVEL
 X : DON'T CARE

A0, A1 : PORT SELECT ADDRESS
 $\overline{CS}$: CHIP SELECT
 D0 - 7 : DATA BUS
 PA0 - 7 : PORT A IN/OUT
 PB0 - 7 : PORT B IN/OUT
 PC0 - 7 : PORT C IN/OUT
 $\overline{RD}$: READ
 $\overline{WR}$: WRITE

