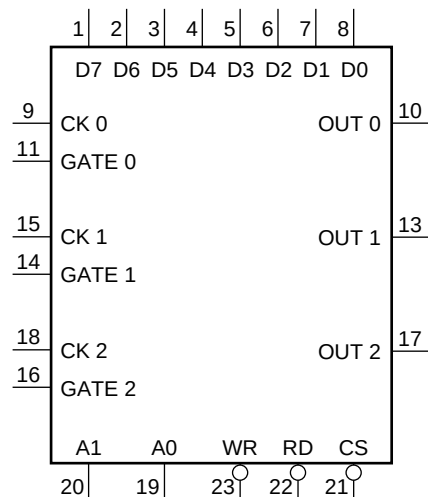
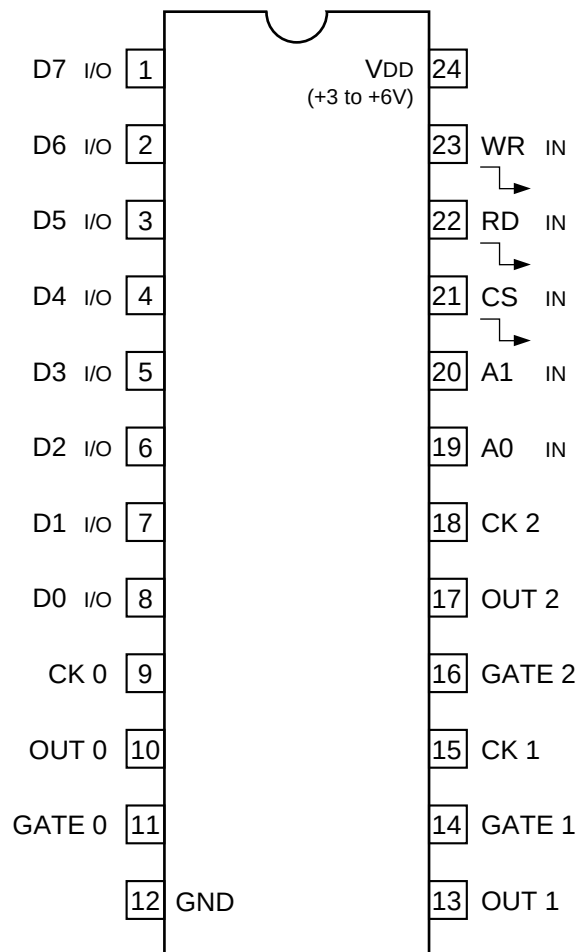

C-MOS PROGRAMABLE INTERVAL TIMER

—TOP VIEW—

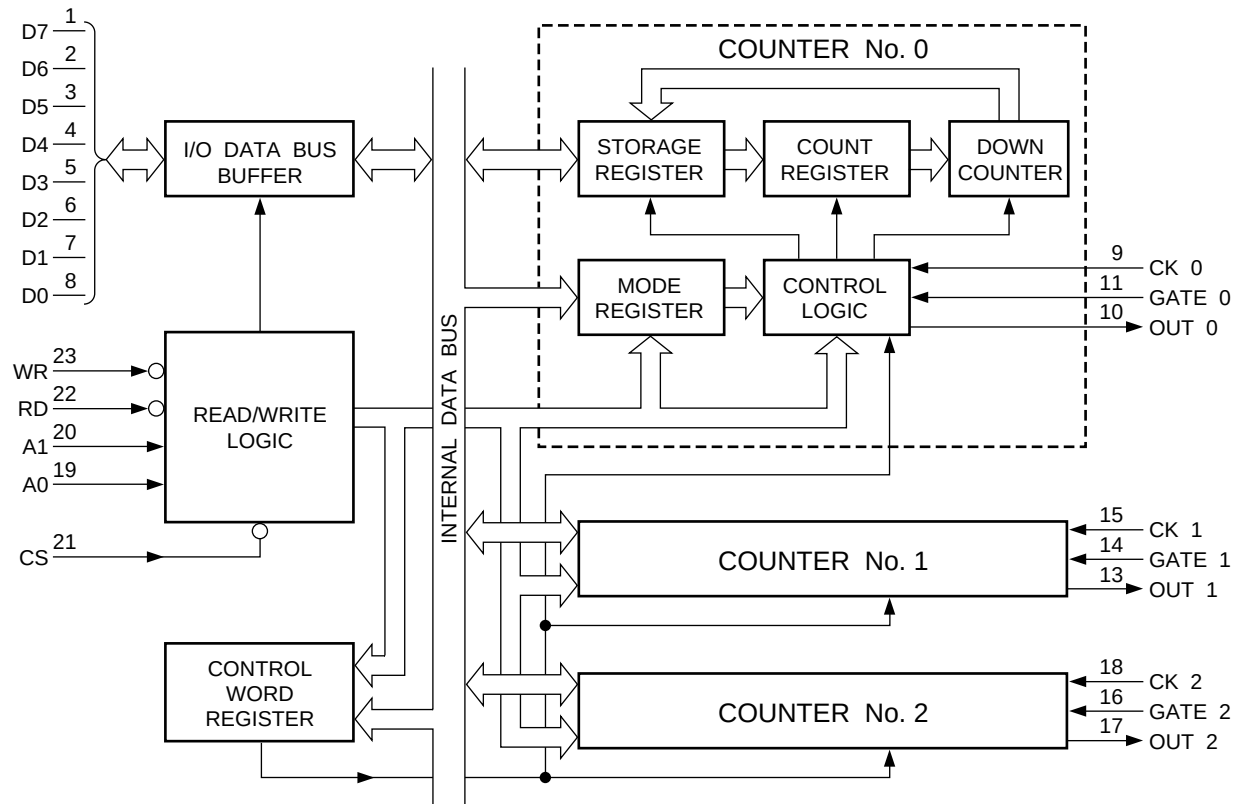


D7 - D0 ; 8-BIT DATA I/O
 CK N ; COUNTER CLOCK
 GATE N ; COUNTER GATE
 OUT N ; COUNTER OUTPUT
 RD ; READ COUNTER
 WR ; WRITE CMD OR DATA
 CS ; CHIP SELECT
 A1, A0 ; COUNTER SELECT

FUNCTION TABLE

INPUTS					FUNCTION
CS	RD	WR	A1	A0	
0	1	0	0	0	Load Counter No. 0
0	1	0	0	1	Load Counter No. 1
0	1	0	1	0	Load Counter No. 2
0	1	0	1	1	Control Word
0	0	1	0	0	Read Counter 0
0	0	1	0	1	Read Counter 1
0	0	1	1	0	Read Counter 2
0	0	1	1	1	No-Operation (HI-Z)
1	X	X	X	X	Disable (HI-Z)
0	1	1	X	X	No-Operation (HI-Z)

0 ; LOW LEVEL
 1 ; HIGH LEVEL
 X ; DON'T CARE
 HI-Z ; HIGH IMPEDANCE



CONTROL WORD FORMAT

D7	D6	D5	D4	D3	D2	D1	D0
SC1	SC0	RL1	RL0	M2	M1	M0	BCD

BCD	OPERATION
0	16-BIT BINARY
1	BCD (4-DECADE)

M2	M1	M0	MODE
0	0	0	0
0	0	1	1
X	1	0	2
X	1	1	3
1	0	0	4
1	0	1	5

RL1	RL0	OPERATION
0	0	COUNTER LATCHING
0	1	READ/LOAD LSB ONLY
1	0	READ/LOAD MSB ONLY
1	1	LSB FIRST THEN MSB

SC1	SC0	SELECTED COUNTER
0	0	COUNTER No. 0
0	1	COUNTER No. 1
1	0	COUNTER No. 2
1	1	ILLEGAL