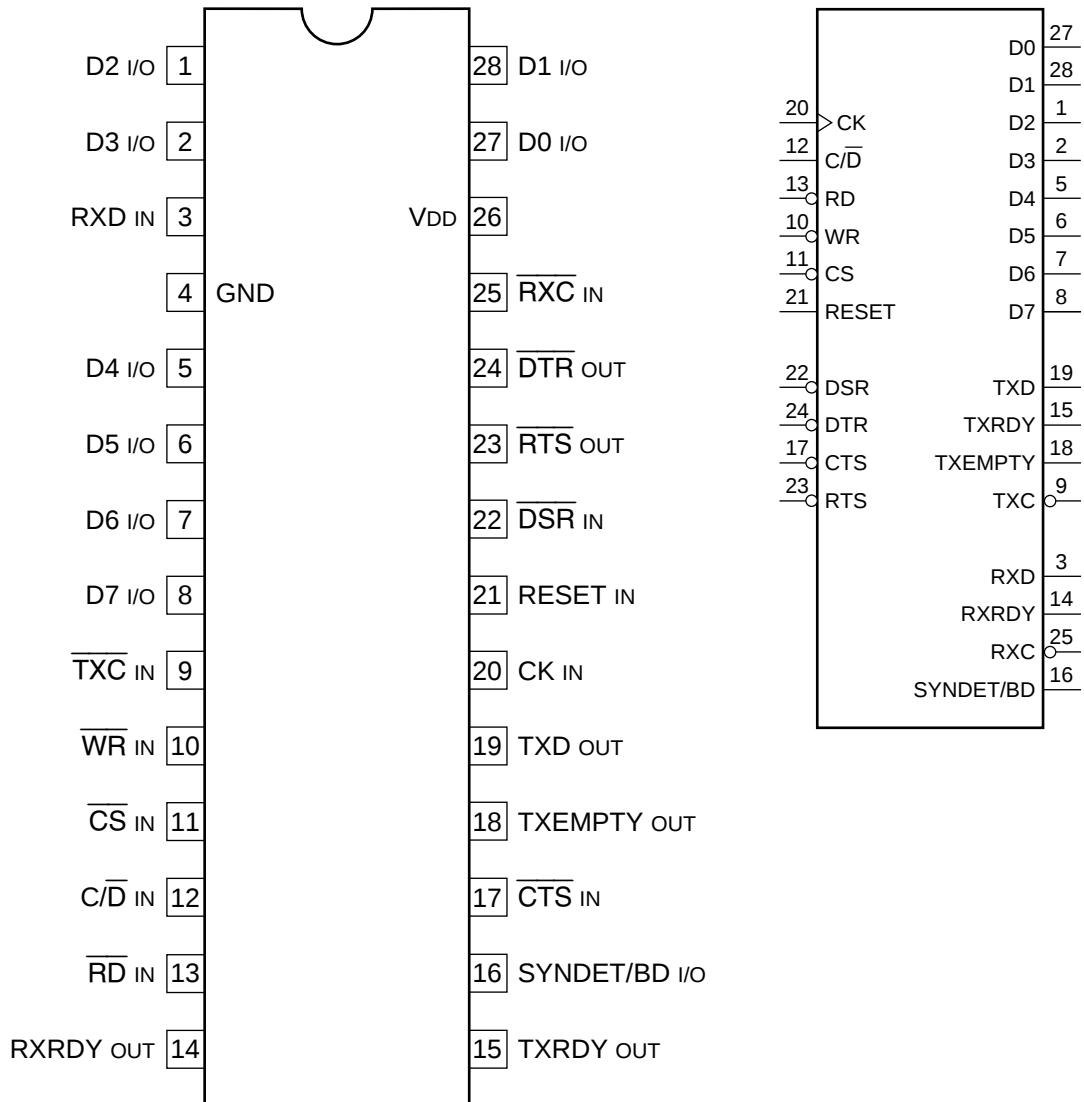


C-MOS PROGRAMMABLE COMMUNICATION INTERFACE

—TOP VIEW—



OPERATION WITH CPU

$\overline{\text{CS}}$	$\text{C}/\overline{\text{D}}$	$\overline{\text{RD}}$	$\overline{\text{WR}}$	FUNCTION
1	x	x	x	DATA BUS 3-STATE
0	x	1	1	DATA BUS 3-STATE
0	1	0	1	STATUS TO CPU
0	1	1	0	CONTROL WORD FROM CPU
0	0	0	1	DATA TO CPU
0	0	1	0	DATA FROM CPU

0 : LOW LEVEL

1 : HIGH LEVEL

x : DON'T CARE

HI-Z : HIGH IMPEDANCE

INPUT

CK : CLOCK SIGNAL
CS : CHIP ENABLE
CTS : CLEAR TO SEND DATA
C/D : DATA, COMMAND WORD OR STATUS WORD
IS TO BE WRITTEN OR READ
DSR : DATA SET READY
RD : READ DATA OR STATUS WORD
RESET : RESET BY HIGH LEVEL
RXC : RECEIVING CLOCK
RXD : RECEIVING DATA
TXC : TRANSMITTING CLOCK
WR : WRITE DATA OR CONTROL WORD

OUTPUT

DTR : DATA TERMINAL READY
RTS : REQUEST TO SEND DATA
RXRDY : RECEIVING READY
TXD : TRANSMITTING DATA
TXEMPTY : TRANSMITTING CHARACTER EMPTY
TXRDY : TRANSMITTING READY

INPUT/OUTPUT

D0 - D7 : DATA
SYNDET/BD : SYNC DETECT/BREAK DETECT

