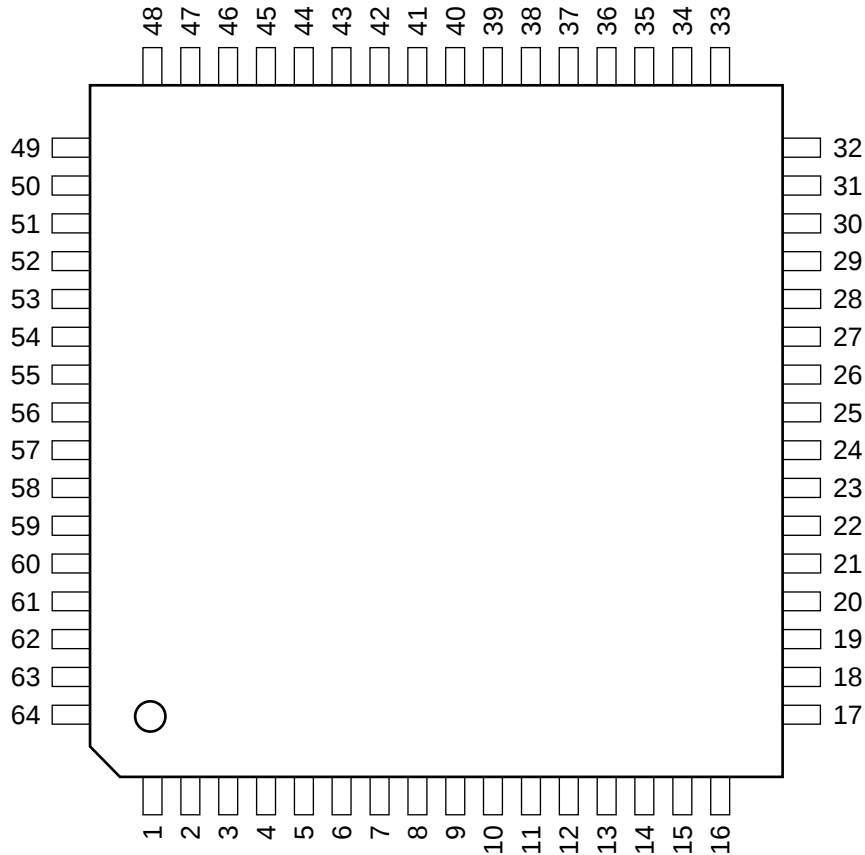


DIGITAL VIDEO NTSC/PAL DECODER

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	CD0	17	—	Vcc	33	O	Y7	49	—	Vcc
2	I	CD1	18	—	GND	34	O	Y6	50	—	GND
3	I	CD2	19	I	SCL	35	O	Y5	51	O	ODD
4	I	CD3	20	I/O	SDA	36	O	Y4	52	O	VVALID
5	I	CD4	21	I	MODE0	37	O	Y3	53	O	HVALID
6	I	CD5	22	I	MODE1	38	O	Y2	54	O	VSYNC L
7	I	CD6	23	I	MODE2	39	O	Y1	55	O	HSYNC L
8	I	CD7	24	I	MODE3	40	O	Y0	56	O	CLKXO
9	I	CVBS0	25	I	RESET L	41	O	C7	57	O	SYSEL
10	I	CVBS1	26	I	PLLSEL	42	O	C6	58	O	CLKX2O
11	I	CVBS2	27	I	CLKSEL	43	O	C5	59	O	VCO CP
12	I	CVBS3	28	I	TEST1	44	O	C4	60	I/O	SYNC
13	I	CVBS4	29	I	SLEEP	45	O	C3	61	O	HSY
14	I	CVBS5	30	I	TE	46	O	C2	62	I	CLKX2
15	I	CVBS6	31	—	GND	47	O	C1	63	—	GND
16	I	CVBS7	32	—	Vcc	48	O	C0	64	—	Vcc

INPUTS

CD0 - CD7	: CHROMINANCE SIGNAL
CLKSEL	: CLOCK SELECT
CLKX2	: CLOCK
CVBS0 - CVBS7	: COMPOSITE SIGNAL
MODE0 - MODE3	: MODE SELECT
PLLSEL	: (NOT IN USE)
RESET L	: SYSTEM RESET (ACTIVE L)
SCL	: I ² C BUS CLOCK
SLEEP	: SLEEP MODE
TE	: TEST
TEST1	: TEST

OUTPUTS

C0 - C7	: CHROMA SIGNAL
CLKX2O	: CLOCK
CLKXO	: INTERNAL CLOCK PULSE
HSY	: A/D CONVERTER CLAMP TIMING SIGNAL
HSYNC L	: H SYNC
HVALID	: H EFFECTIVE PICTURE ELEMENT TIMING
ODD	: FIELD DISPLAY
SYSSEL	: SYSTEM SELECT
VCO CP	: (NOT IN USE)
VSYSN L	: V SYNC
VVALID	: V EFFECTIVE LINES TIMING
Y0 - Y7	: COLOR DIFFERENCE SIGNAL

INPUTS/OUTPUTS

SDA	: I ² C BUS DATA
SYNC	: COMPOSITE SYNC SIGNAL

