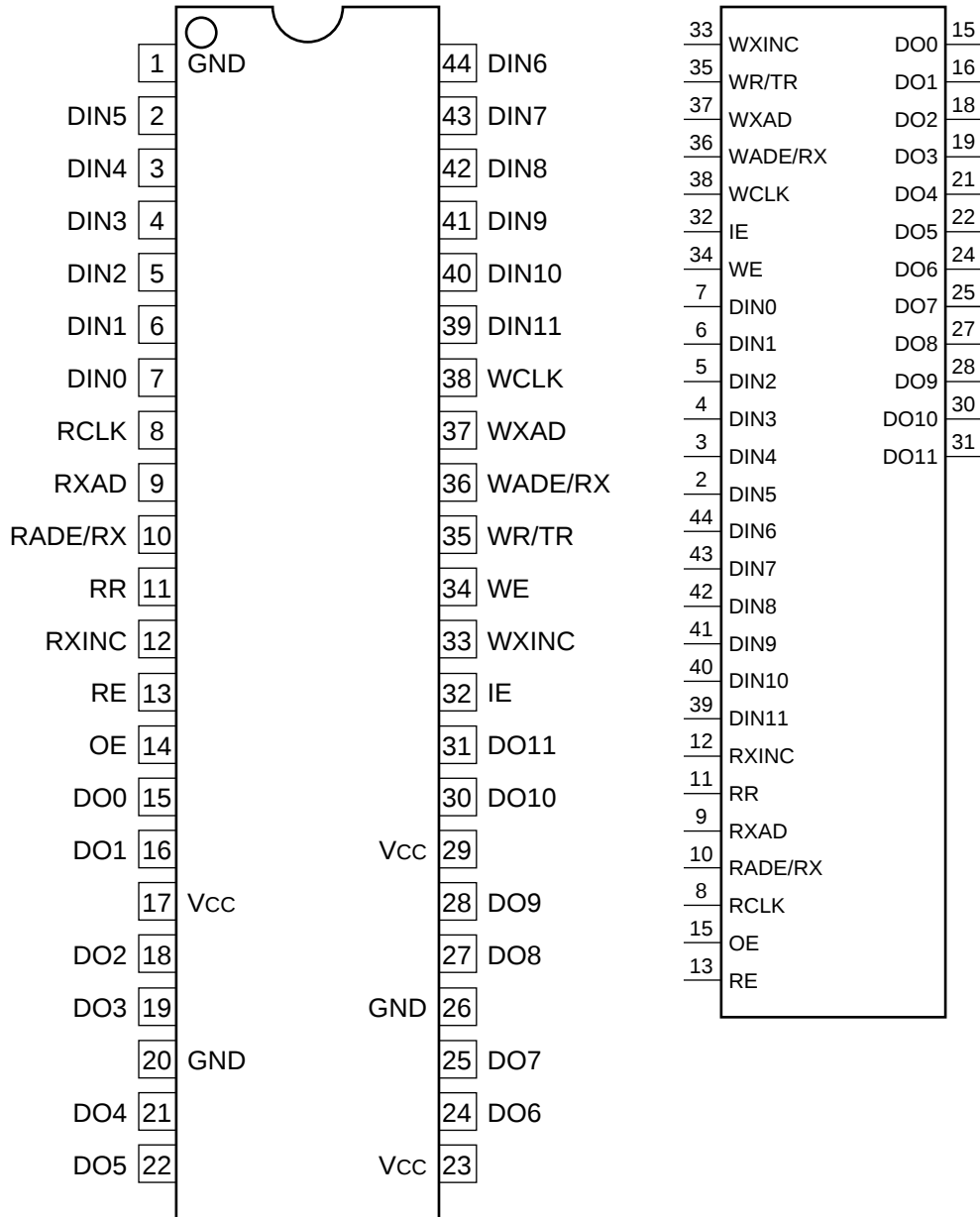


C-MOS 2 M (222,720 × 12)-BIT FIELD MEMORY

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	GND	12	I	RXINC	23	—	Vcc	34	I	WE
2	I	DIN5	13	I	RE	24	O	DO6	35	I	WR/TR
3	I	DIN4	14	I	OE	25	O	DO7	36	I	WADE/RX
4	I	DIN3	15	O	DO0	26	—	GND	37	I	WXAD
5	I	DIN2	16	O	DO1	27	O	DO8	38	I	WCLK
6	I	DIN1	17	—	Vcc	28	O	DO9	39	I	DIN11
7	I	DIN0	18	O	DO2	29	—	Vcc	40	I	DIN10
8	I	RCLK	19	O	DO3	30	O	DO10	41	I	DIN9
9	I	RXAD	20	—	GND	31	O	DO11	42	I	DIN8
10	I	RADE/RX	21	O	DO4	32	I	IE	43	I	DIN7
11	I	RR	22	O	DO5	33	I	WXINC	44	I	DIN6

INPUT

DIN0 - DIN11 : DATA
IE : INPUT ENABLE
OE : OUTPUT ENABLE
RADE/RX : ADDRESS INPUT ENABLE/ADDRESS RESET
RCLK : SERIAL READ CLOCK
RE : READ ENABLE
RX : ADDRESS RESET
RR : ADDRESS RESET MODE ENABLE
RXAD : SERIAL ADDRESS DATA
RXINC : ADDRESS INCREMENT
WADE/RX : ADDRESS INPUT ENABLE/ADDRESS RESET
WCLK : SERIAL WRITE CLOCK
WE : WRITE ENABLE
WR/TR : WRITE DATA TRANSFER
WXAD : SERIAL ADDRESS DATA
WXINC : ADDRESS INCREMENT

OUTPUT

DO0 - DO11 : DATA

